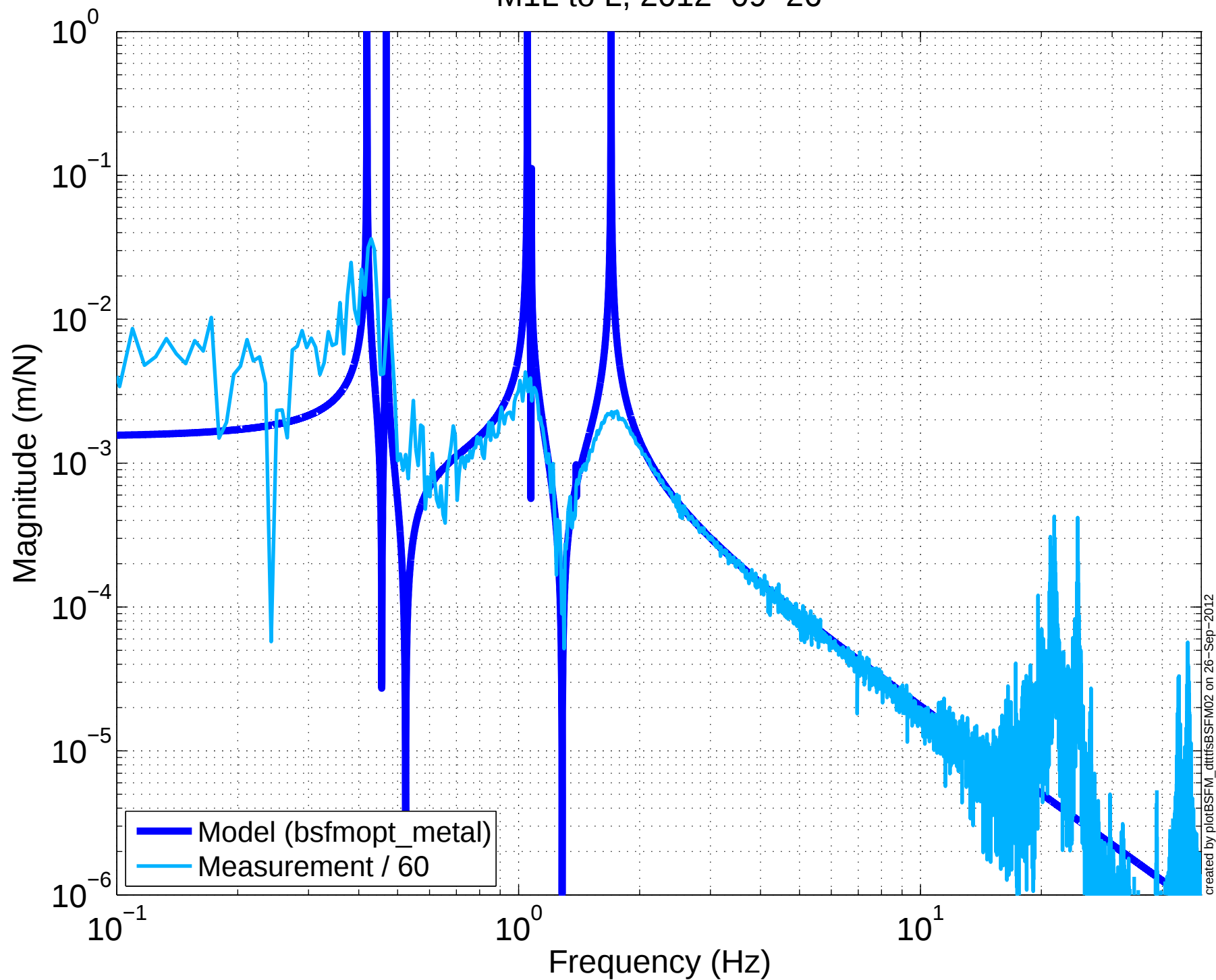


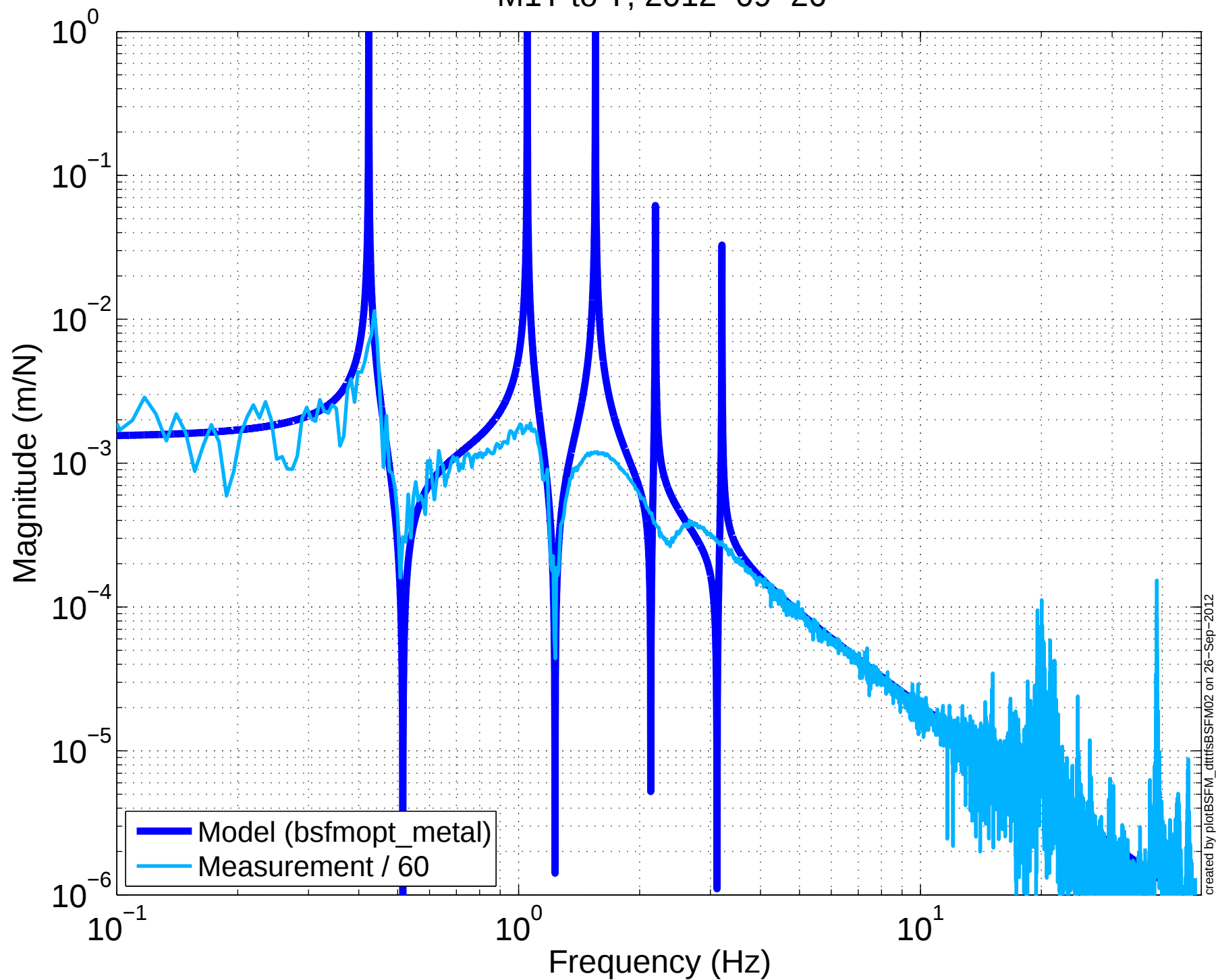
X1SUSBSFM02 BUILD01

M1L to L, 2012-09-26



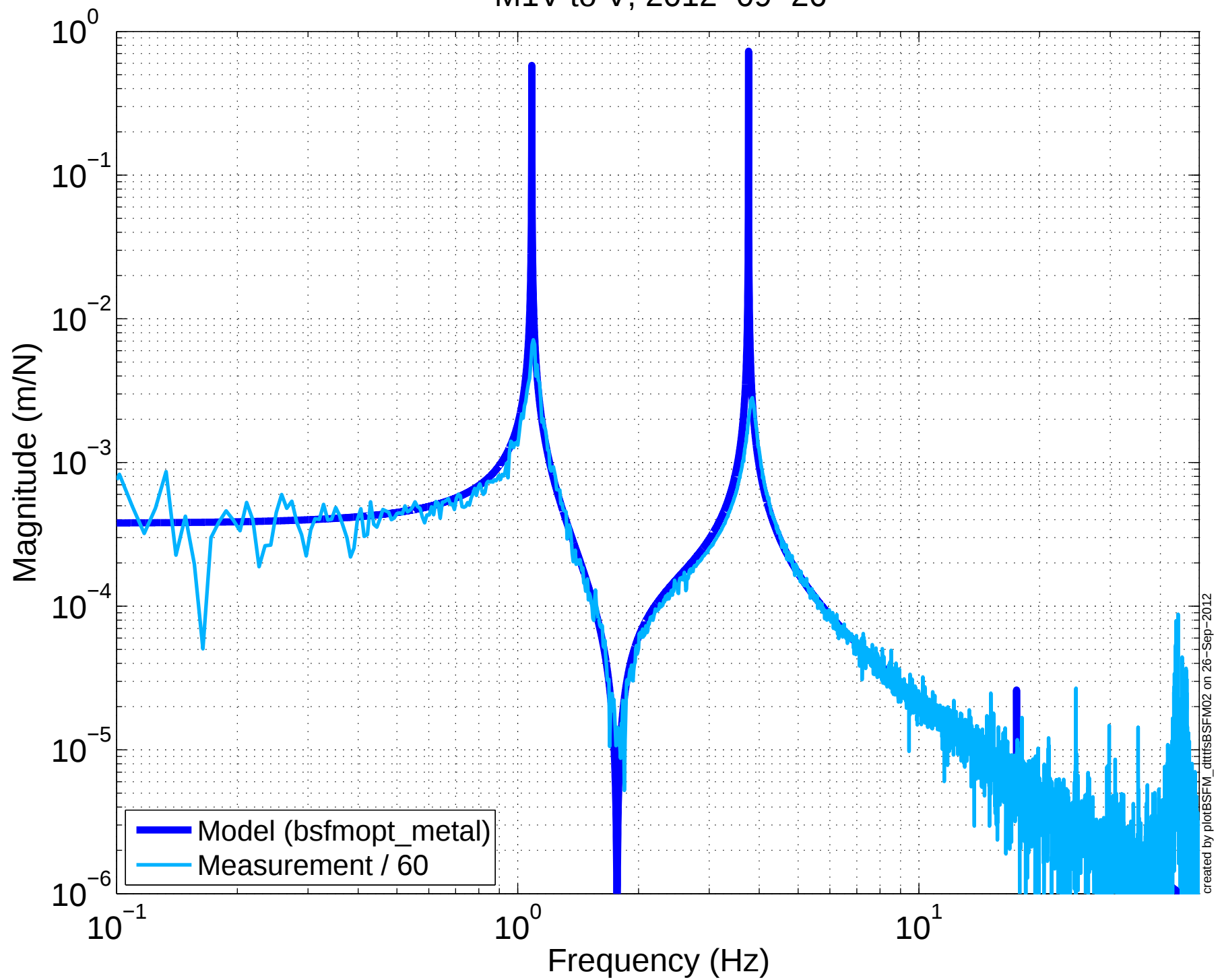
X1SUSBSFM02 BUILD01

M1T to T, 2012-09-26

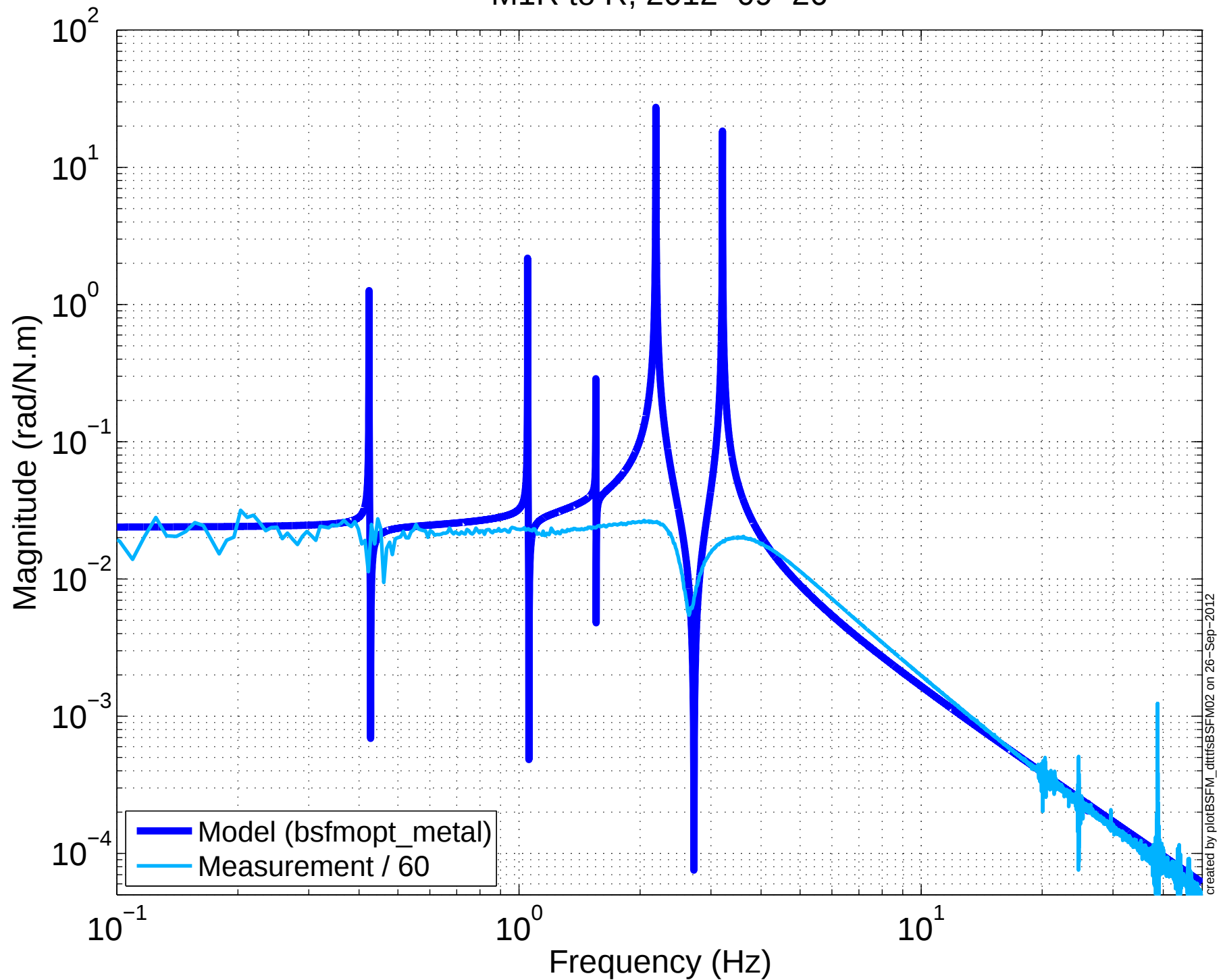


X1SUSBSFM02 BUILD01

M1V to V, 2012-09-26

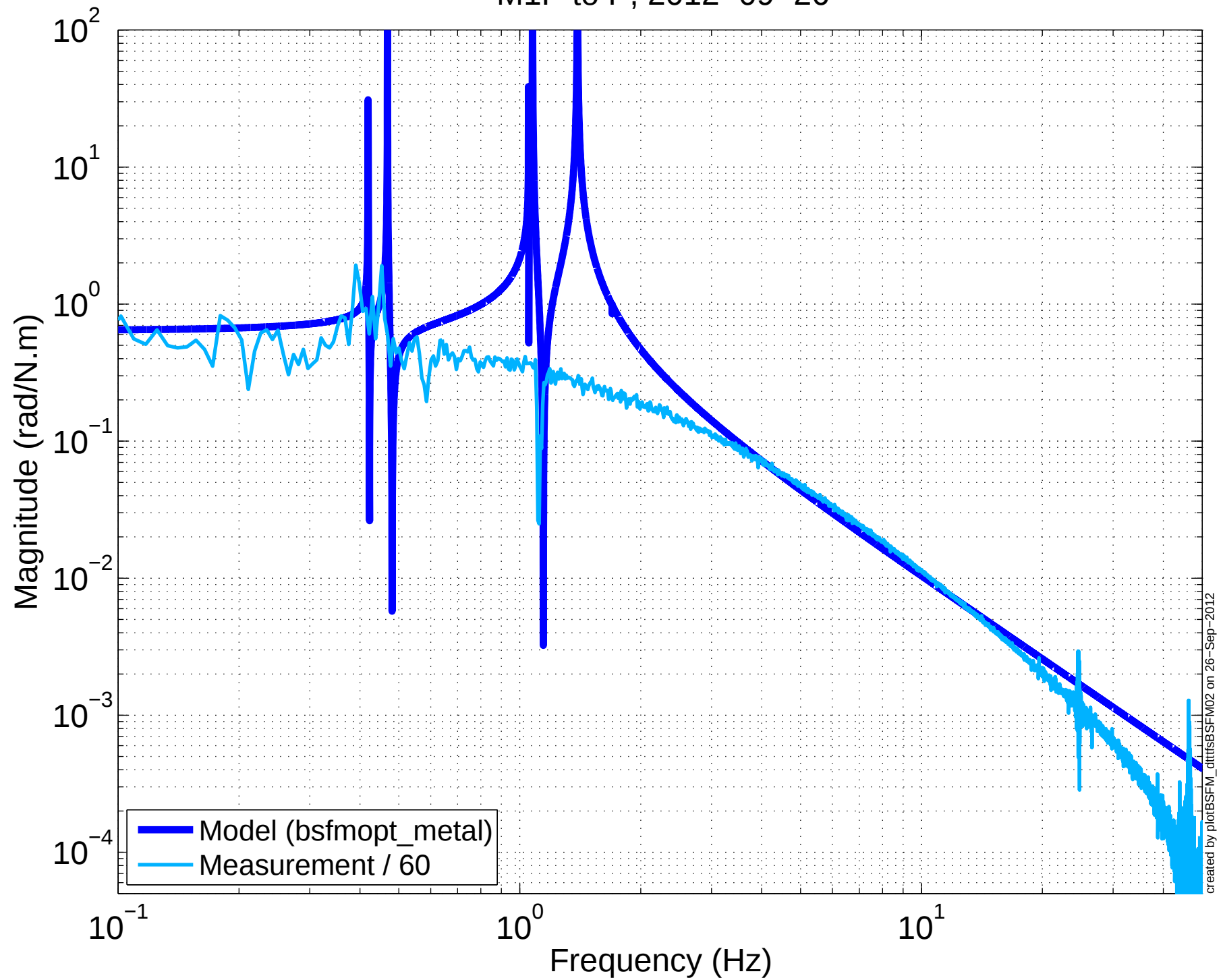


X1SUSBSFM02 BUILD01
M1R to R, 2012-09-26



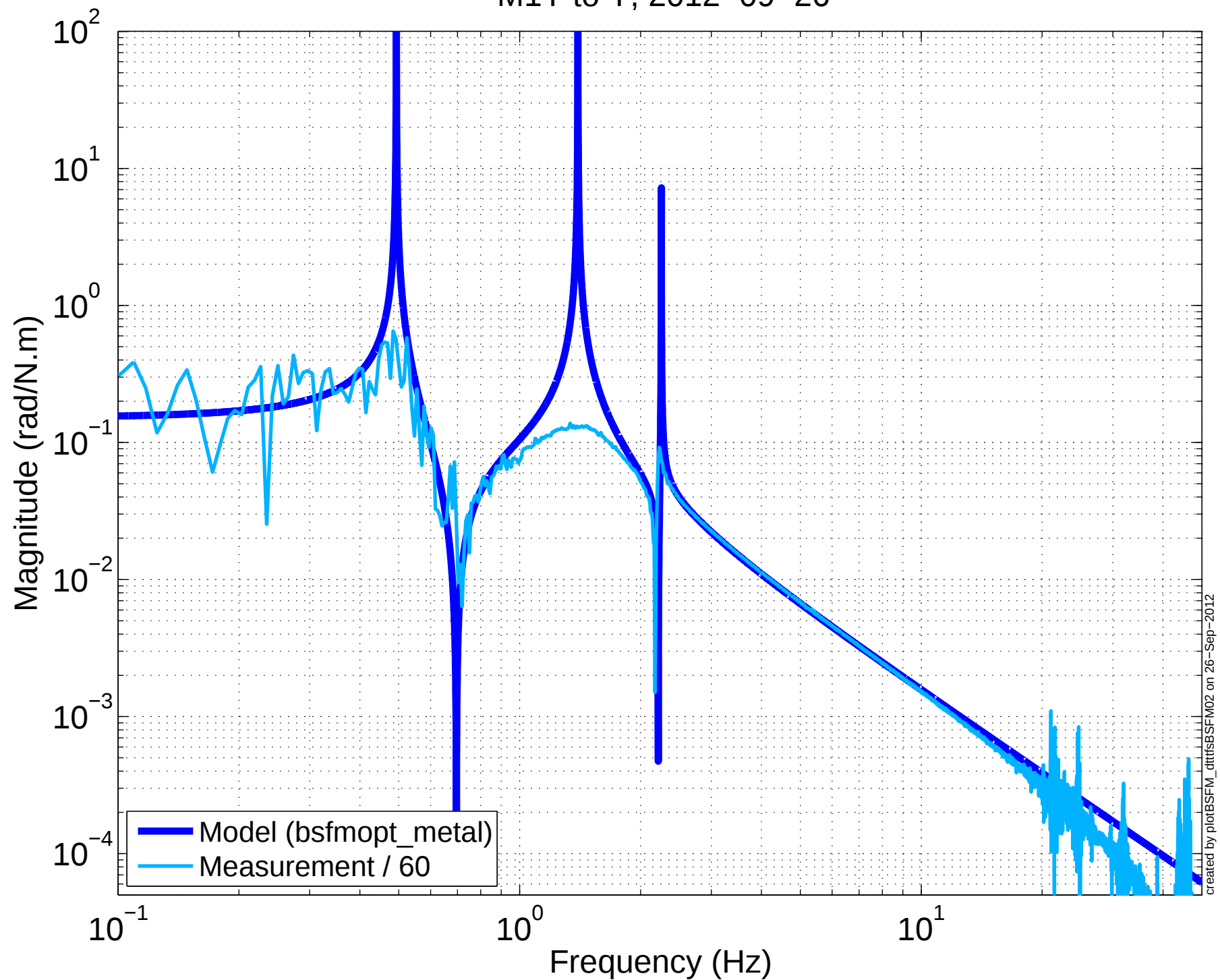
X1SUSBSFM02 BUILD01

M1P to P, 2012-09-26



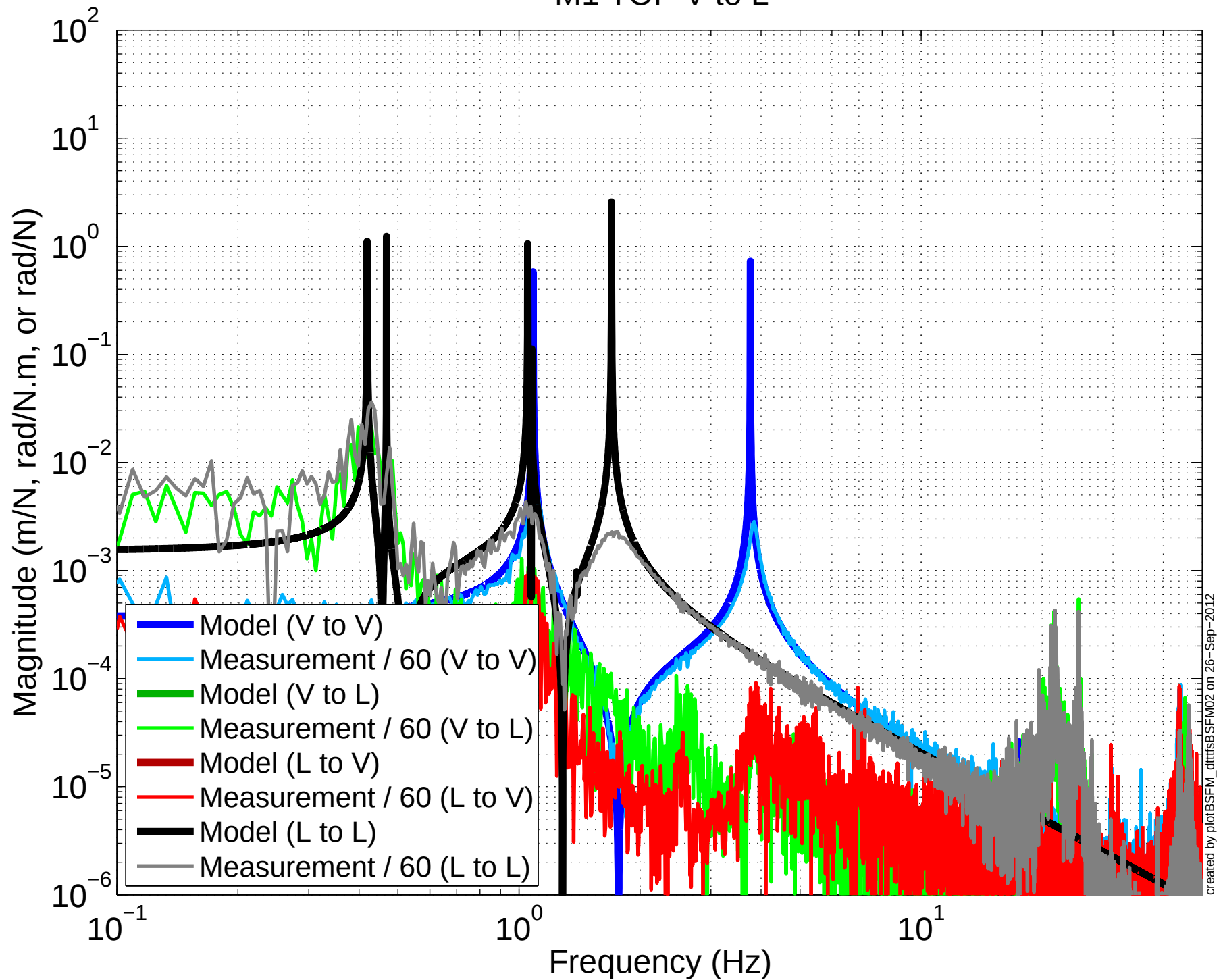
X1SUSBSFM02 BUILD01

M1Y to Y, 2012-09-26



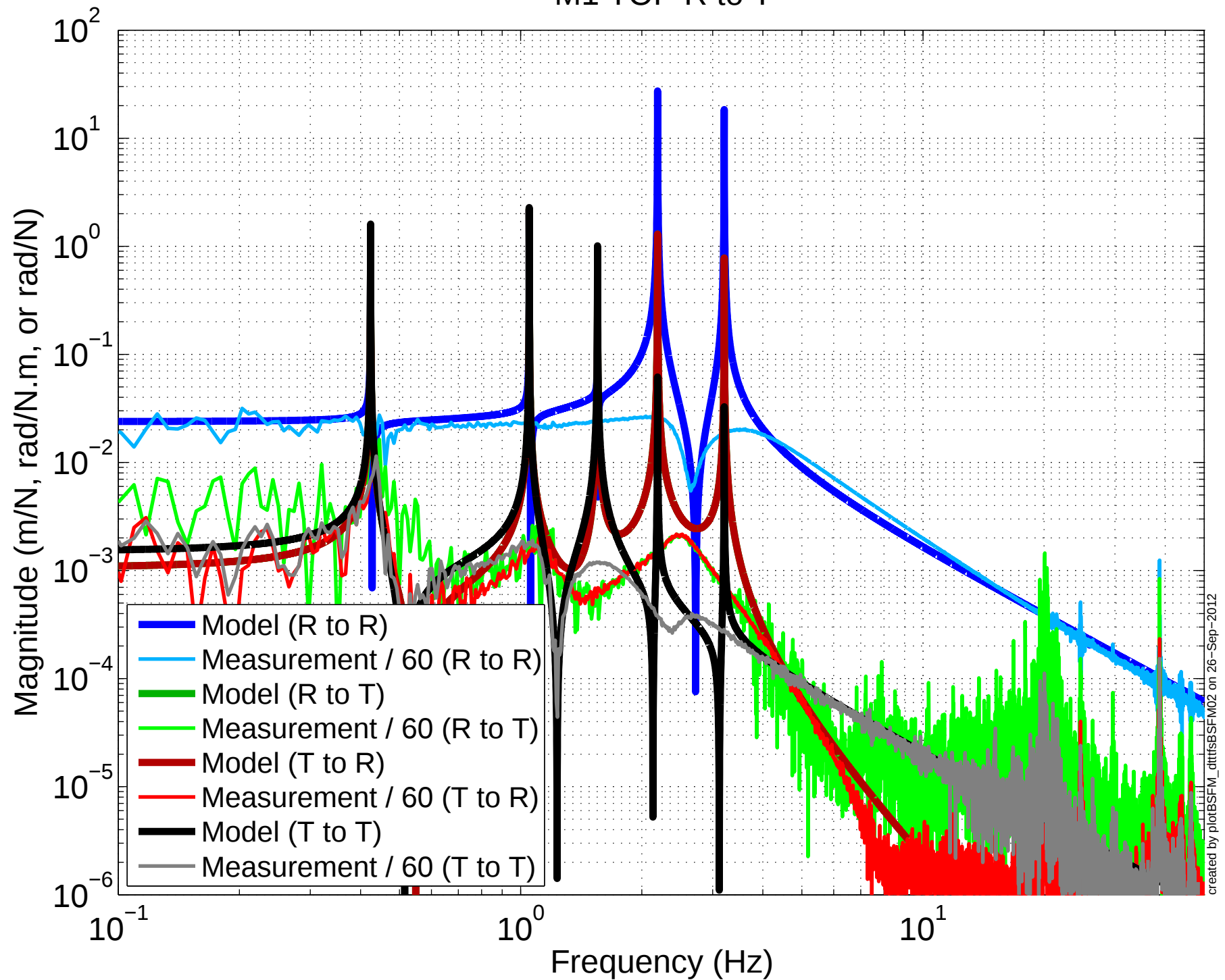
X1SUBSFM02 BUILD01

M1 TOP V to L



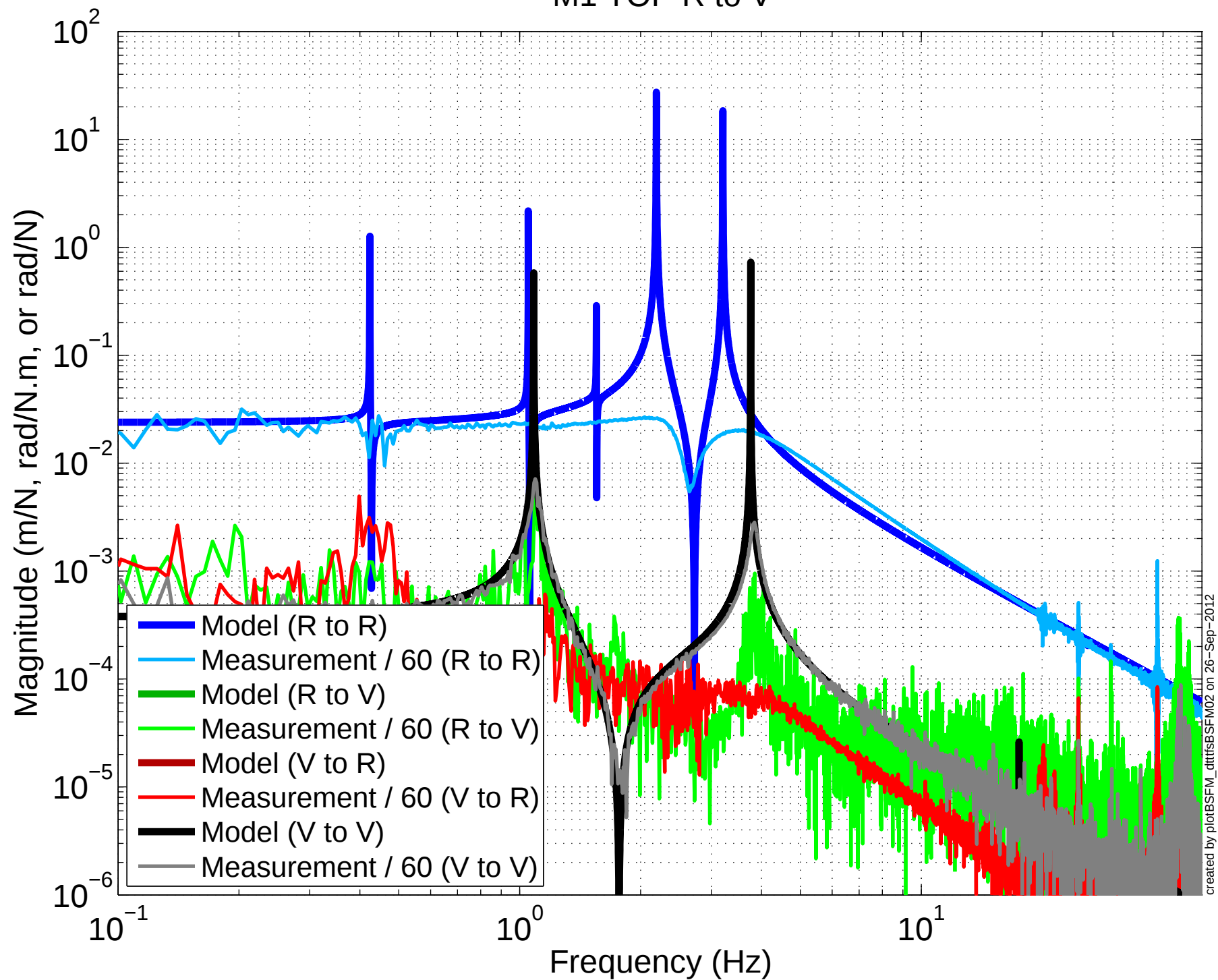
X1SUSBSFM02 BUILD01

M1 TOP R to T



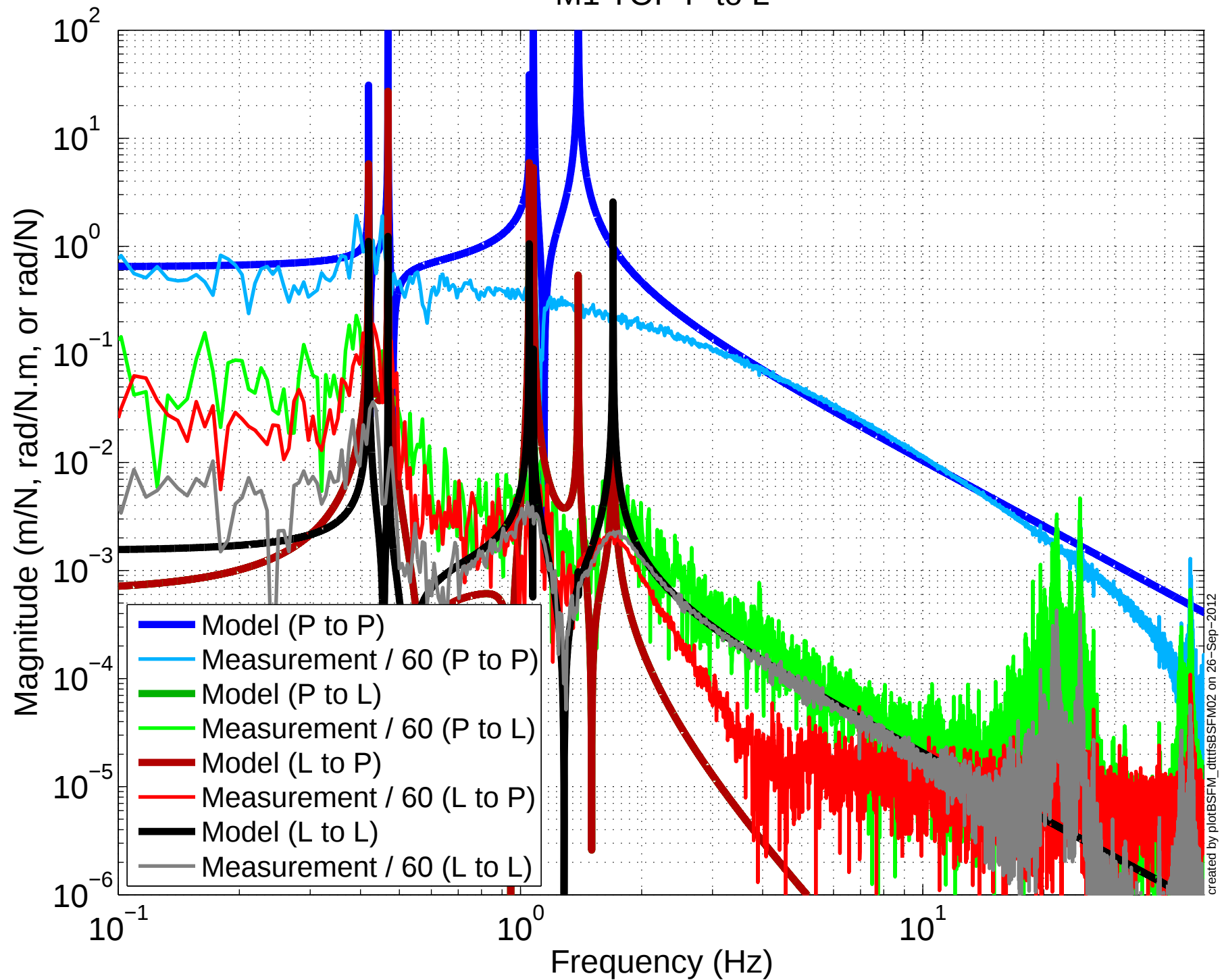
X1SUBSFM02 BUILD01

M1 TOP R to V



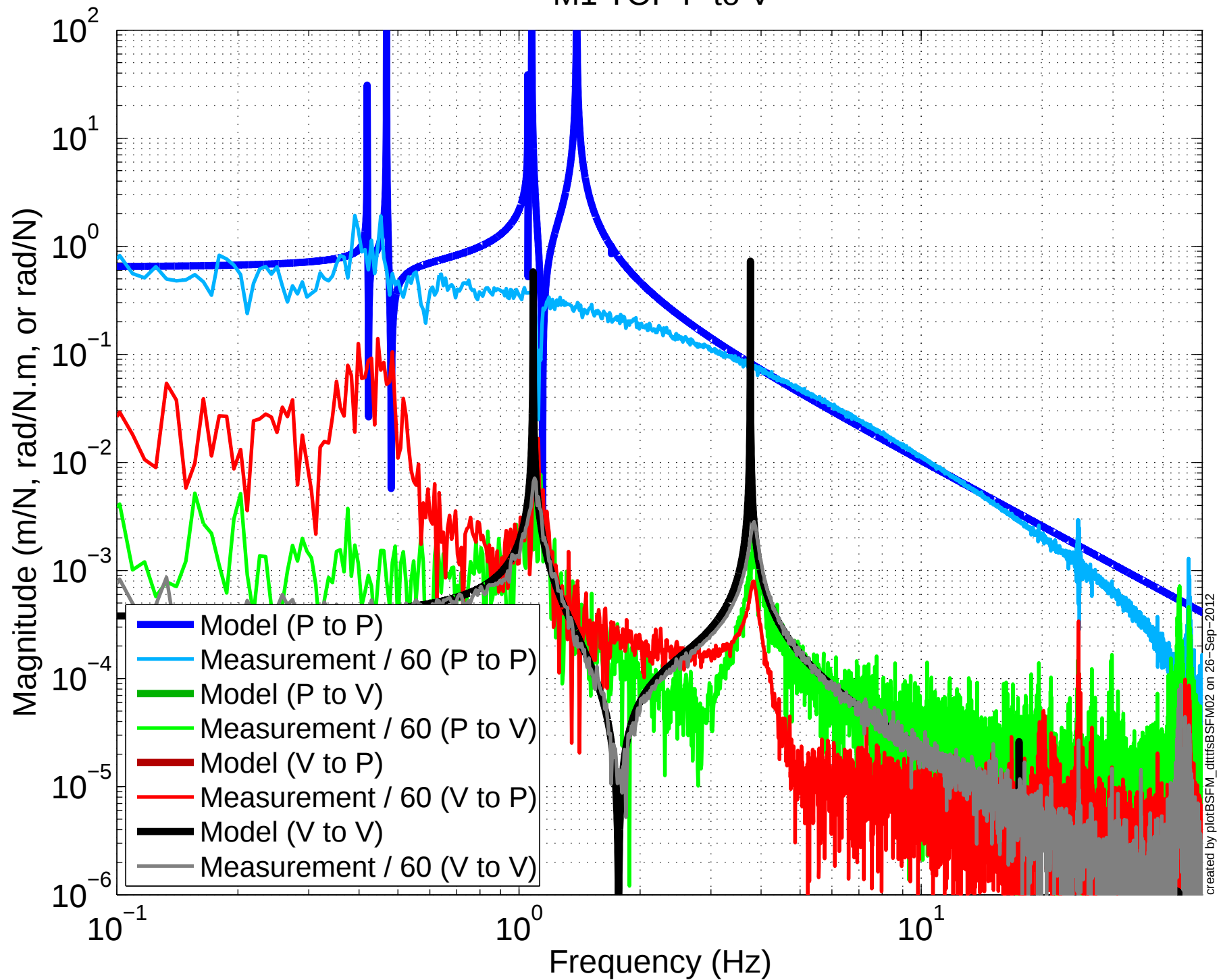
X1SUBSFM02 BUILD01

M1 TOP P to L



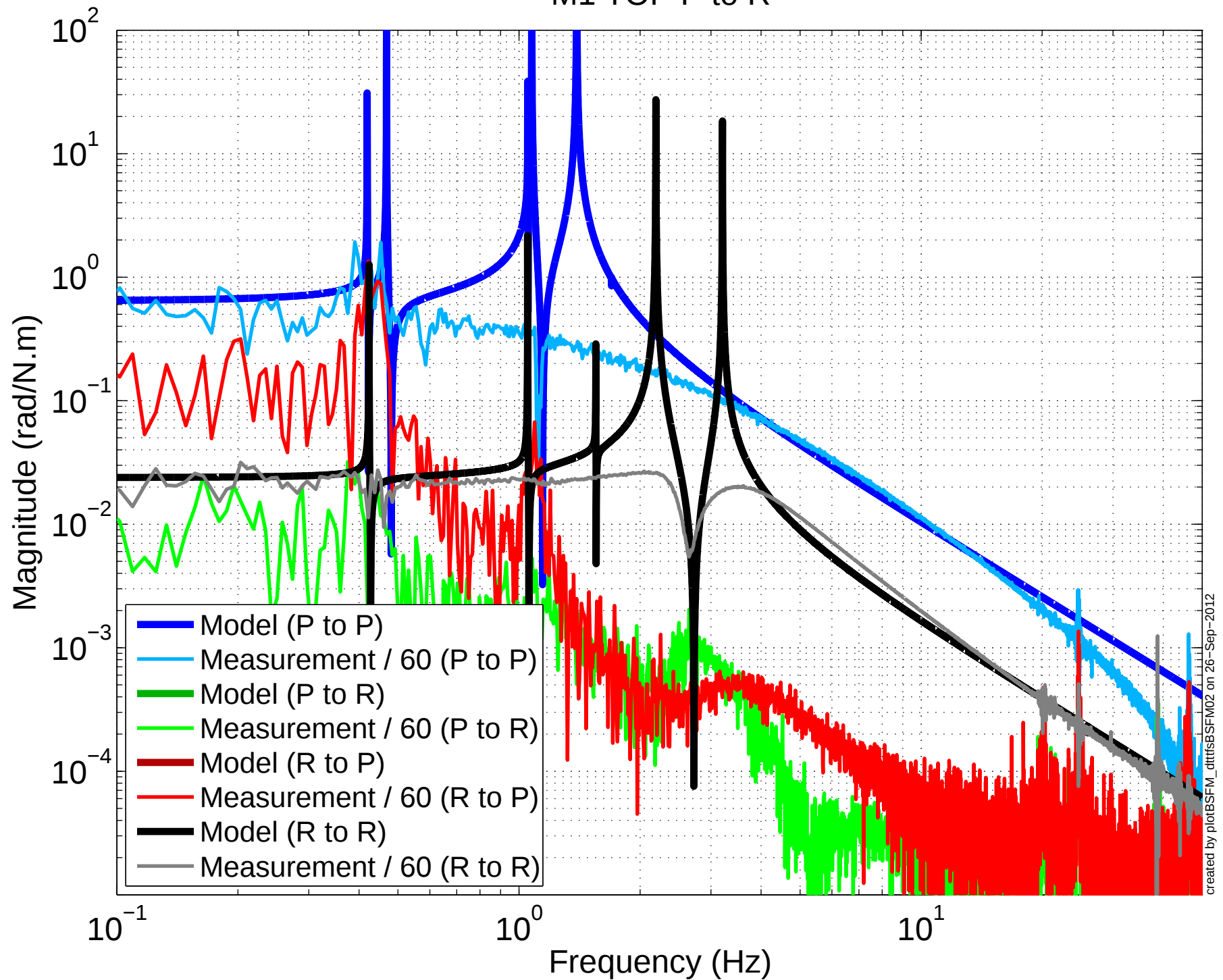
X1SUBSFM02 BUILD01

M1 TOP P to V



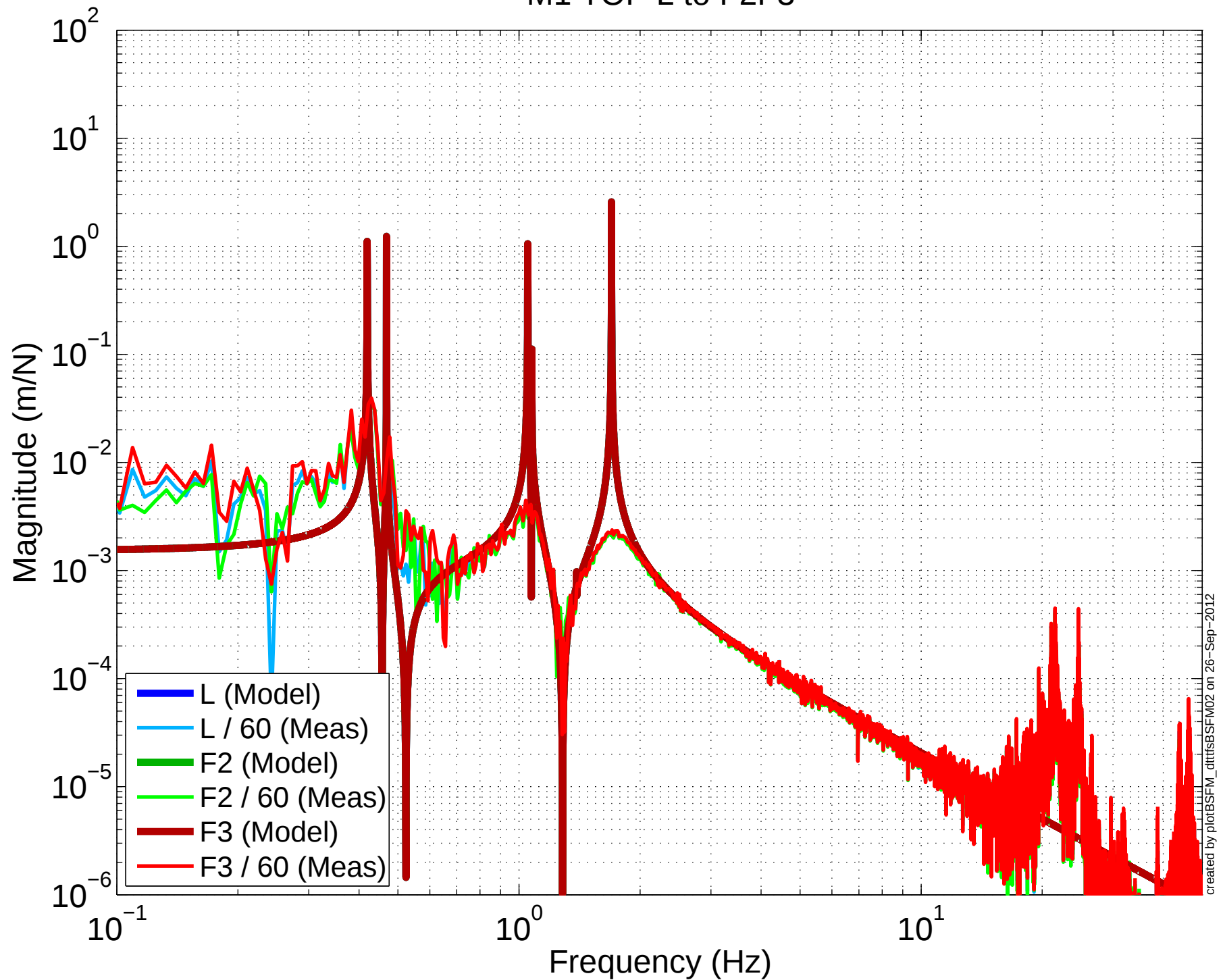
X1SUBSFM02 BUILD01

M1 TOP P to R



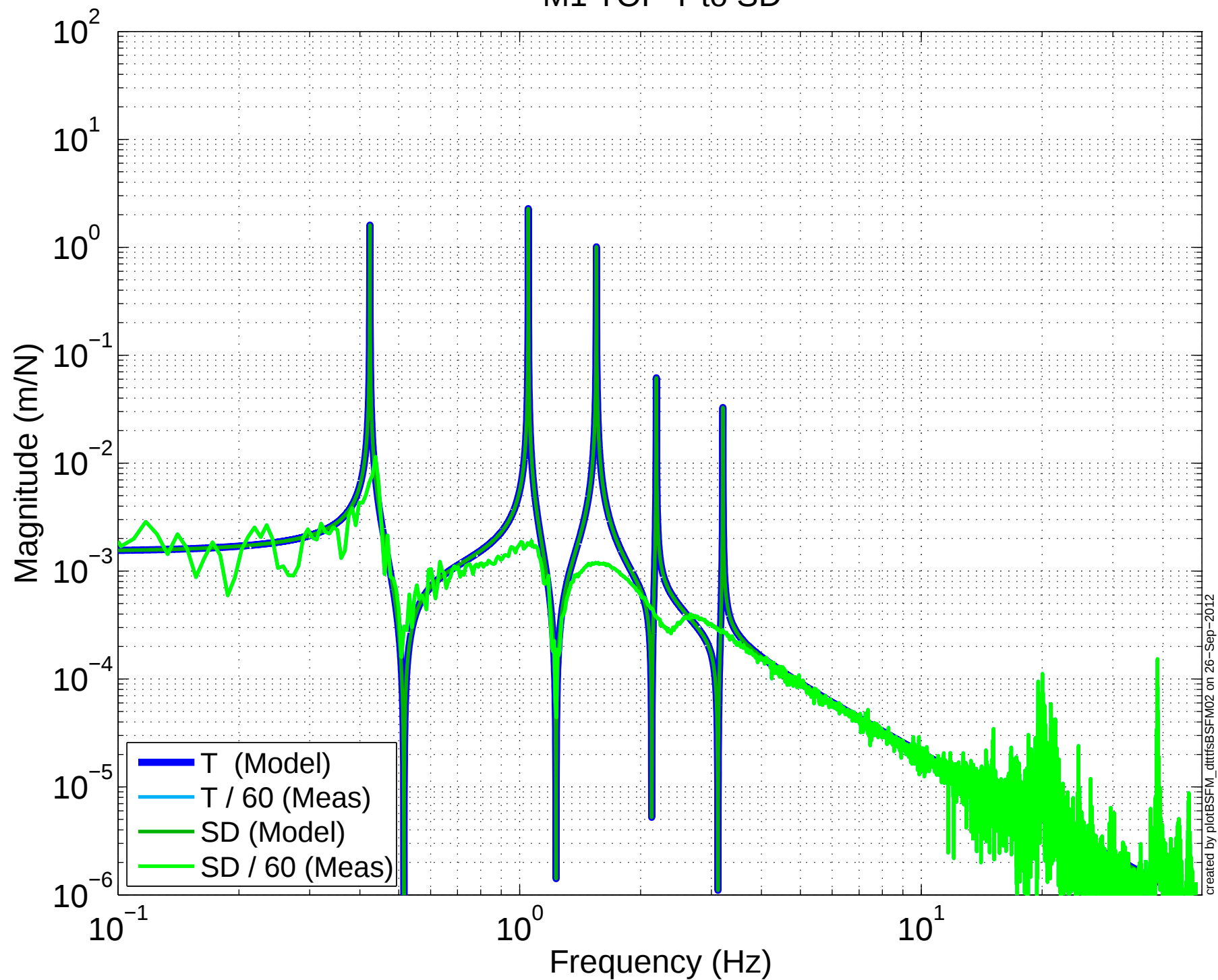
X1SUSBSFM02 BUILD01

M1 TOP L to F2F3

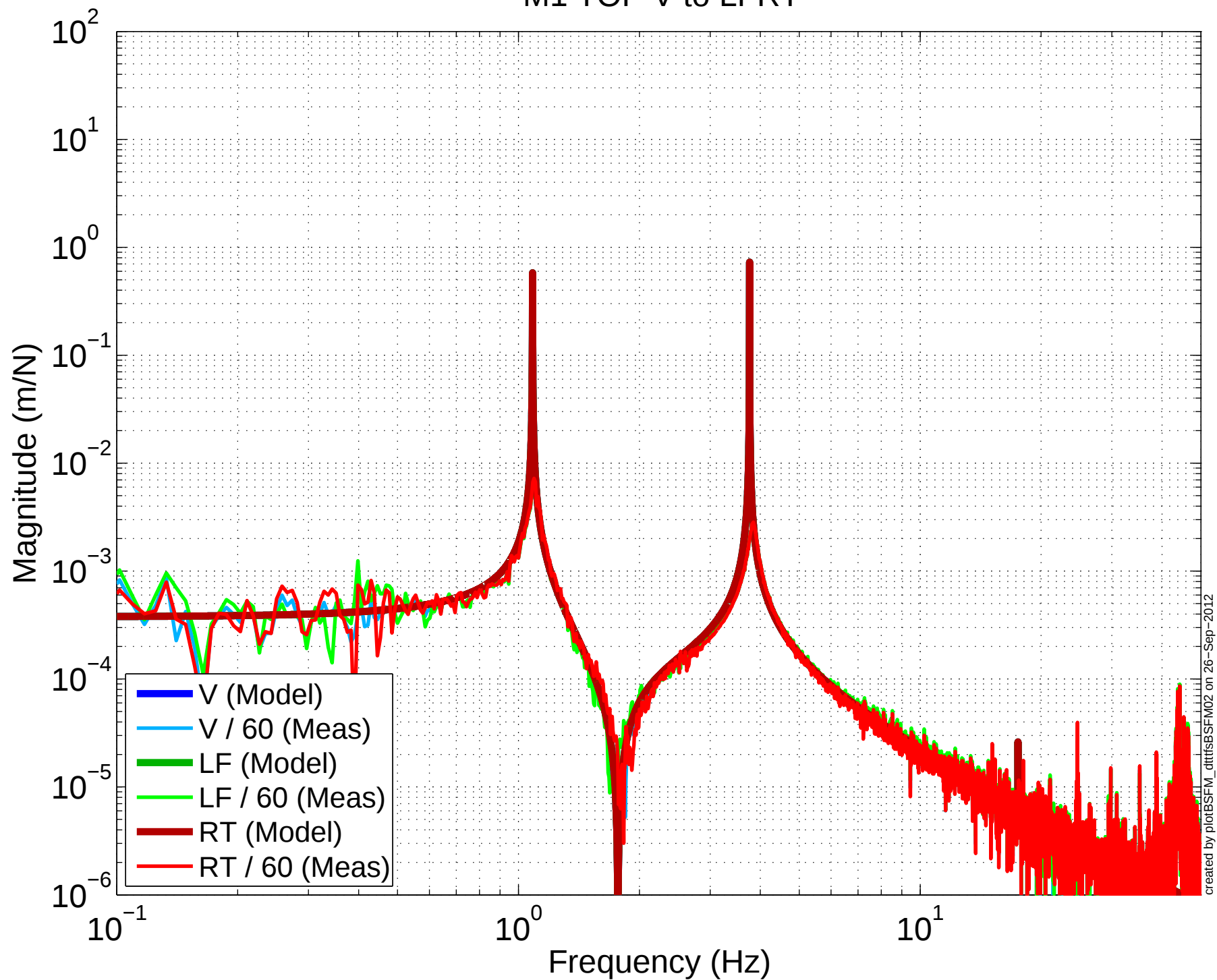


X1SUBSFM02 BUILD01

M1 TOP T to SD

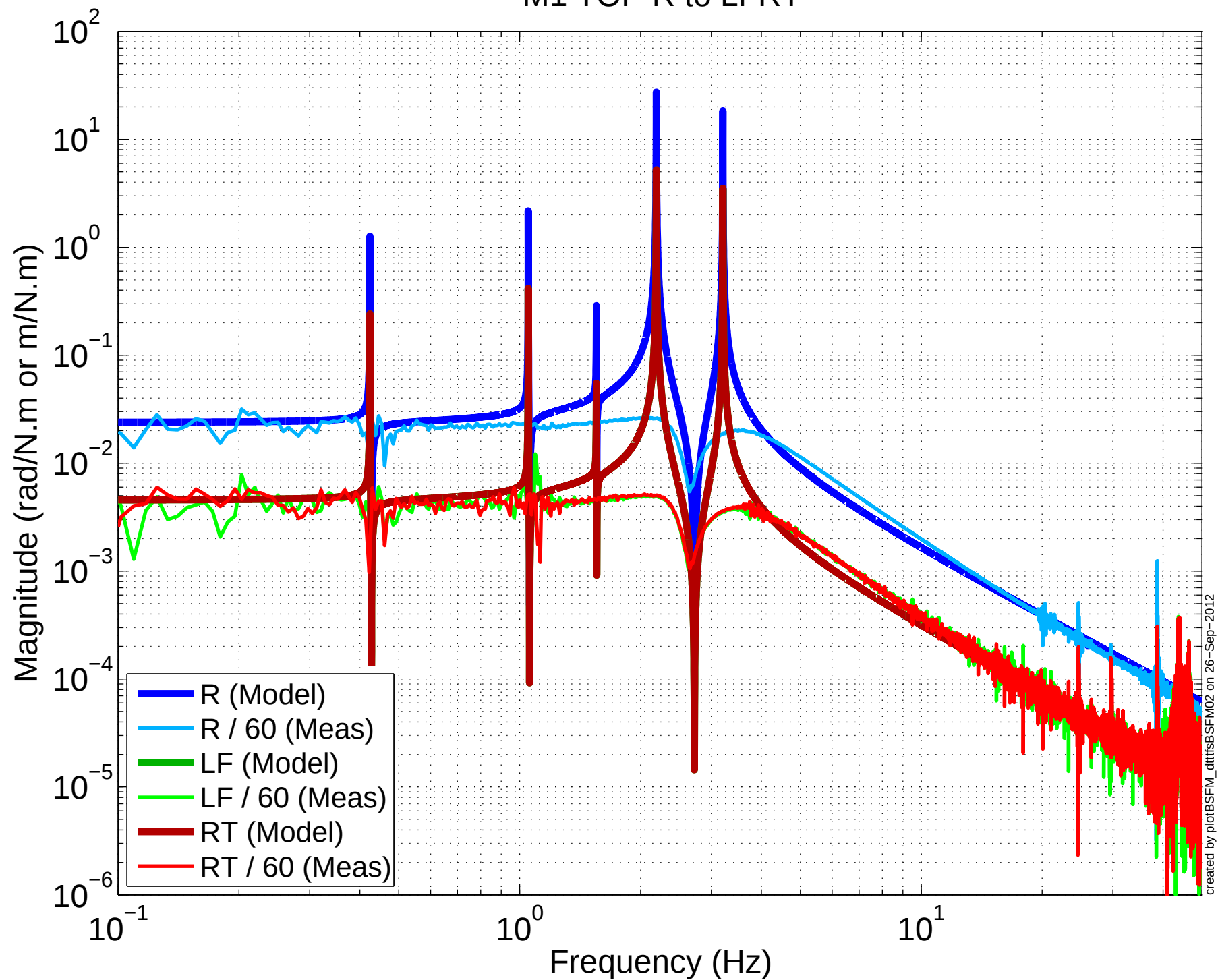


X1SUSBSFM02 BUILD01
M1 TOP V to LFRT



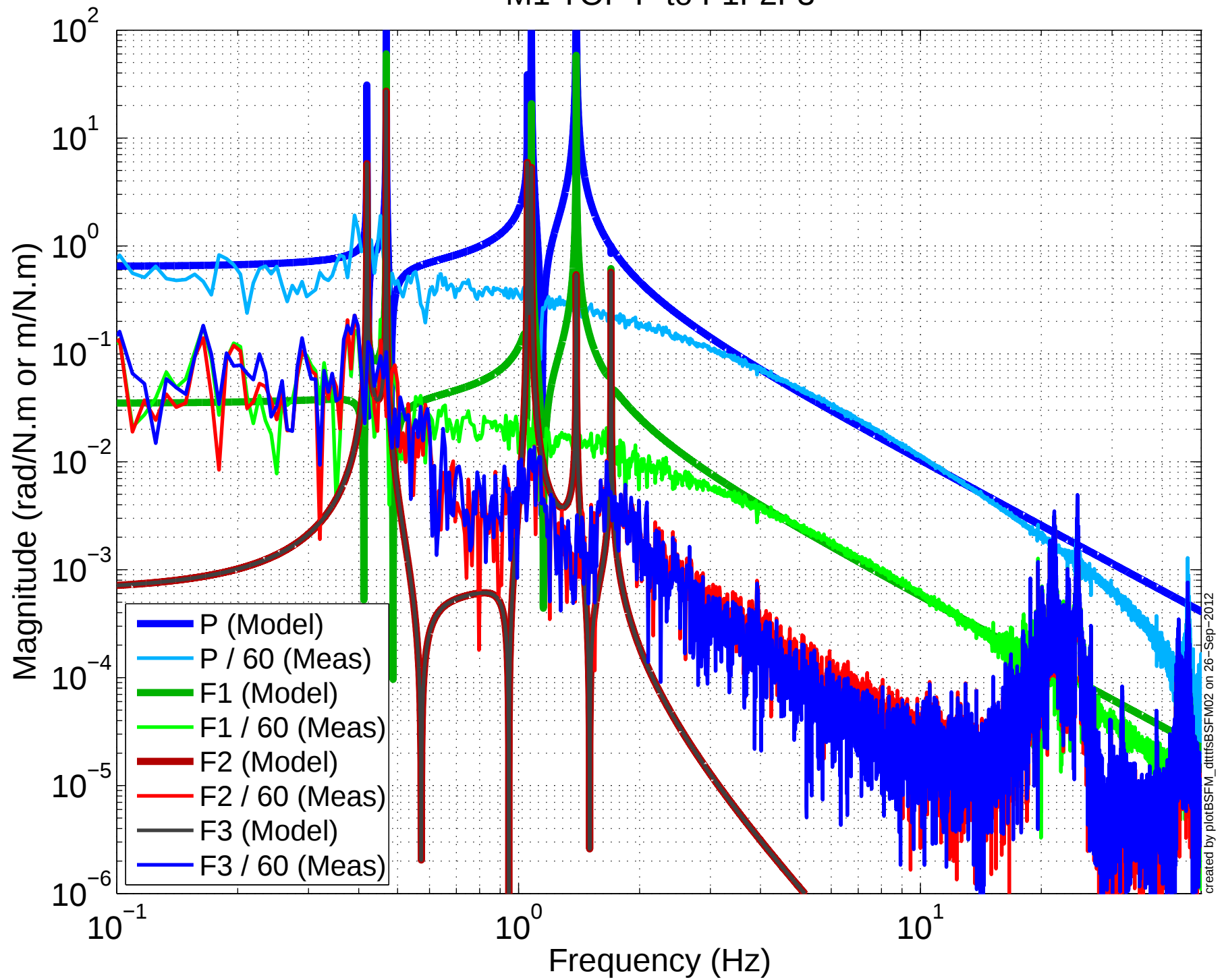
X1SUSBSFM02 BUILD01

M1 TOP R to LFRT



X1SUBSFM02 BUILD01

M1 TOP P to F1F2F3



X1SUSBSFM02 BUILD01

M1 TOP Y to F2F3

