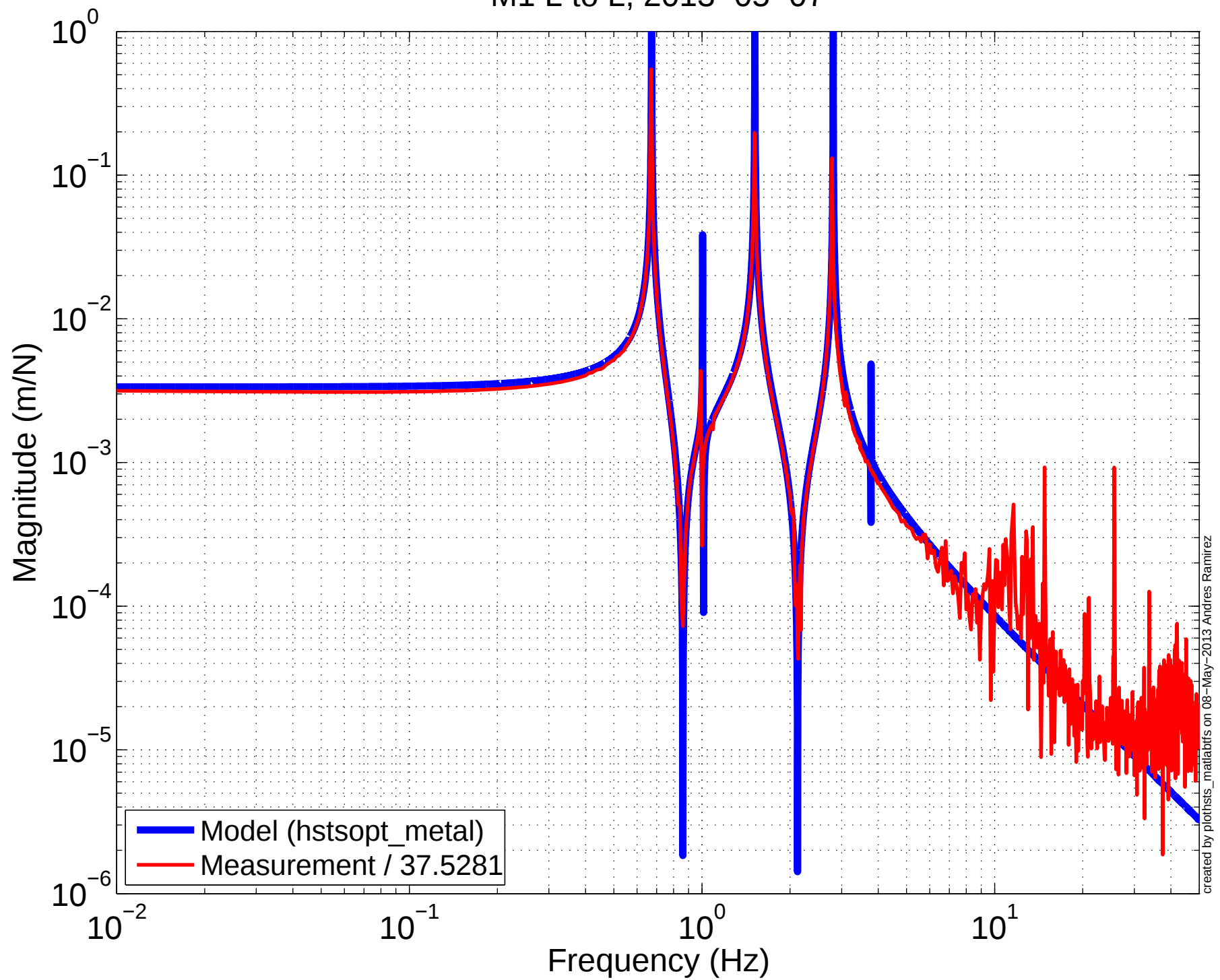
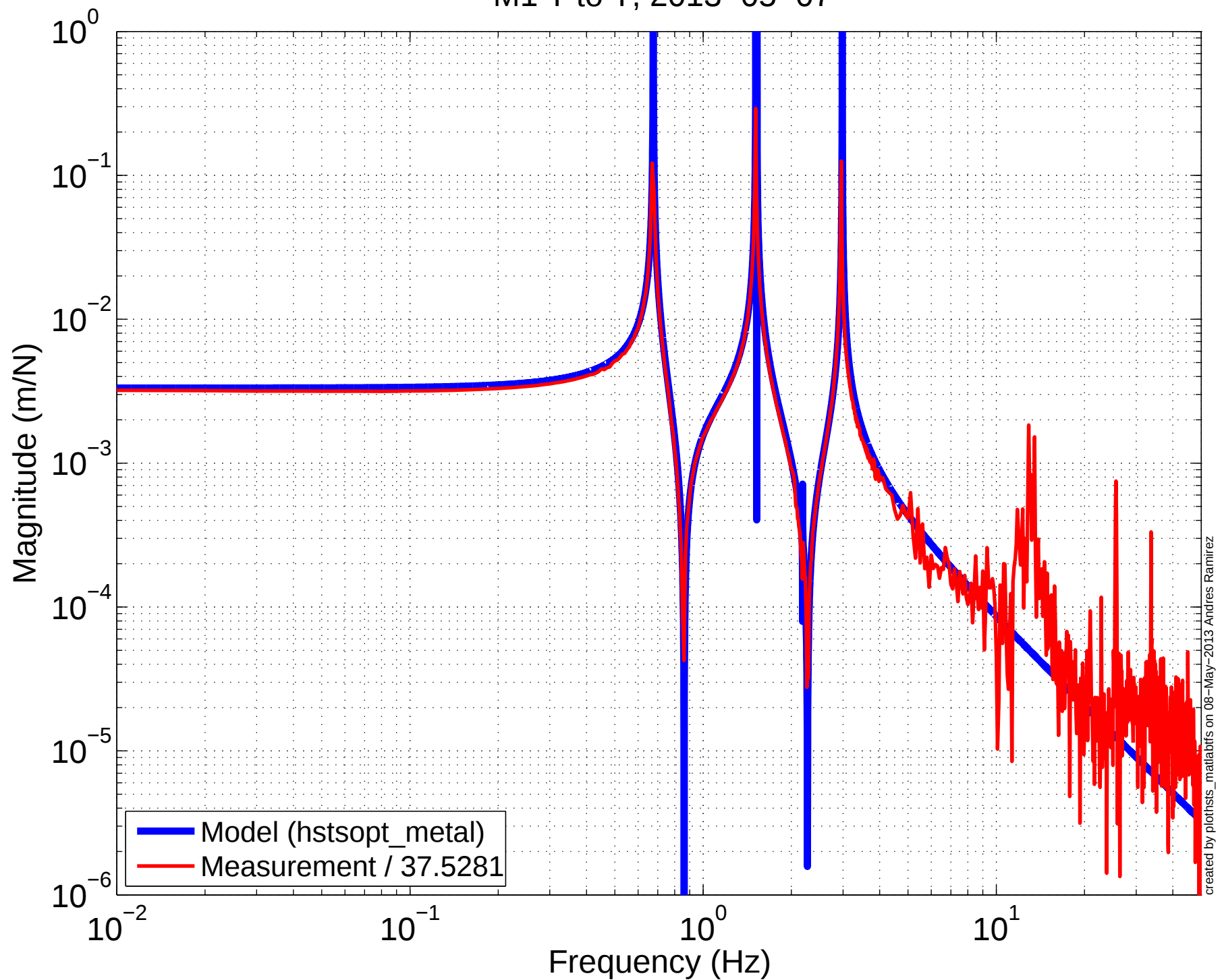


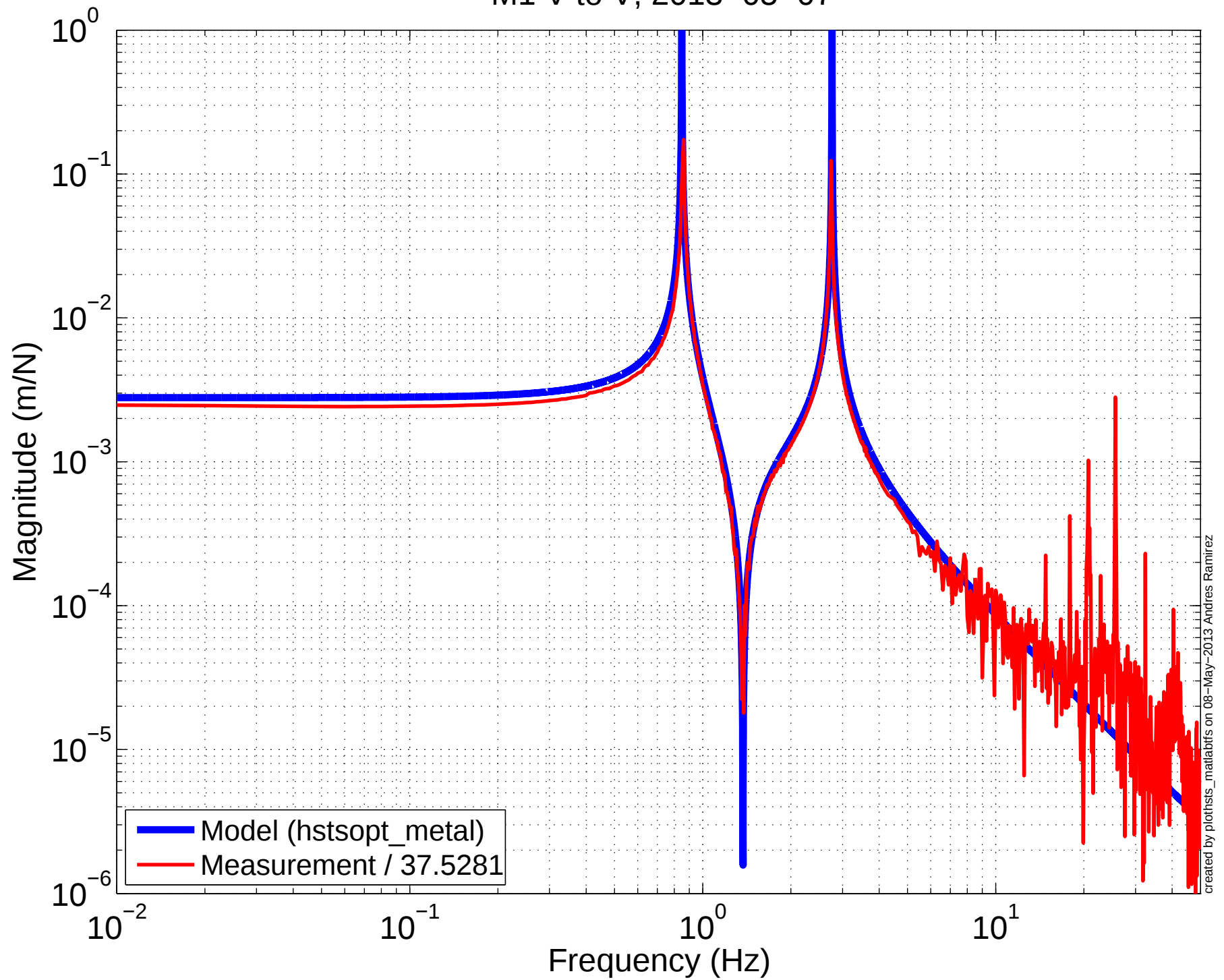
X1SUSPR2
M1 L to L, 2013-05-07



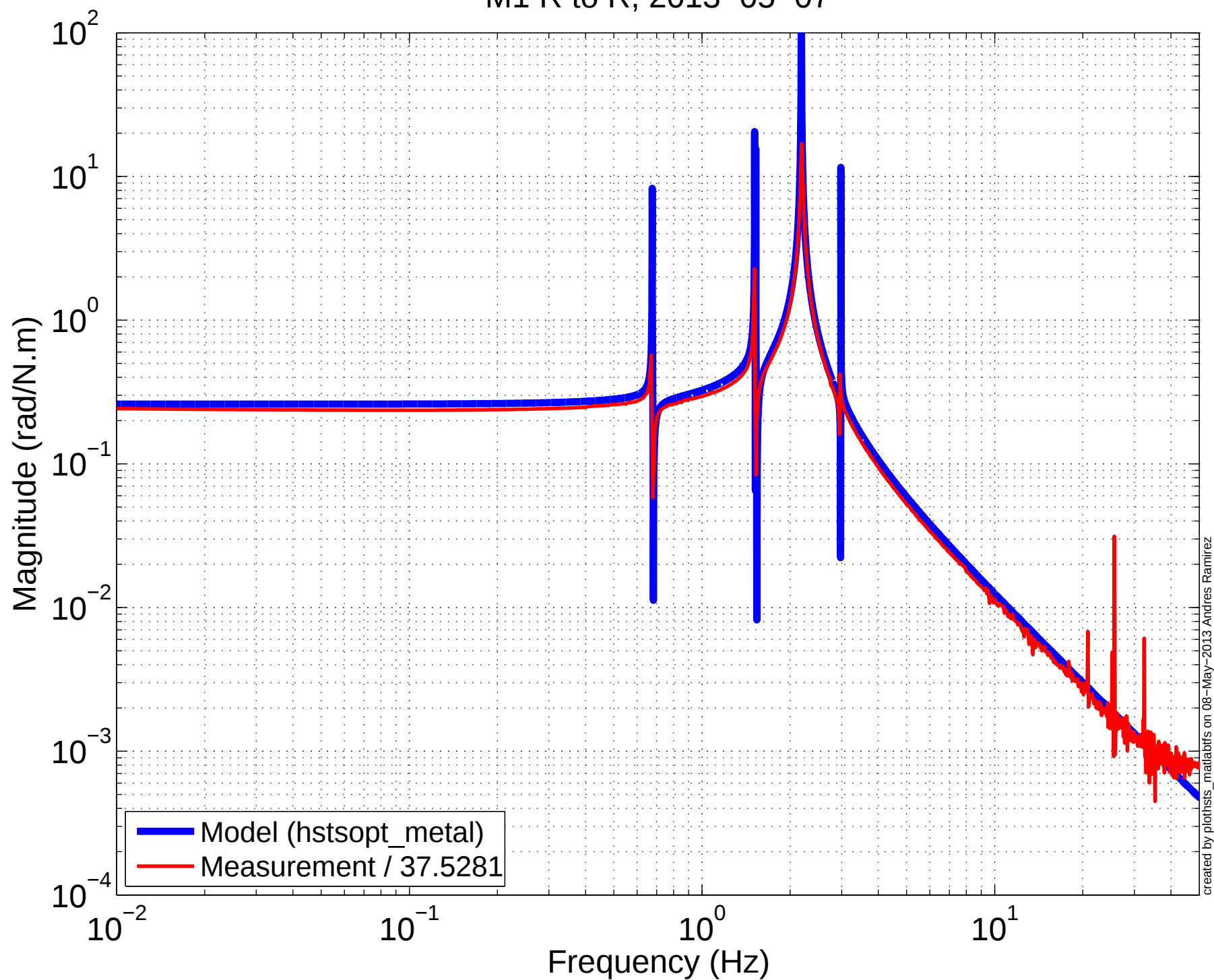
X1SUSPR2
M1 T to T, 2013-05-07



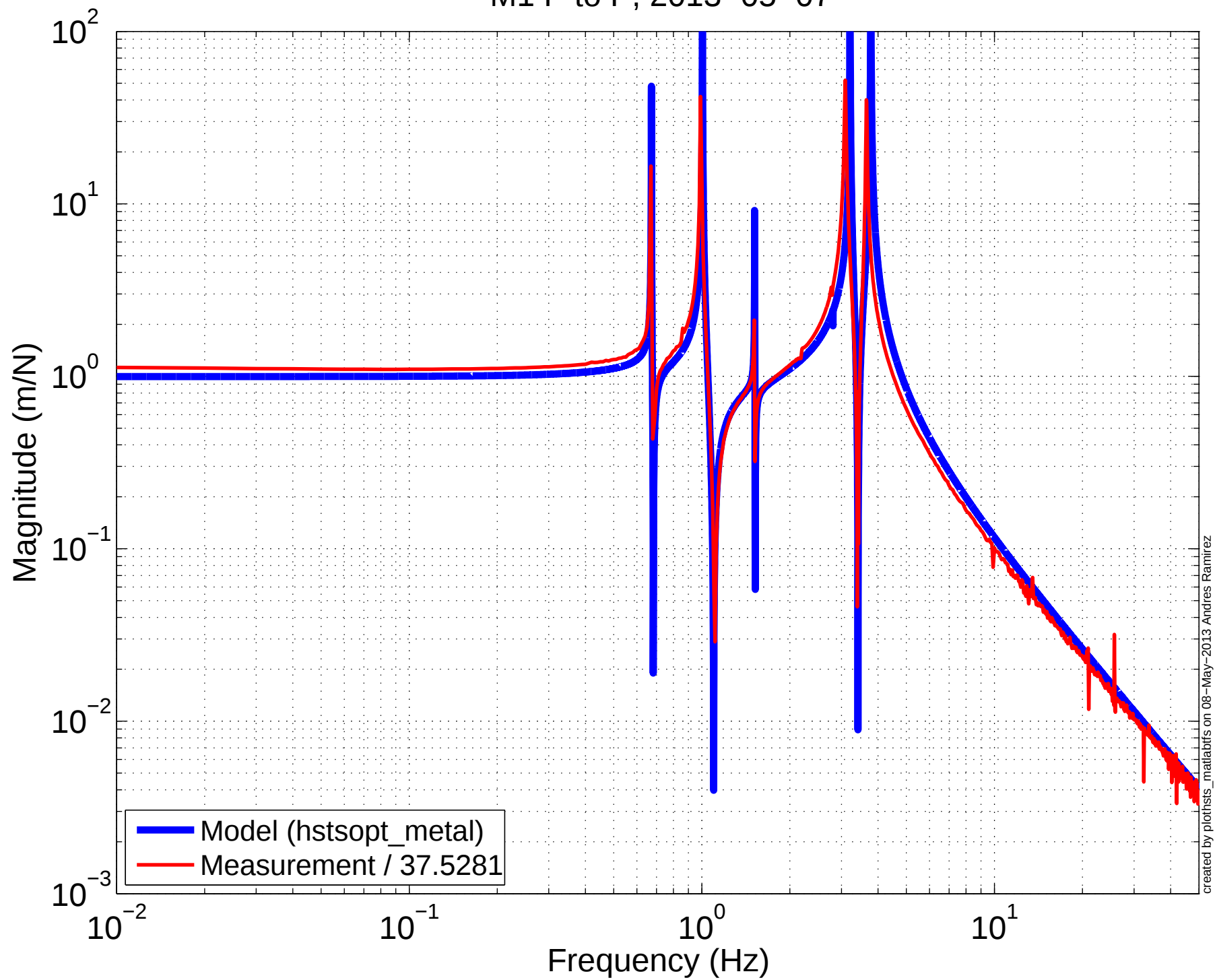
X1SUSPR2
M1 V to V, 2013-05-07



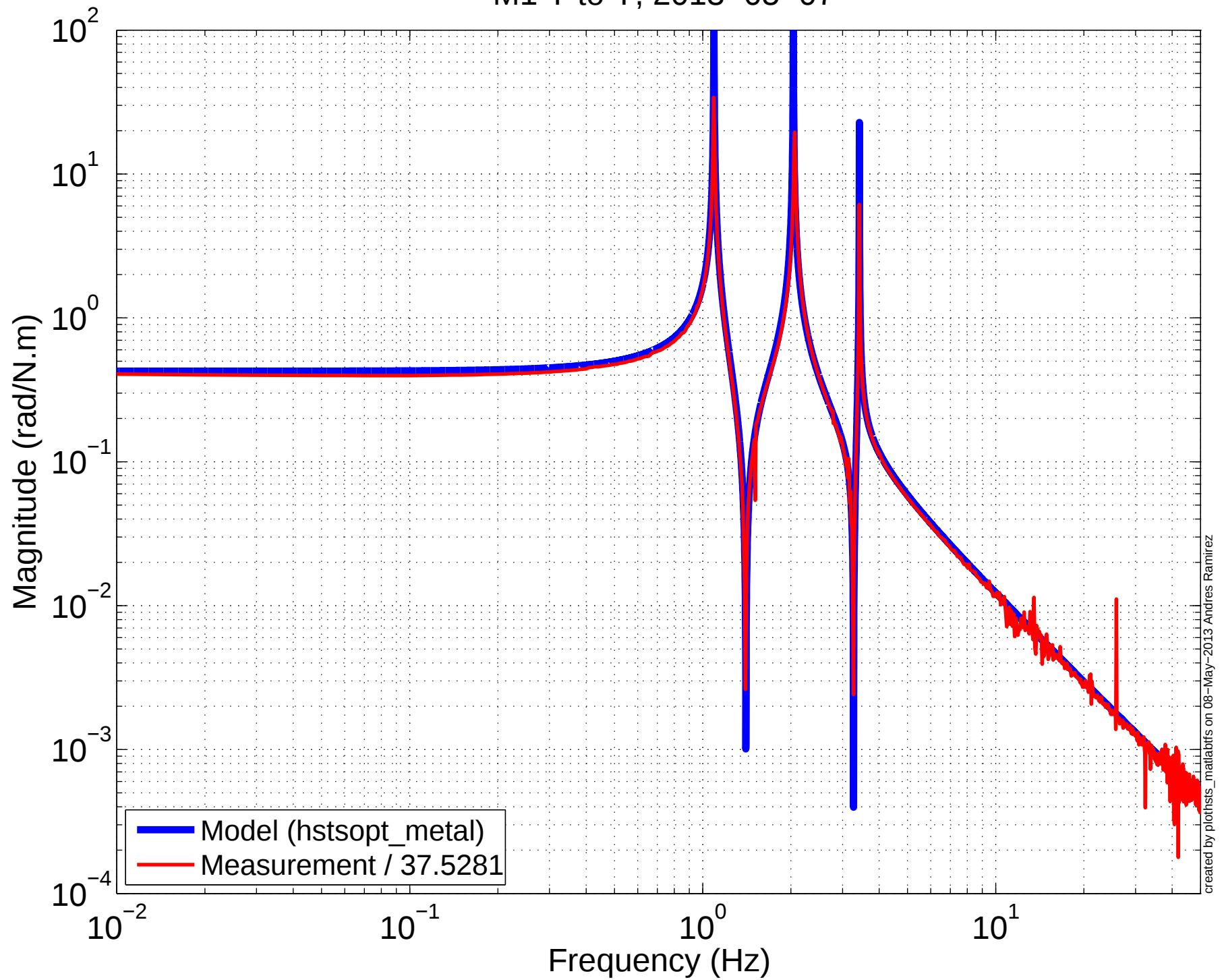
X1SUSPR2
M1 R to R, 2013-05-07



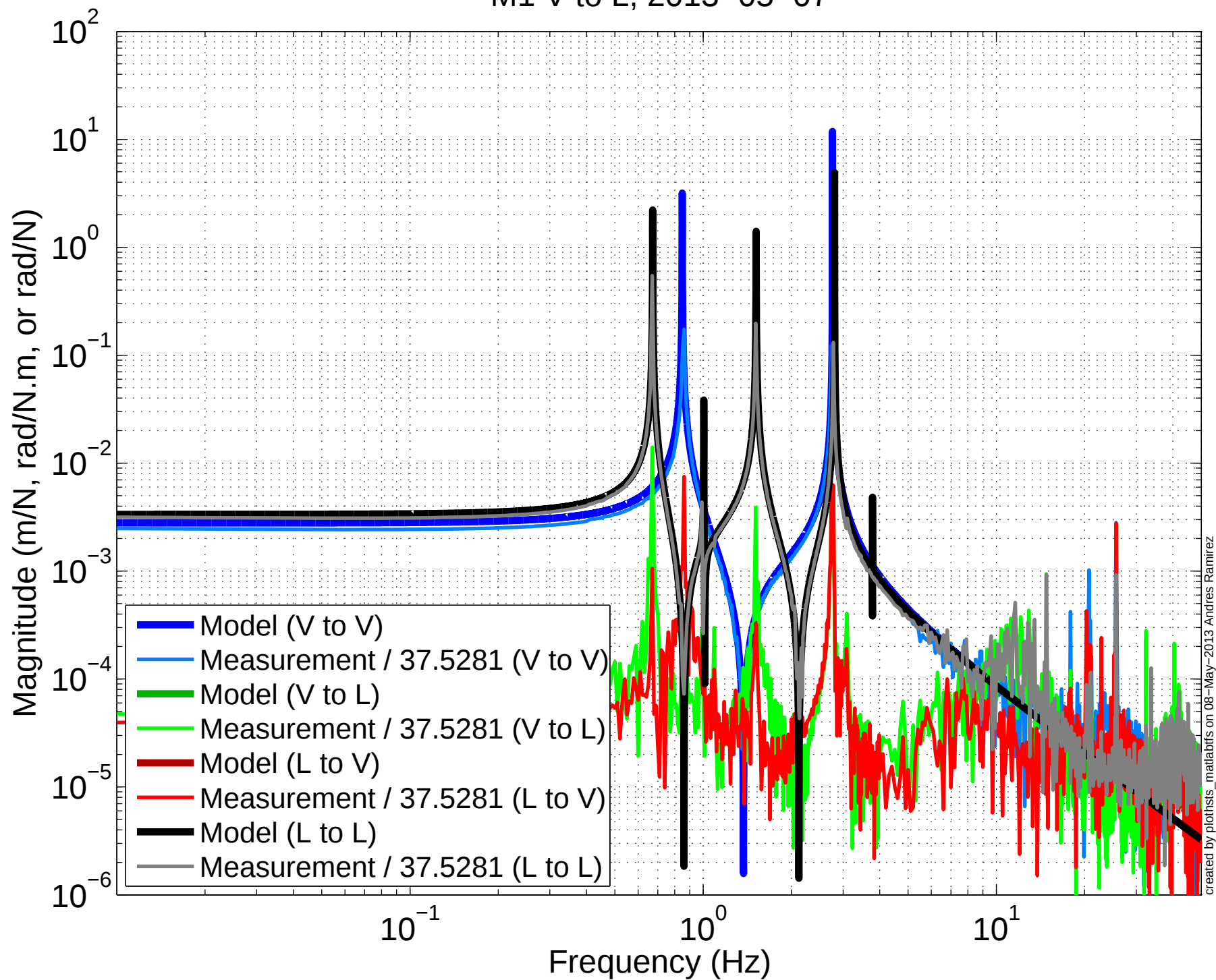
X1SUSPR2
M1 P to P, 2013-05-07



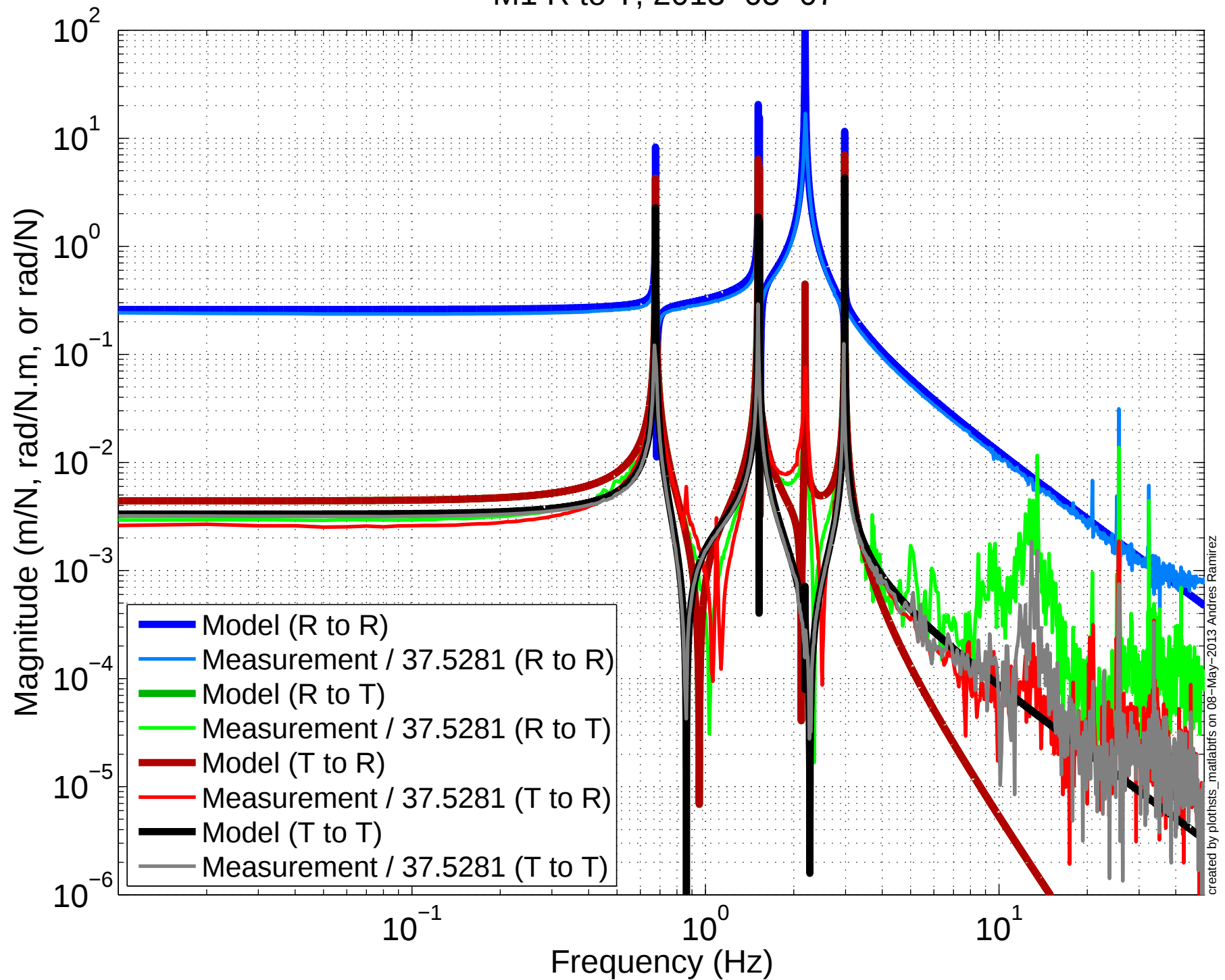
X1SUSPR2
M1 Y to Y, 2013-05-07



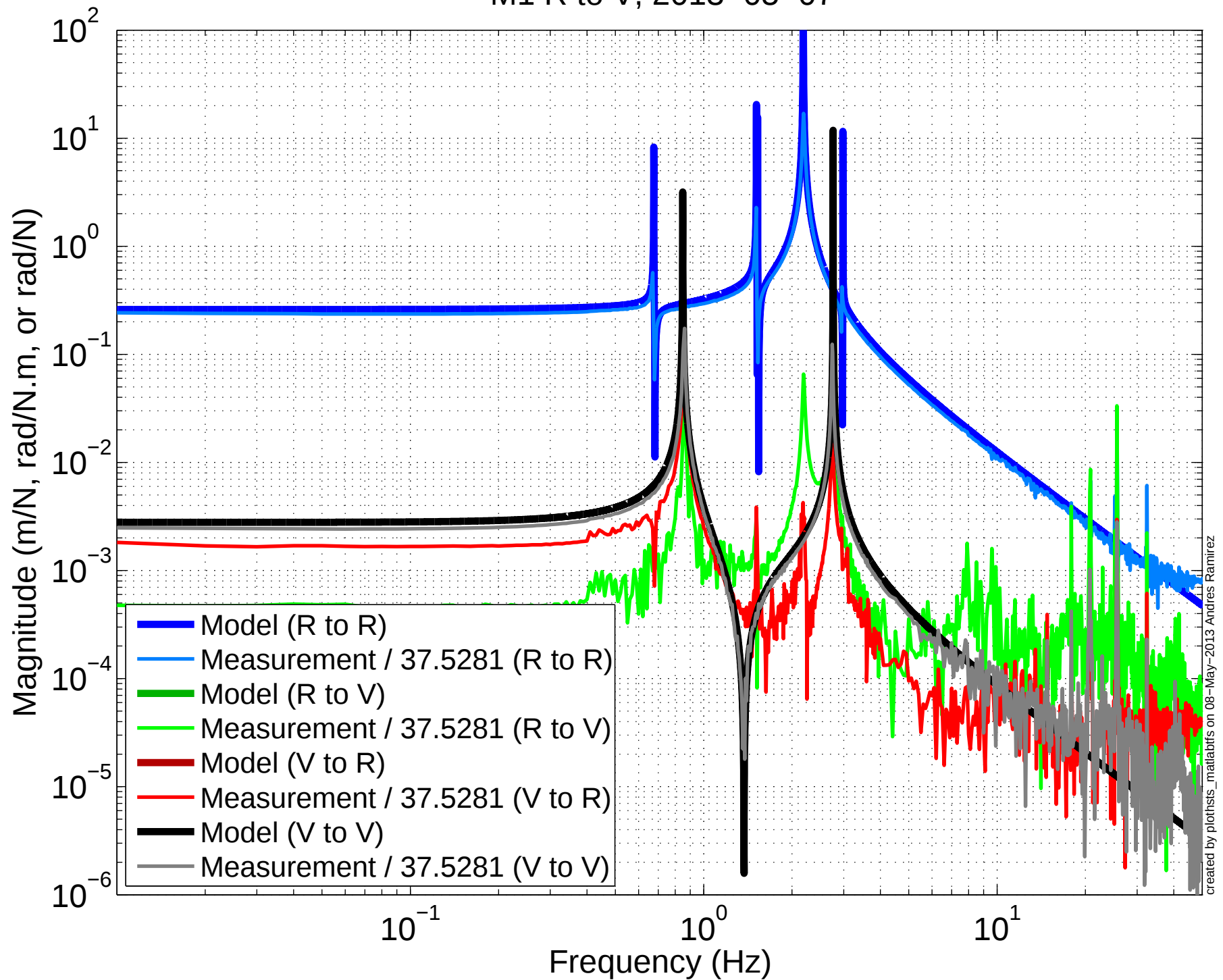
X1SUSPR2
M1 V to L, 2013-05-07



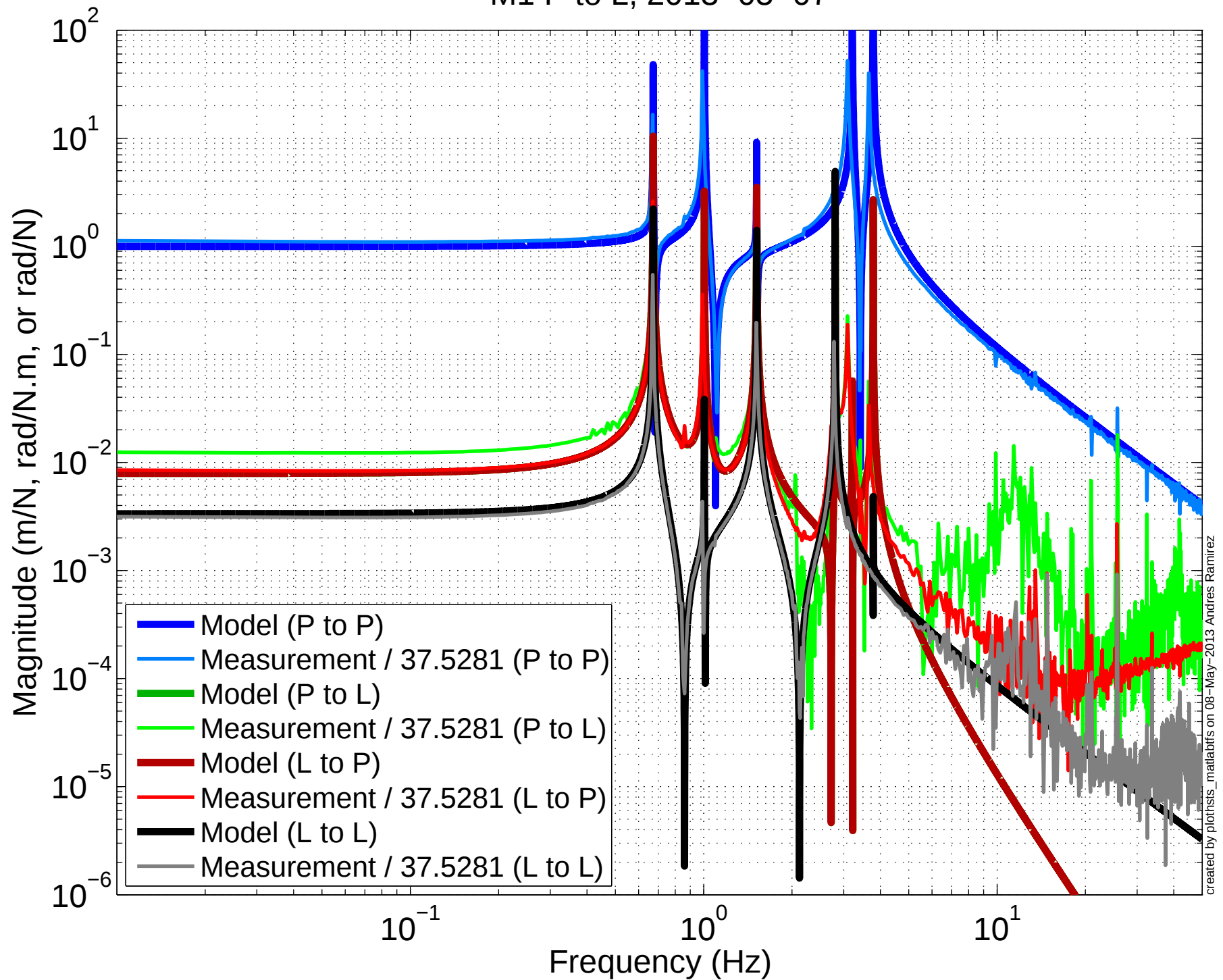
X1SUSPR2
M1 R to T, 2013-05-07



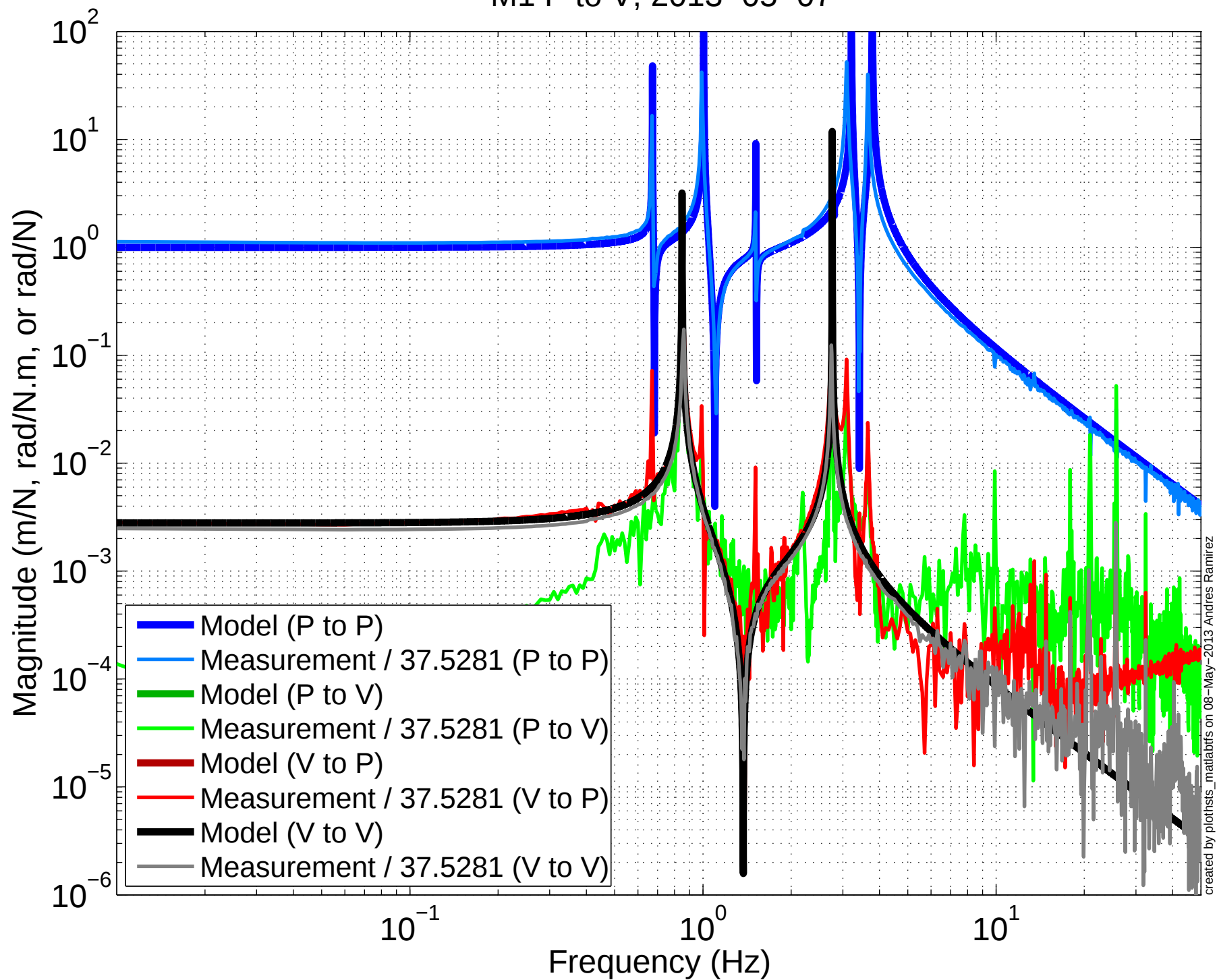
X1SUSPR2
M1 R to V, 2013-05-07



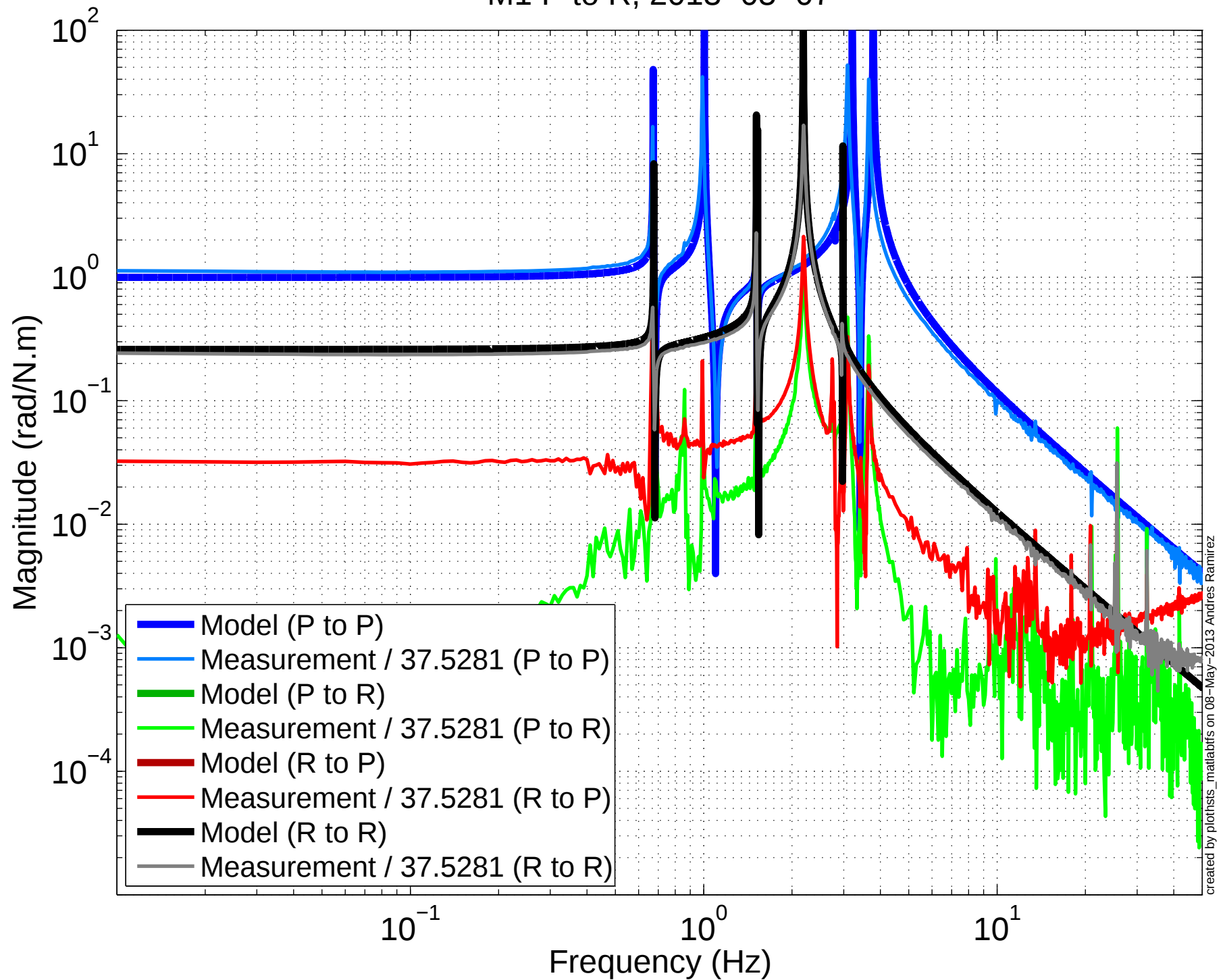
X1SUSPR2
M1 P to L, 2013-05-07



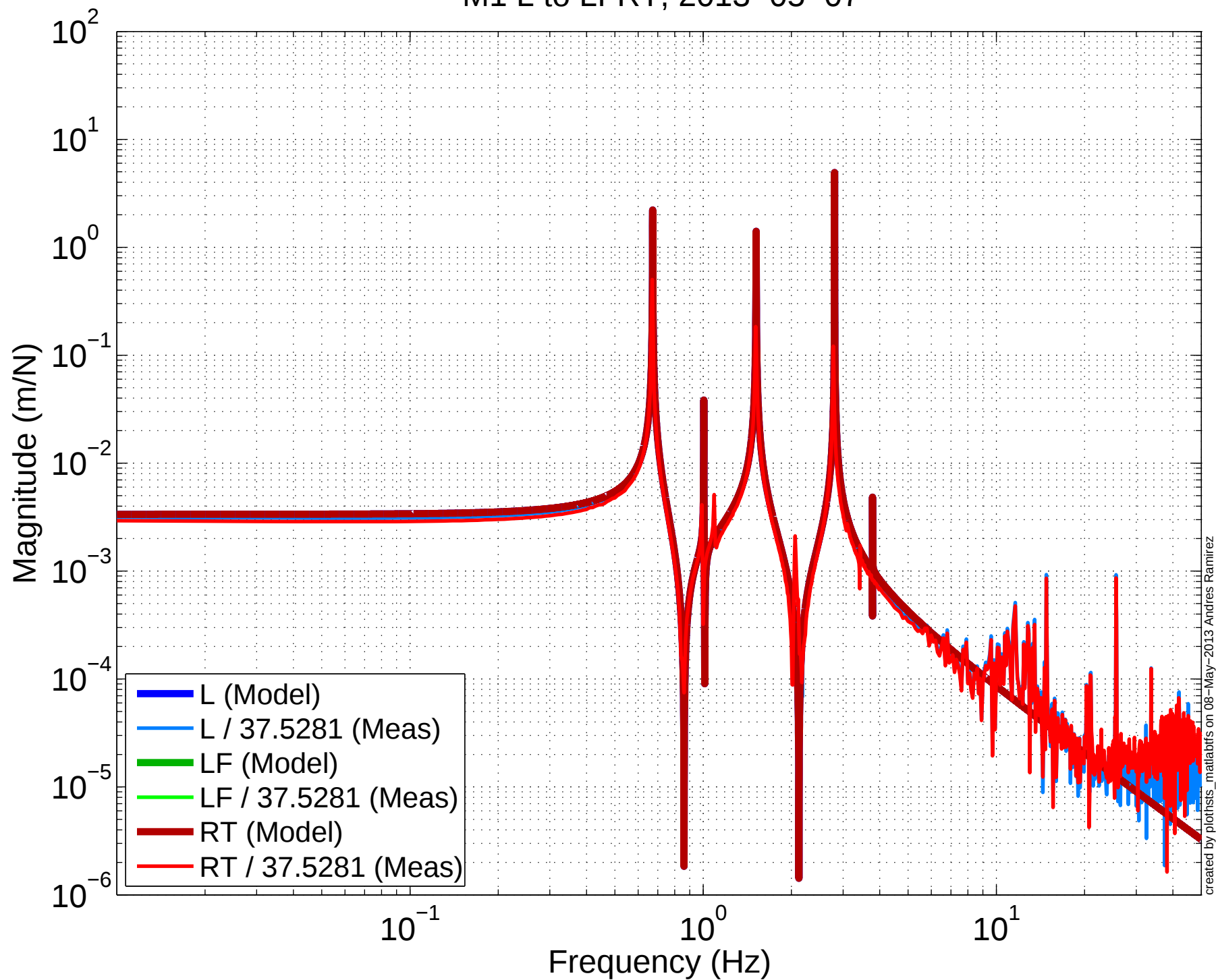
X1SUSPR2
M1 P to V, 2013-05-07



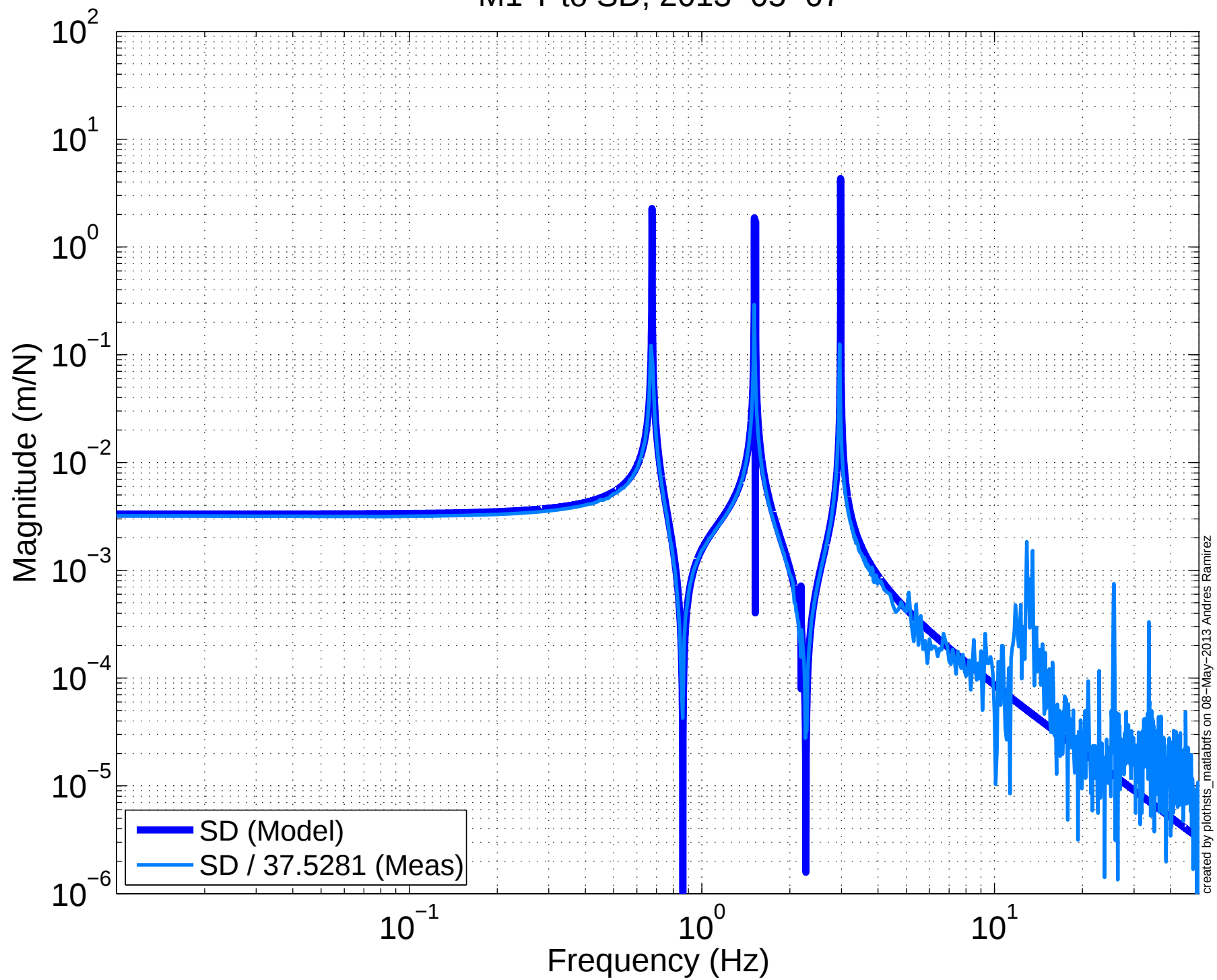
X1SUSPR2
M1 P to R, 2013-05-07



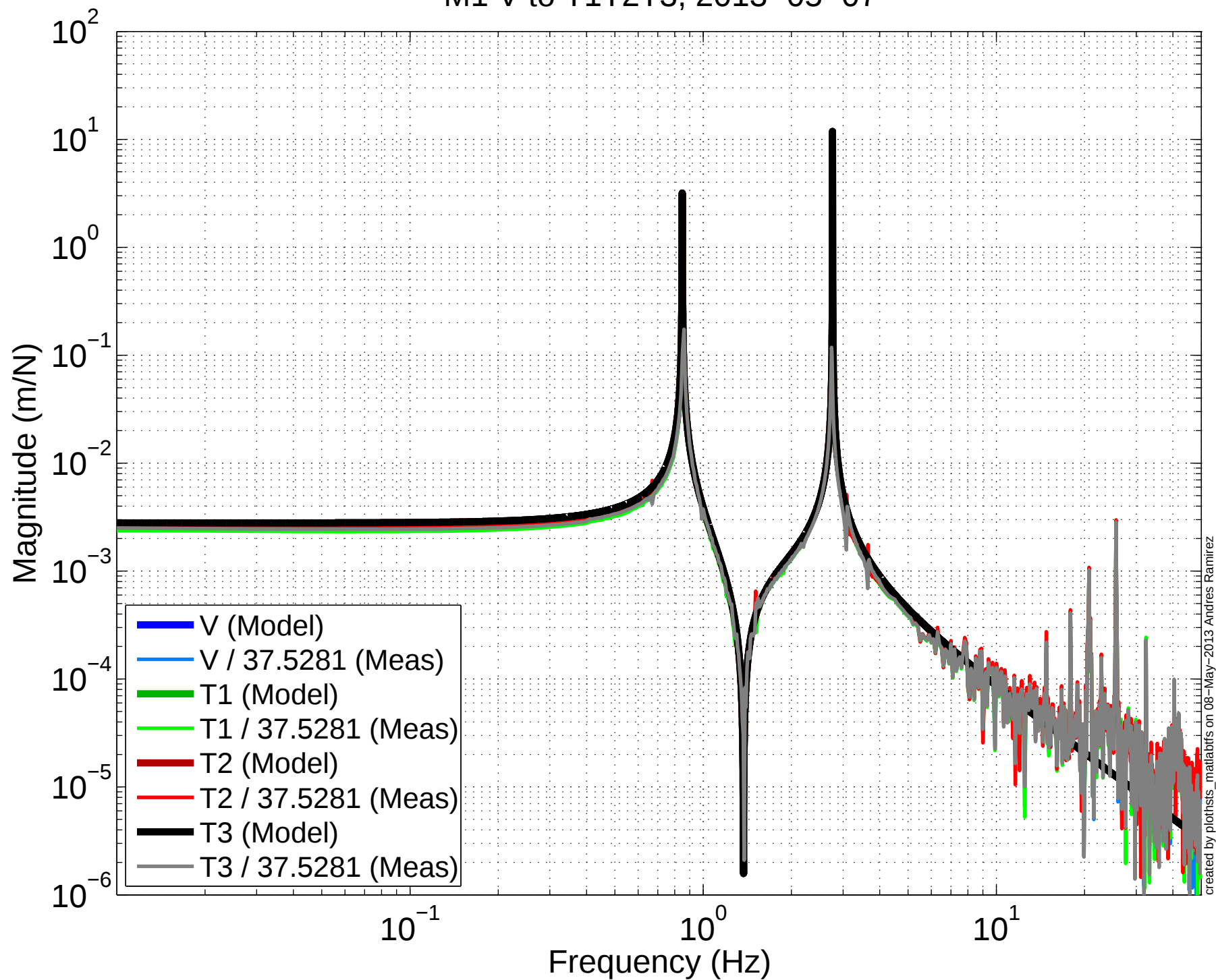
X1SUSPR2
M1 L to LFRT, 2013-05-07



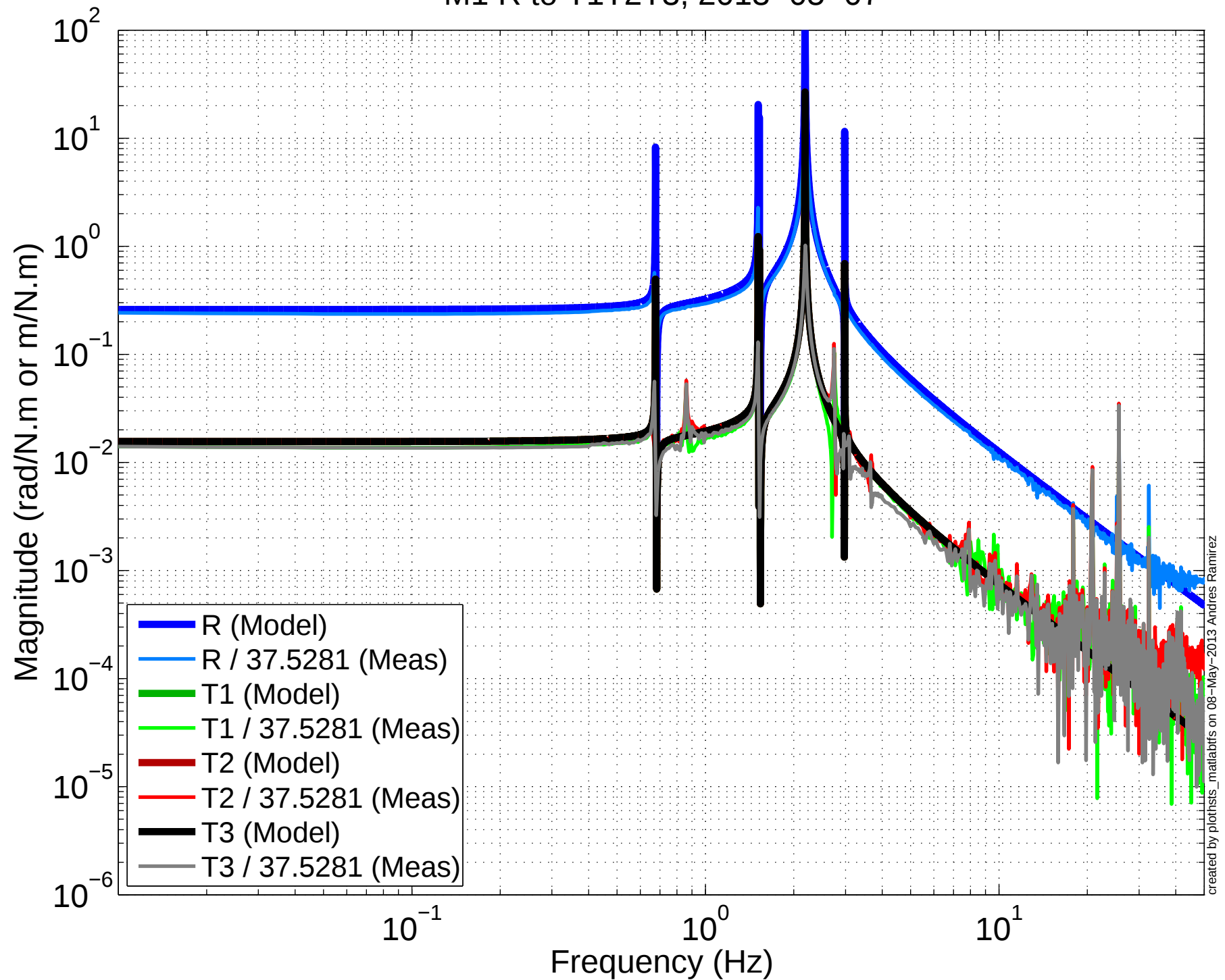
X1SUSPR2
M1 T to SD, 2013-05-07



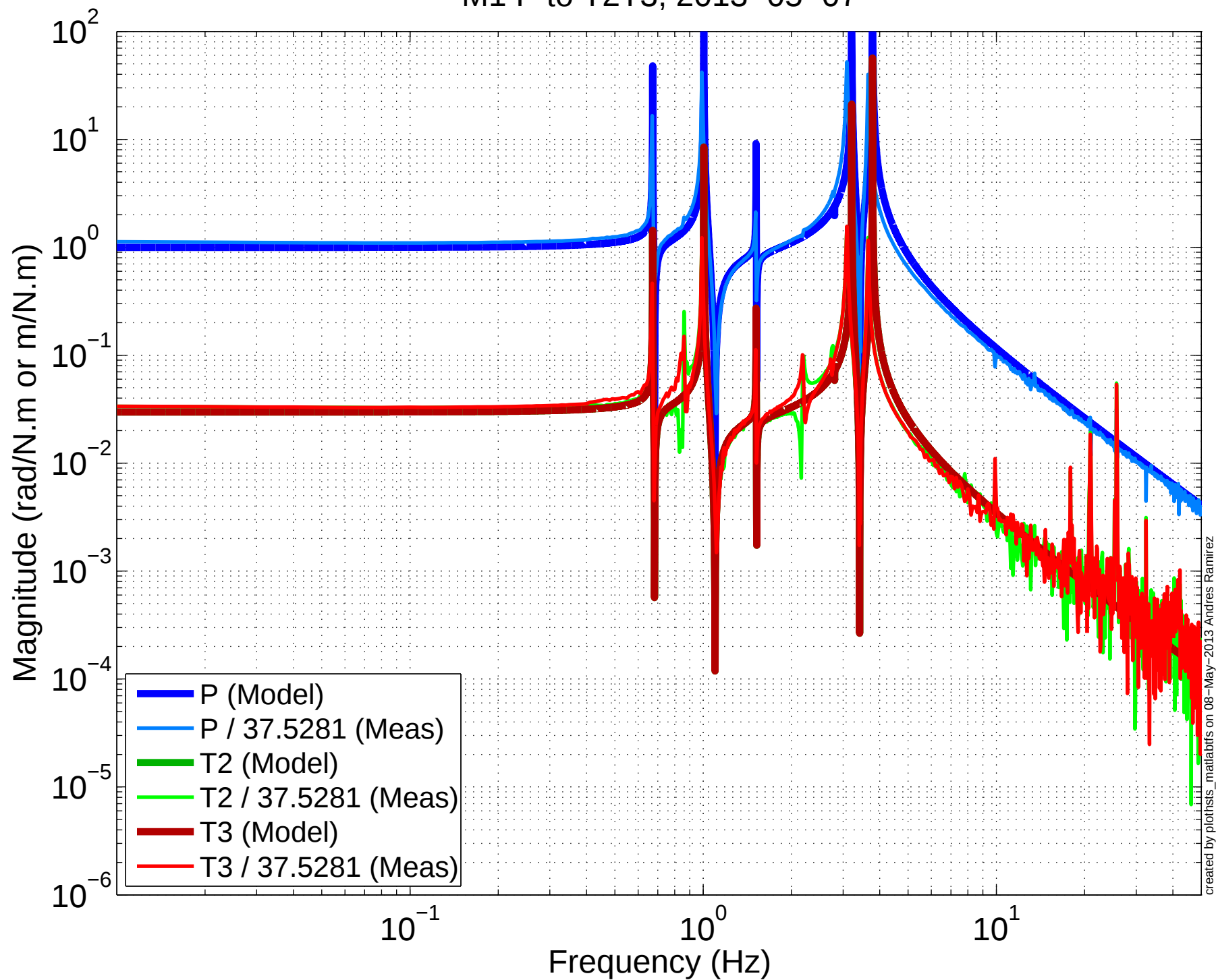
X1SUSPR2
M1 V to T1T2T3, 2013-05-07



X1SUSPR2
M1 R to T1T2T3, 2013-05-07



X1SUSPR2
M1 P to T2T3, 2013-05-07



X1SUSPR2
M1 Y to LFRT, 2013-05-07

