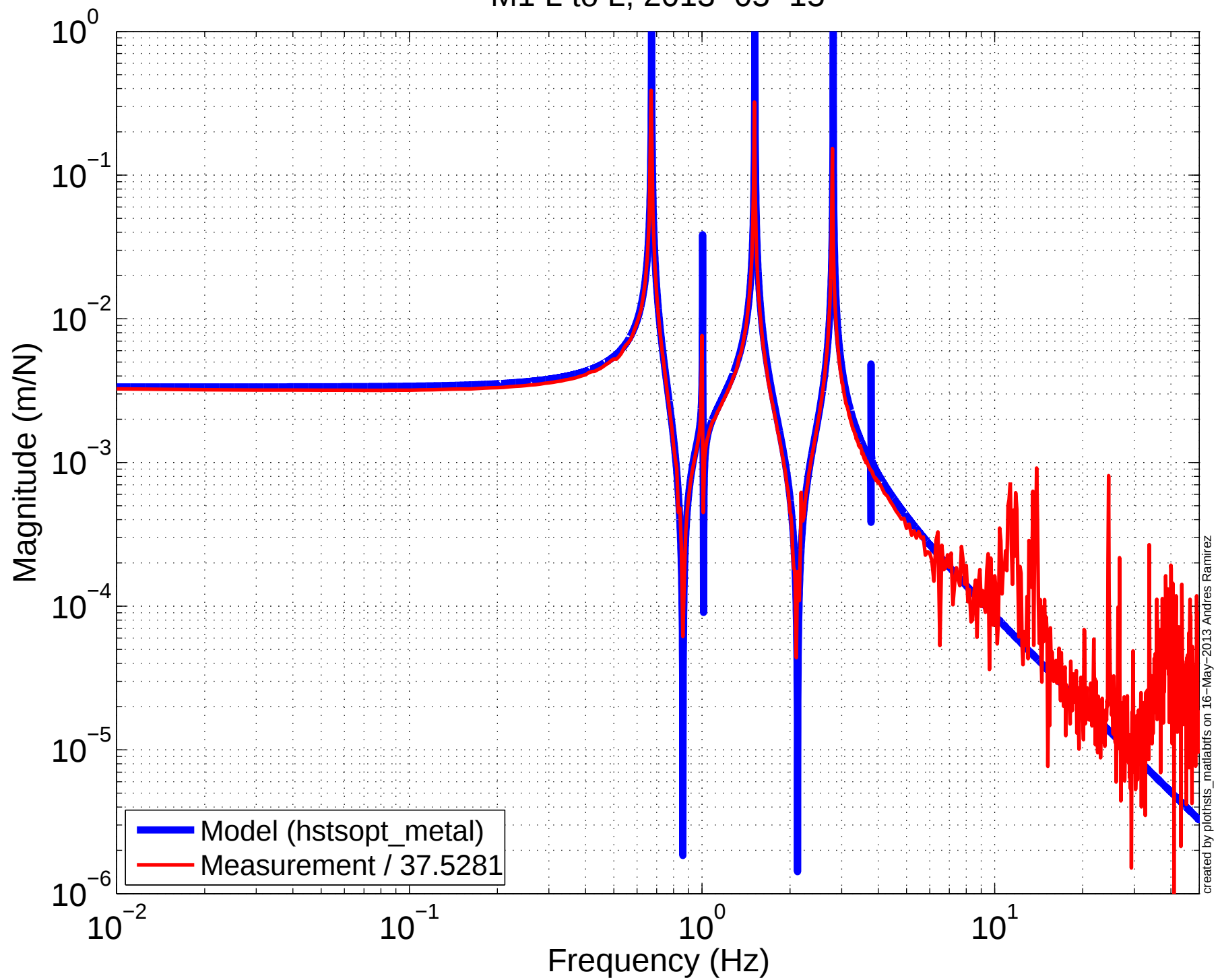
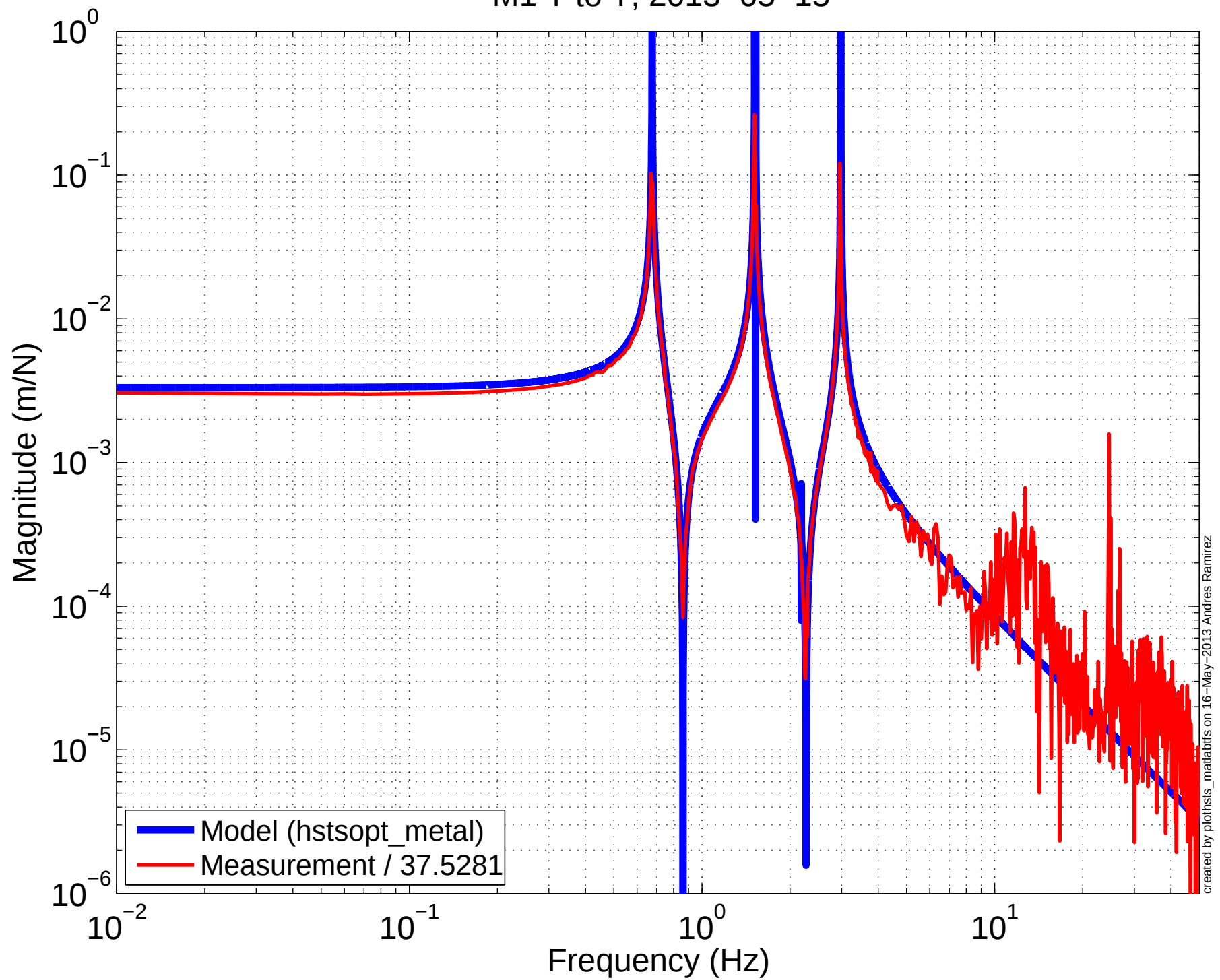


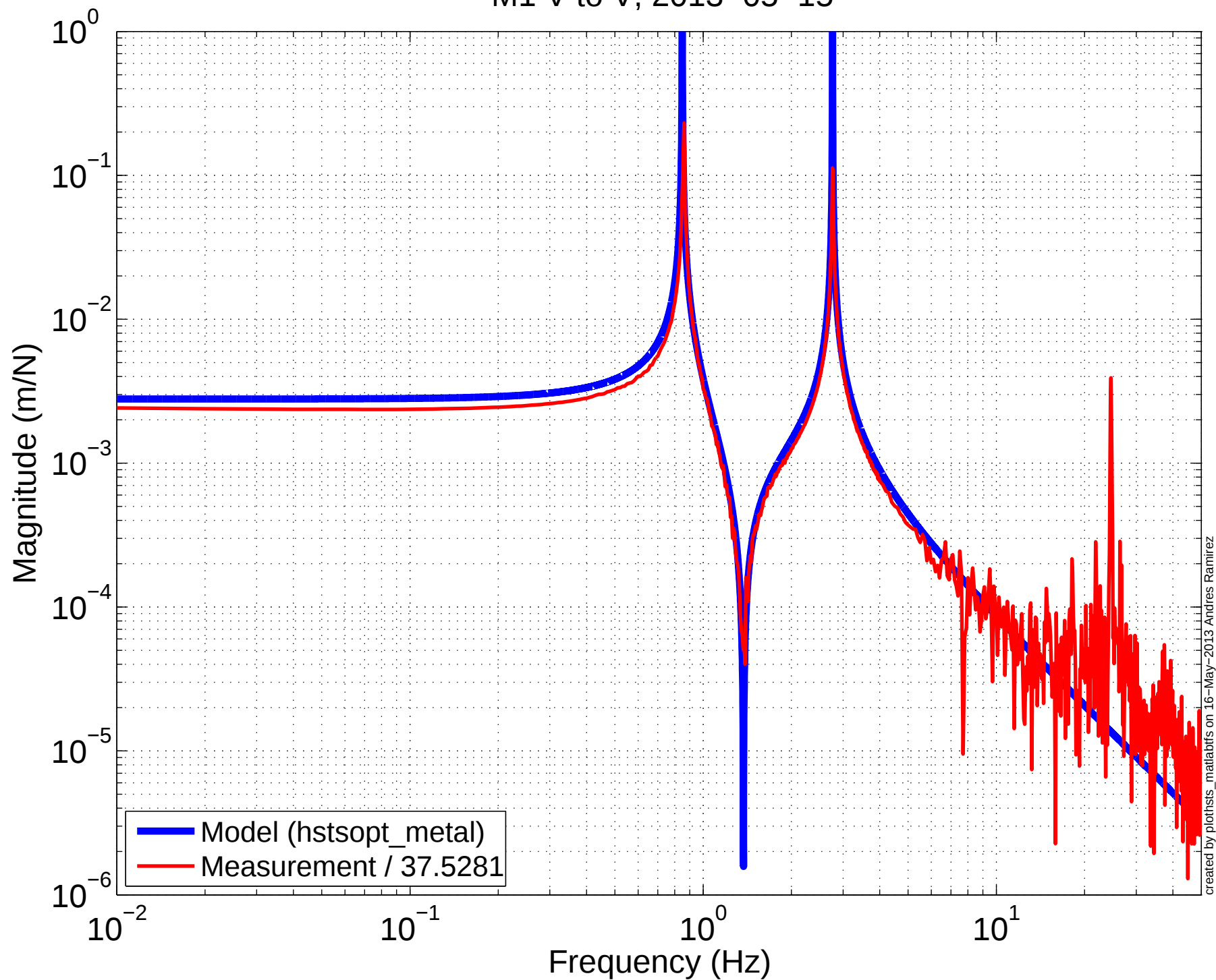
X1SUSSR2
M1 L to L, 2013-05-15



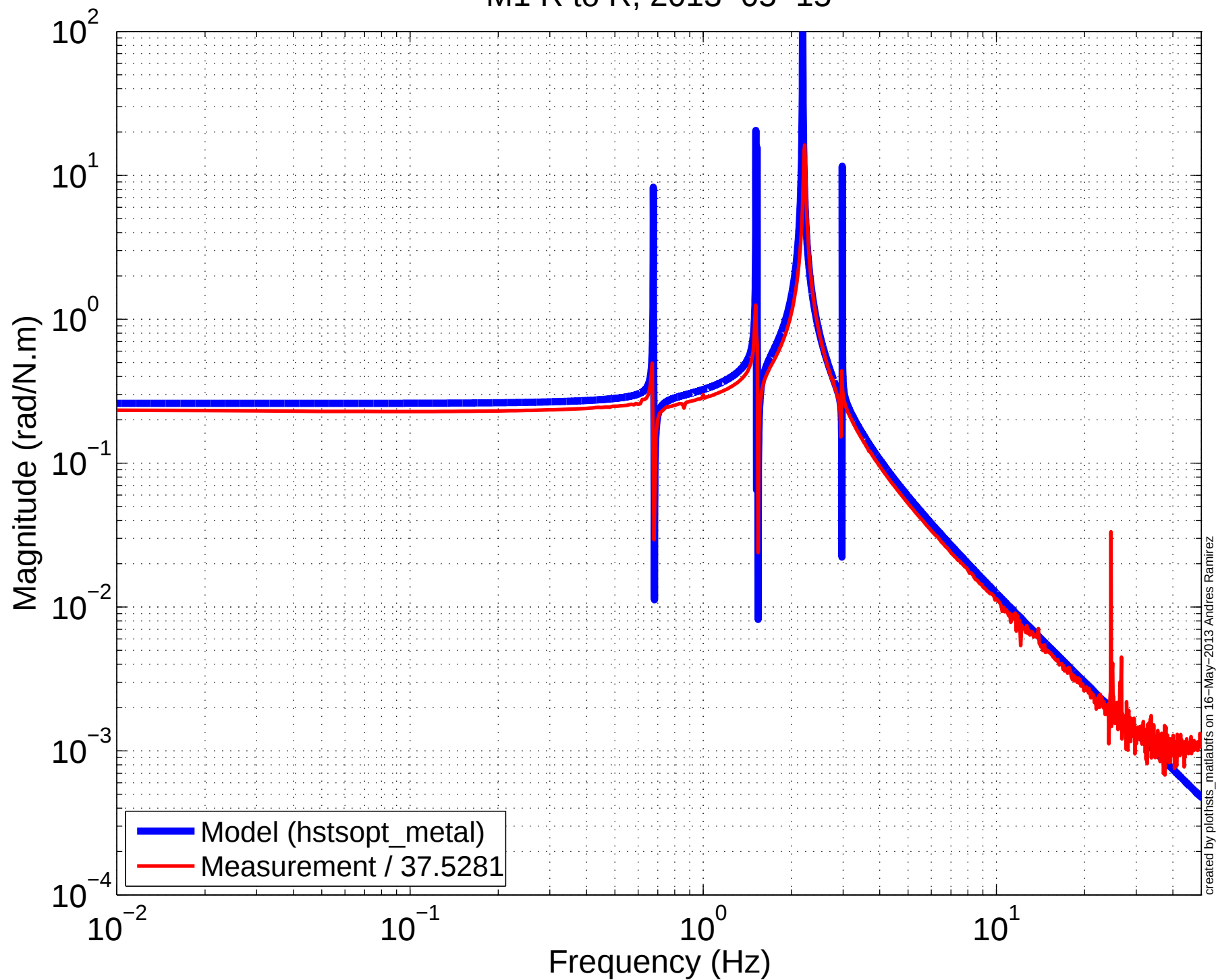
X1SUSSR2
M1 T to T, 2013-05-15



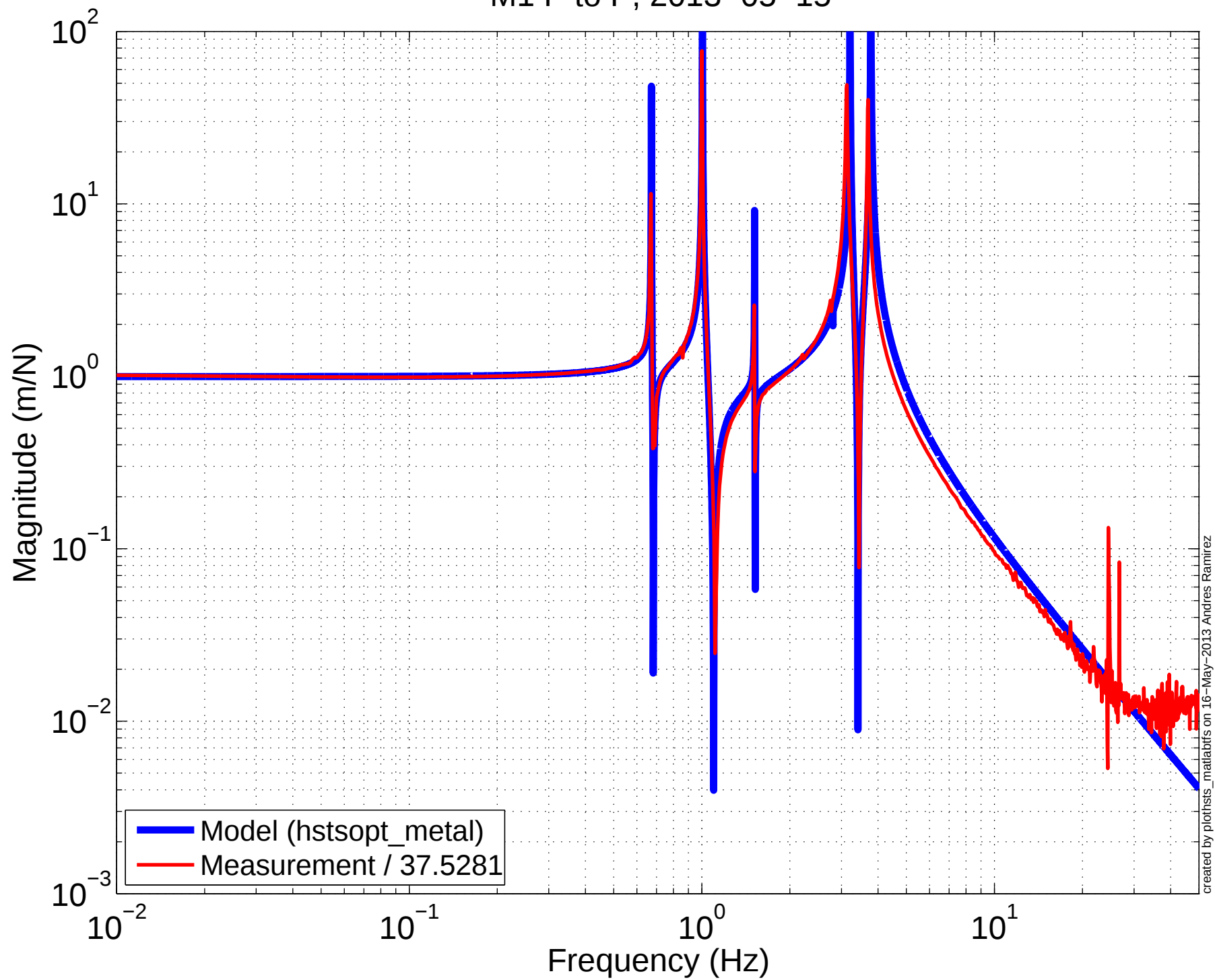
X1SUSSR2
M1 V to V, 2013-05-15



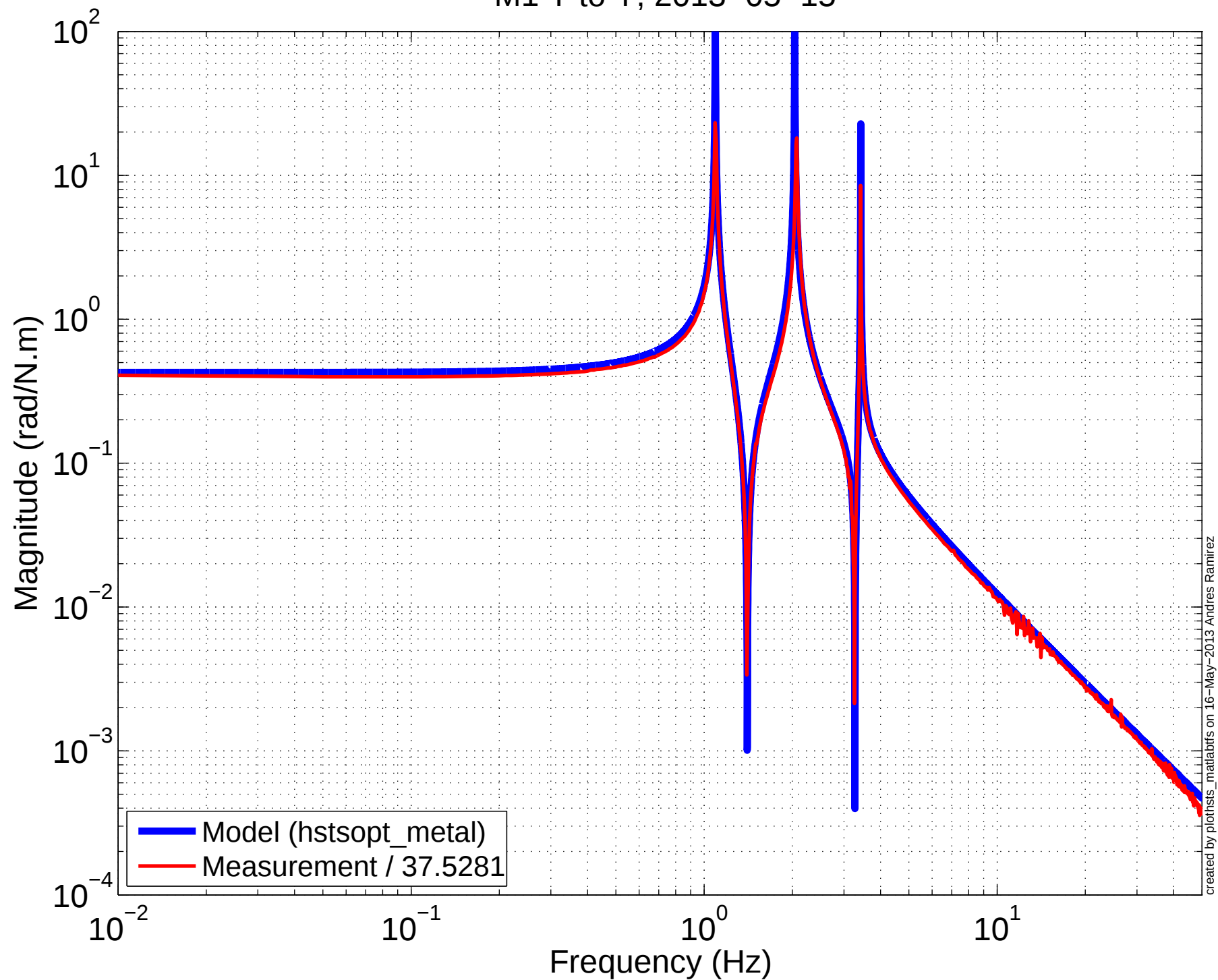
X1SUSSR2
M1 R to R, 2013-05-15



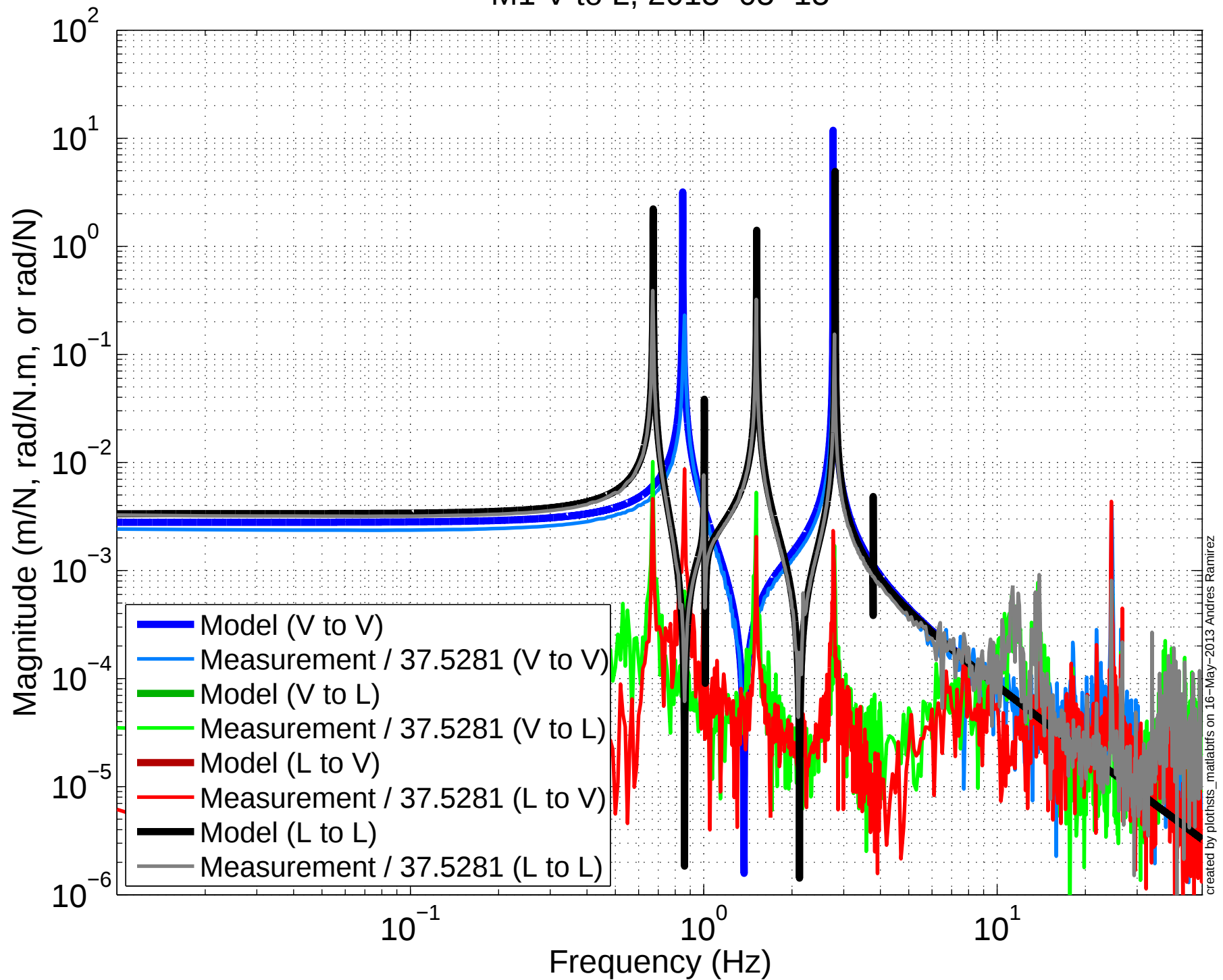
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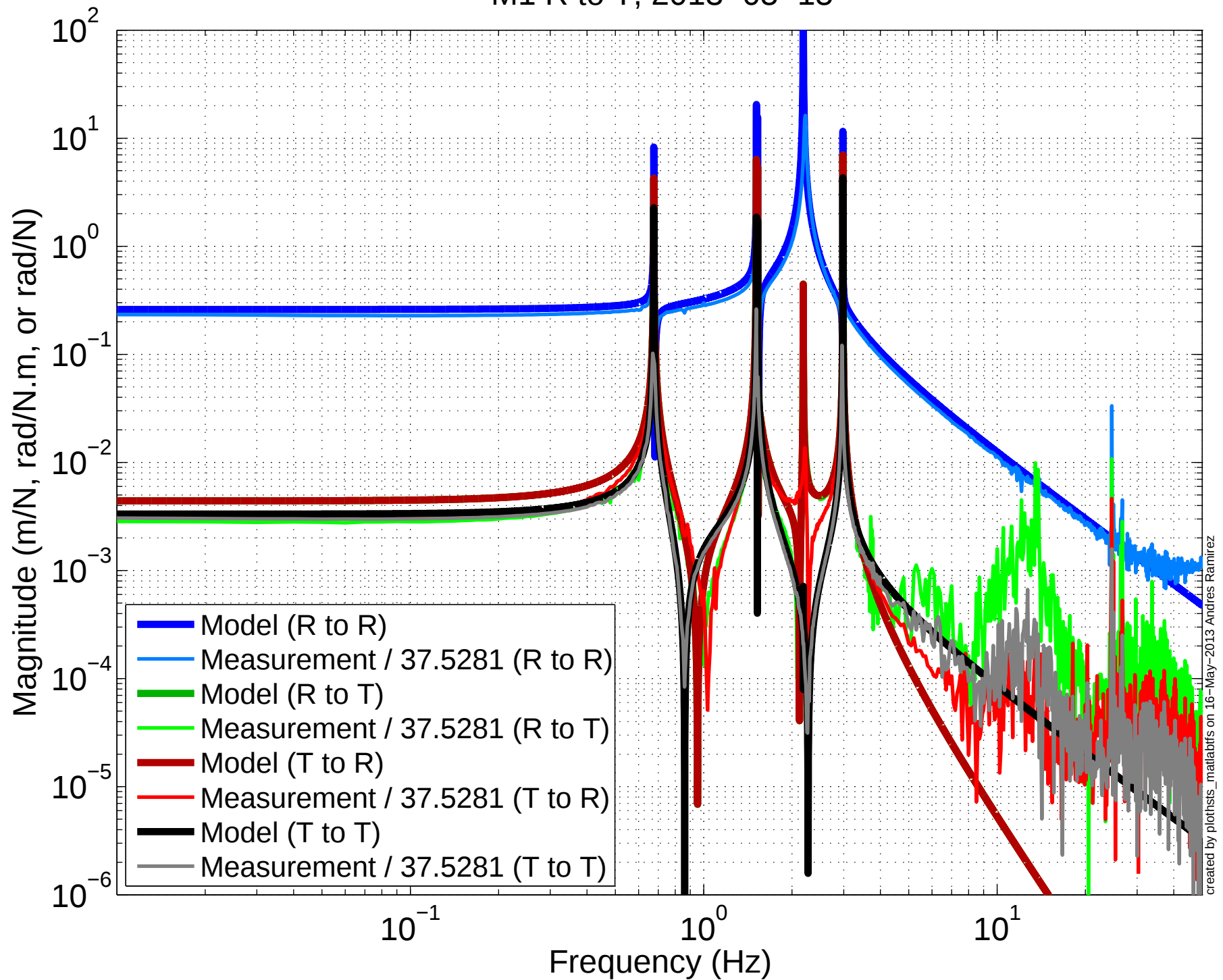
X1SUSSR2
M1 Y to Y, 2013-05-15



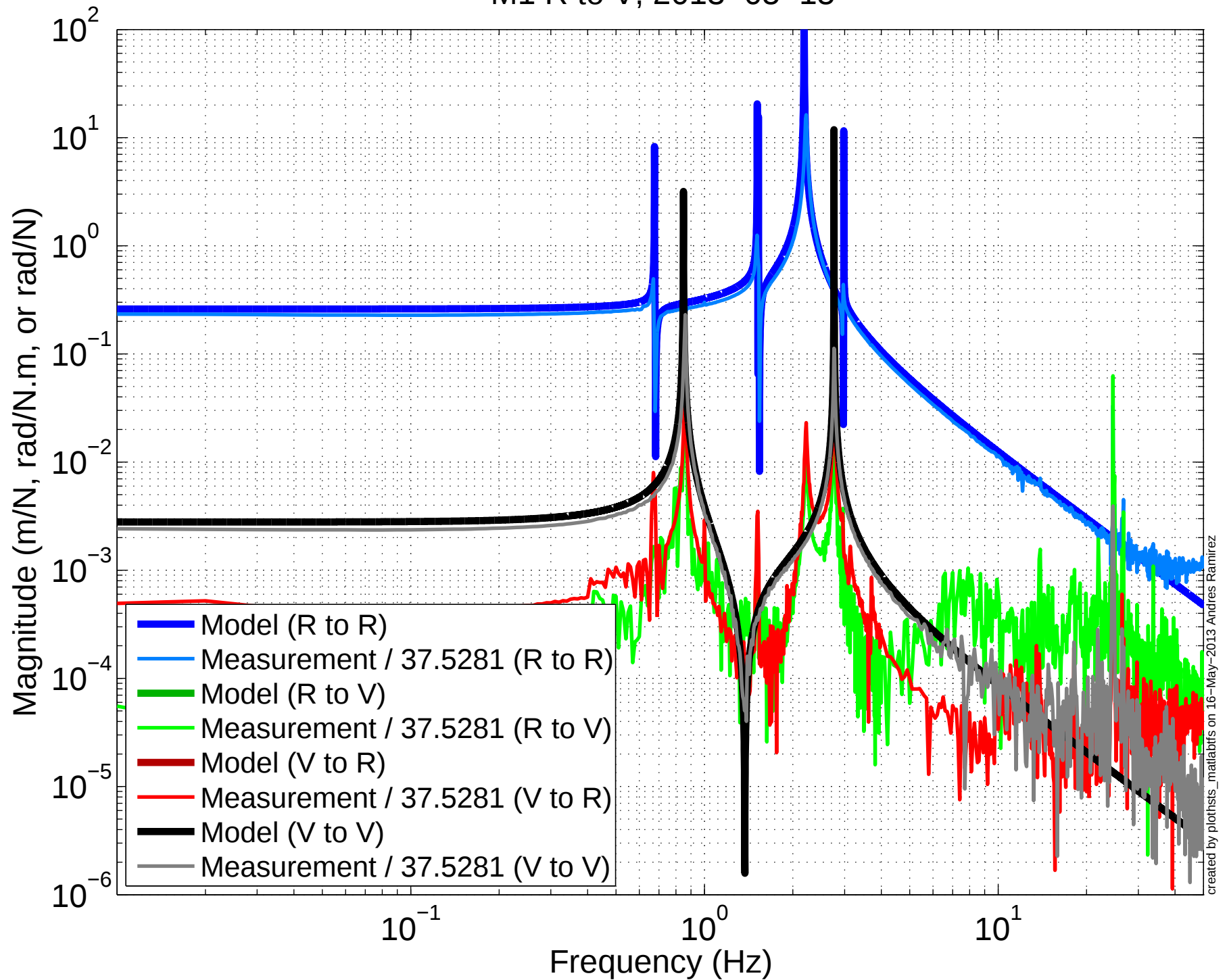
X1SUSSR2
M1 V to L, 2013-05-15



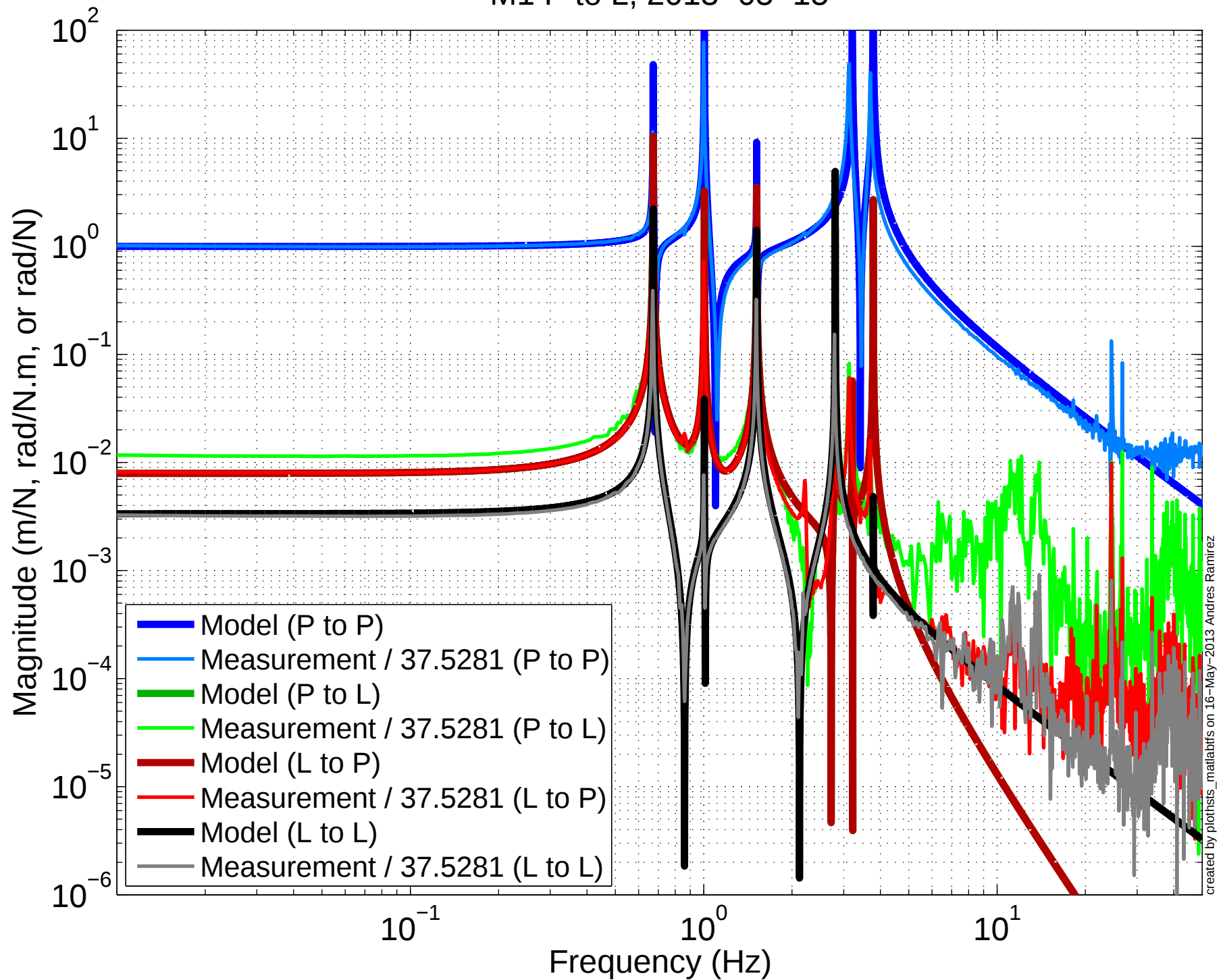
X1SUSSR2
M1 R to T, 2013-05-15



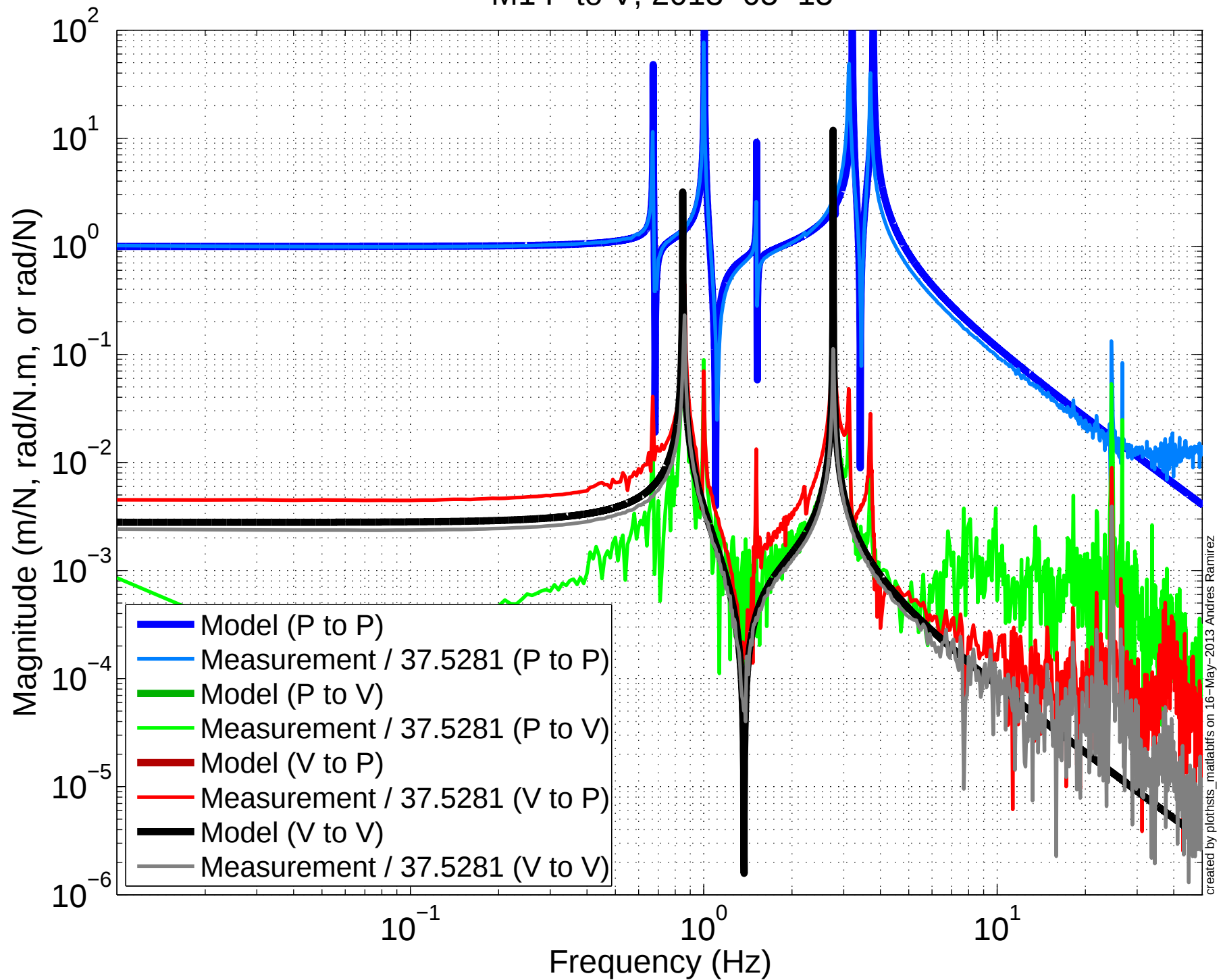
X1SUSSR2
M1 R to V, 2013-05-15



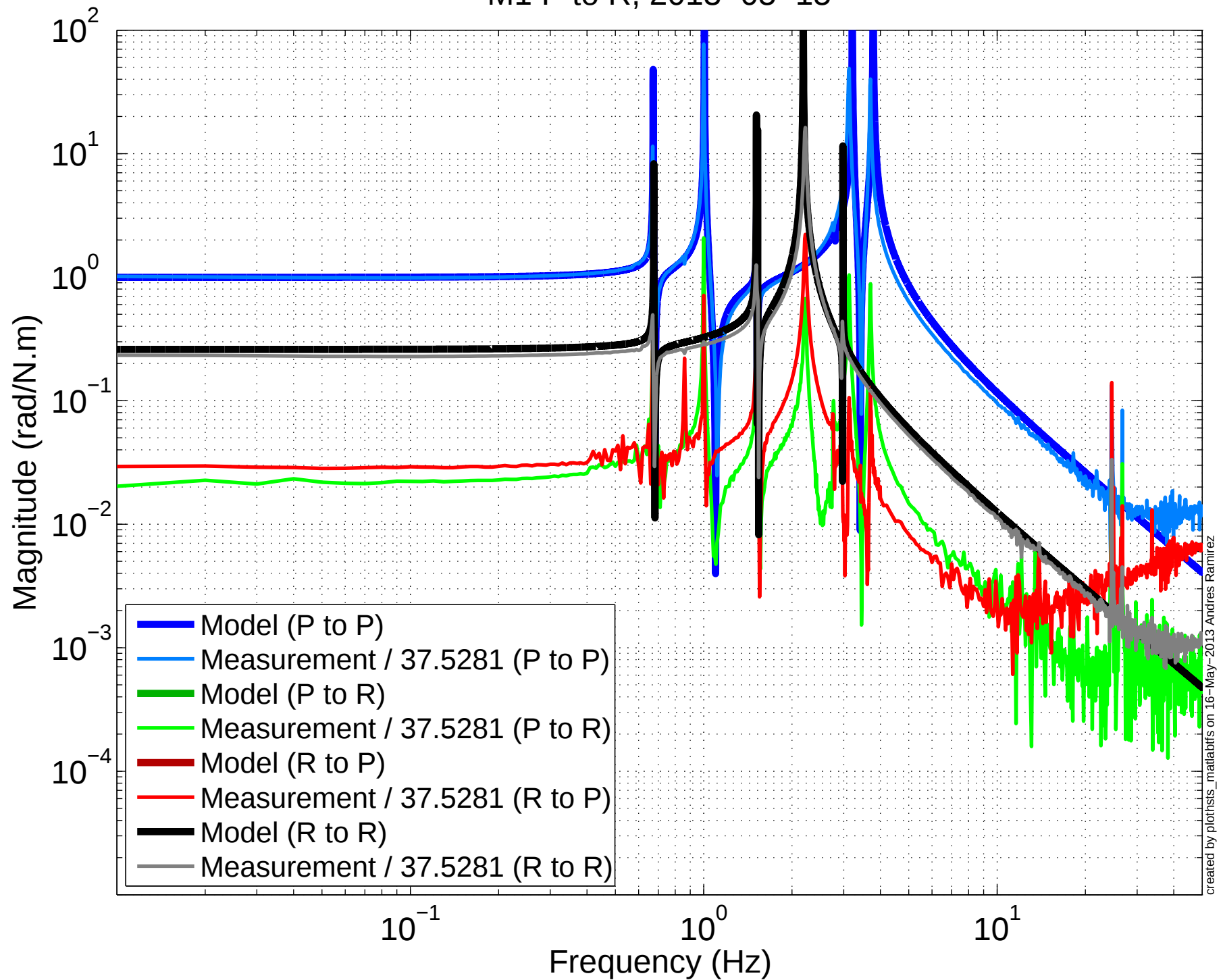
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M1 P to L, 2013-05-15



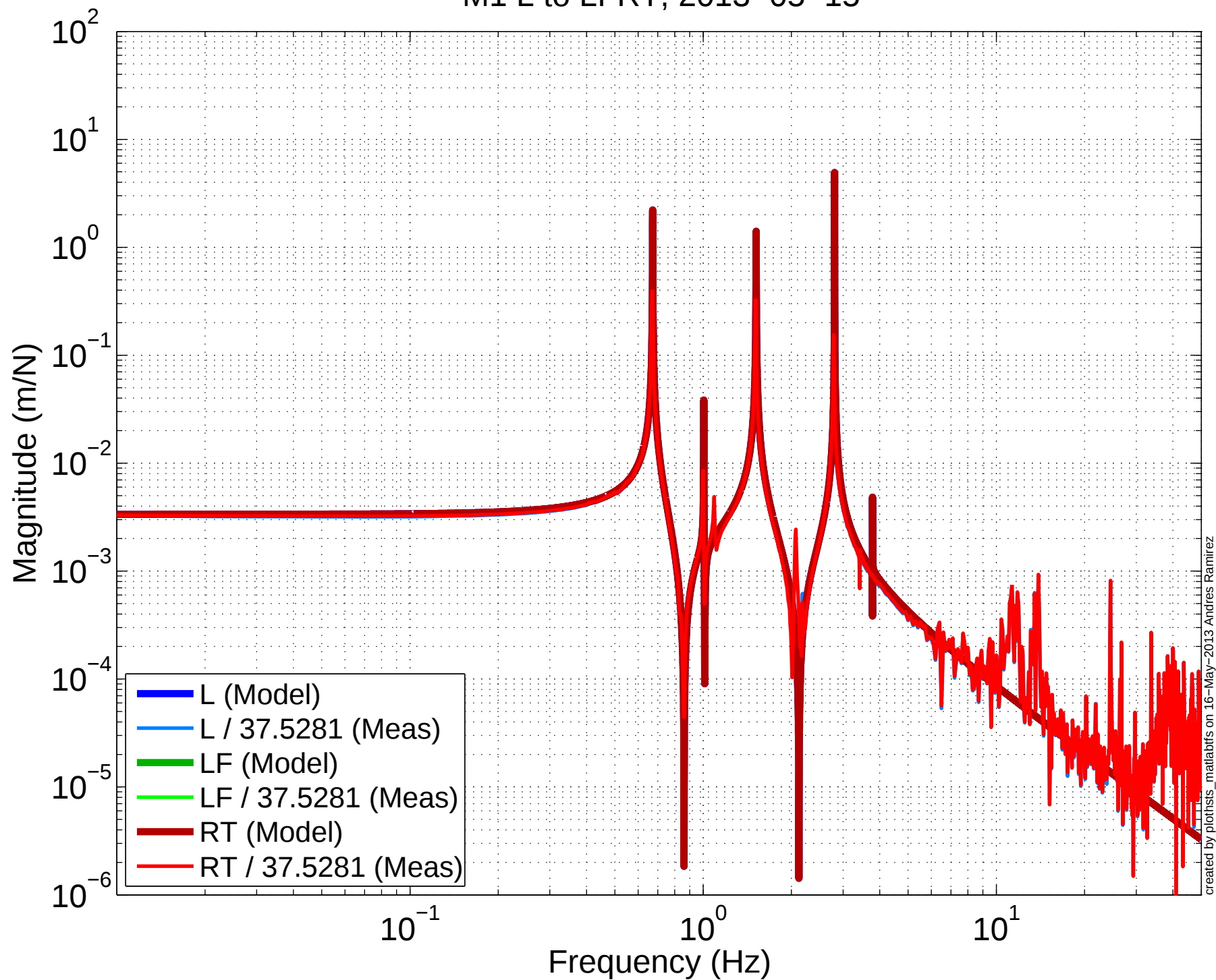
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M1 P to V, 2013-05-15



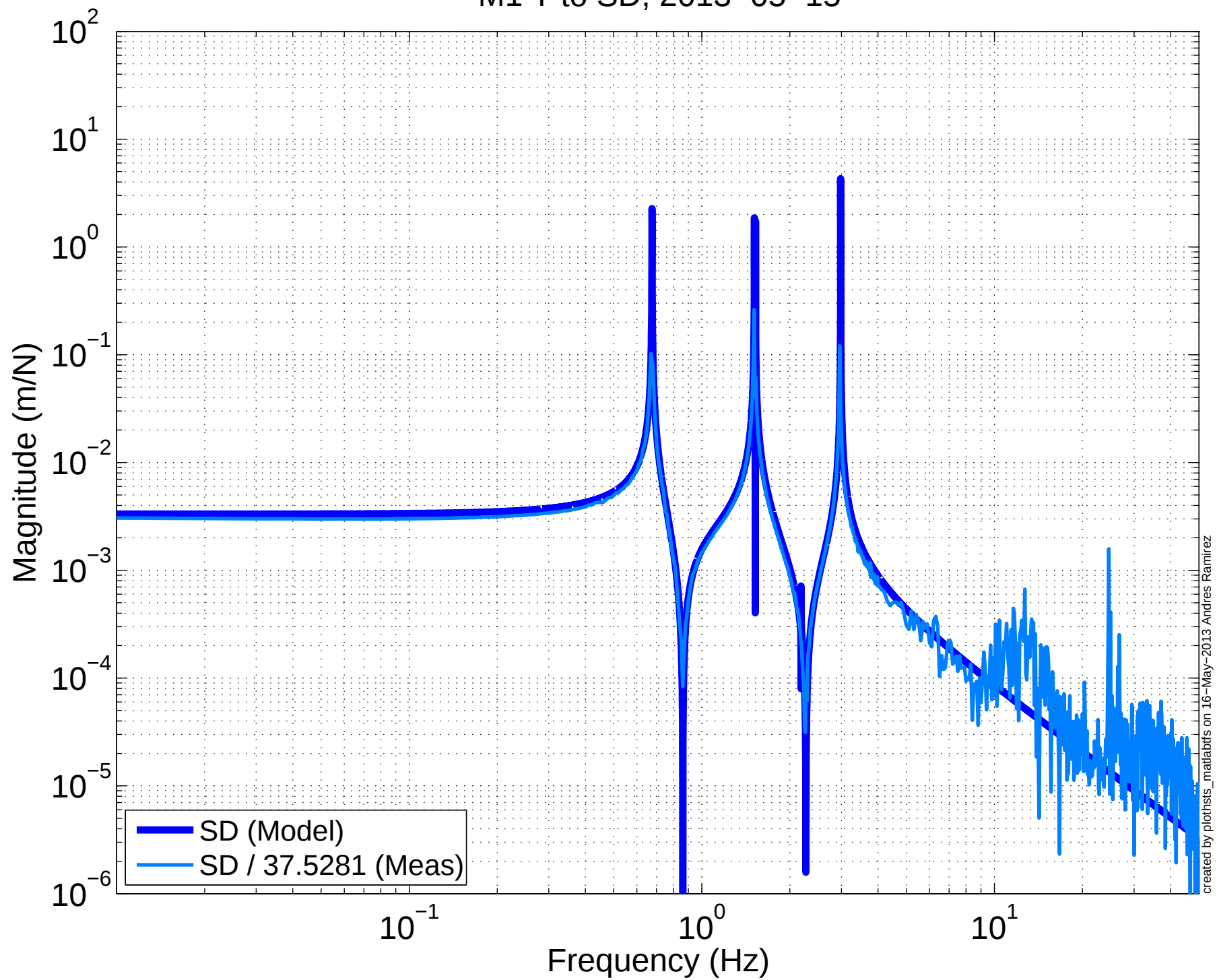
X1SUSSR2
M1 P to R, 2013-05-15



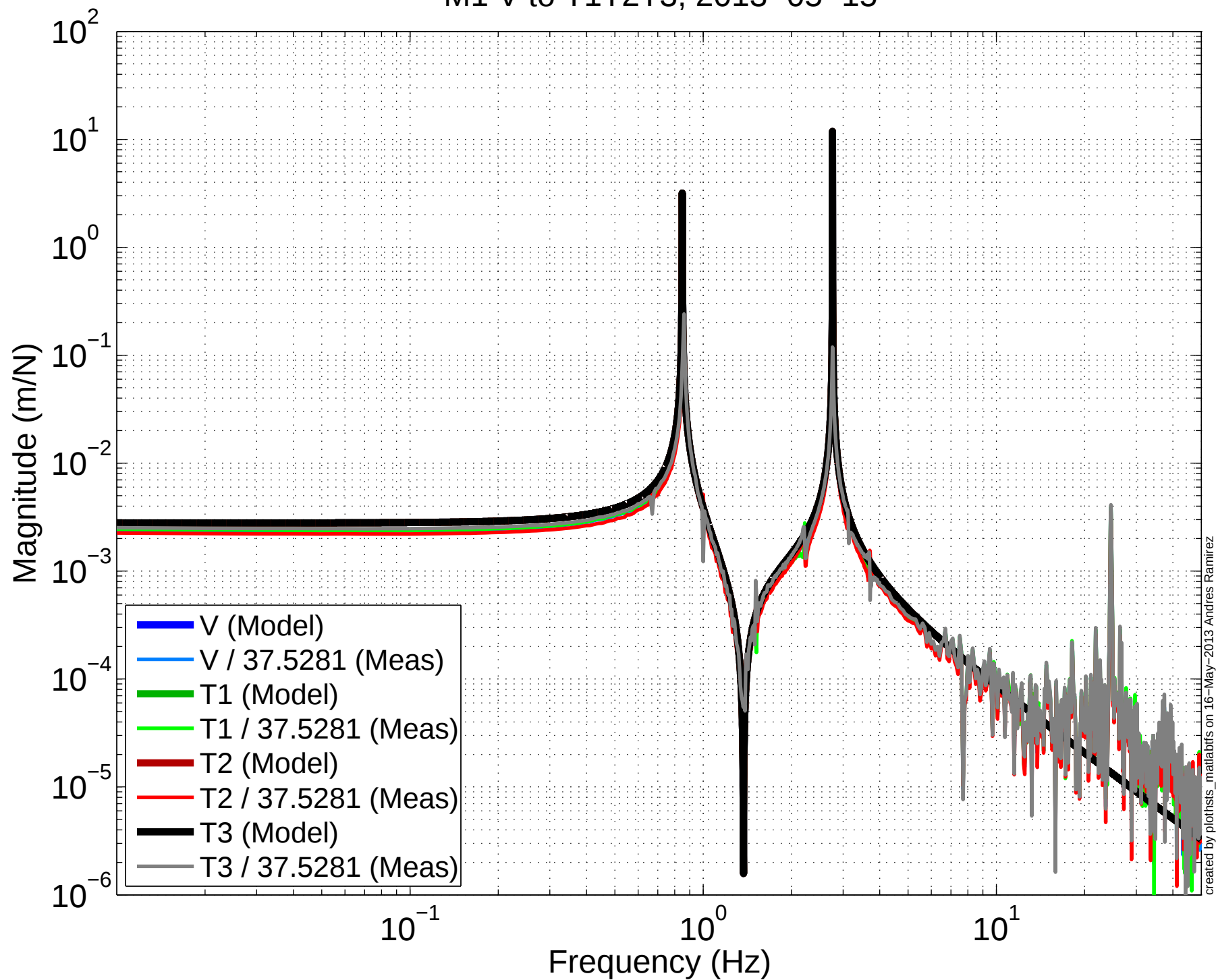
X1SUSSR2
M1 L to LFRT, 2013-05-15



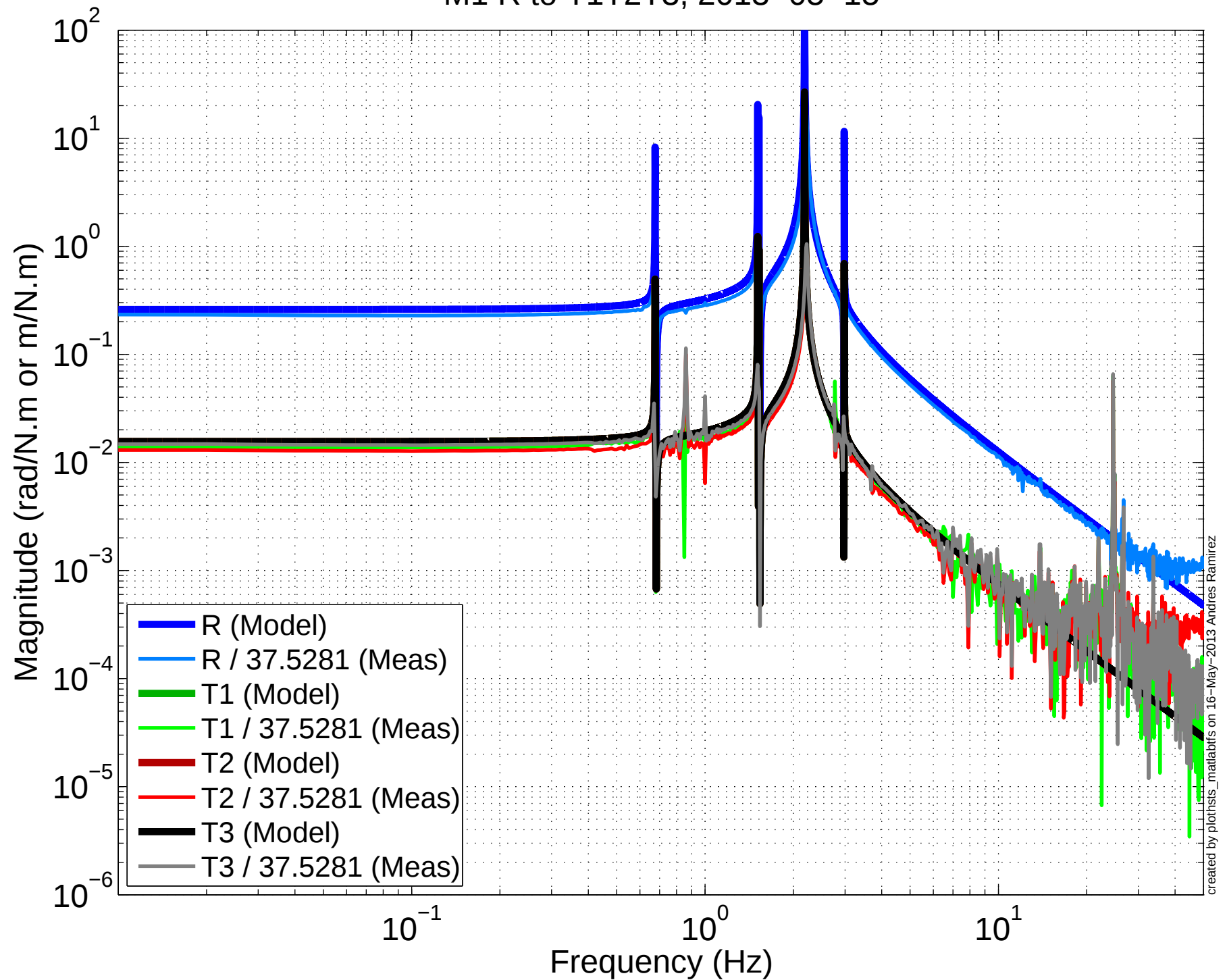
X1SUSSR2
M1 T to SD, 2013-05-15



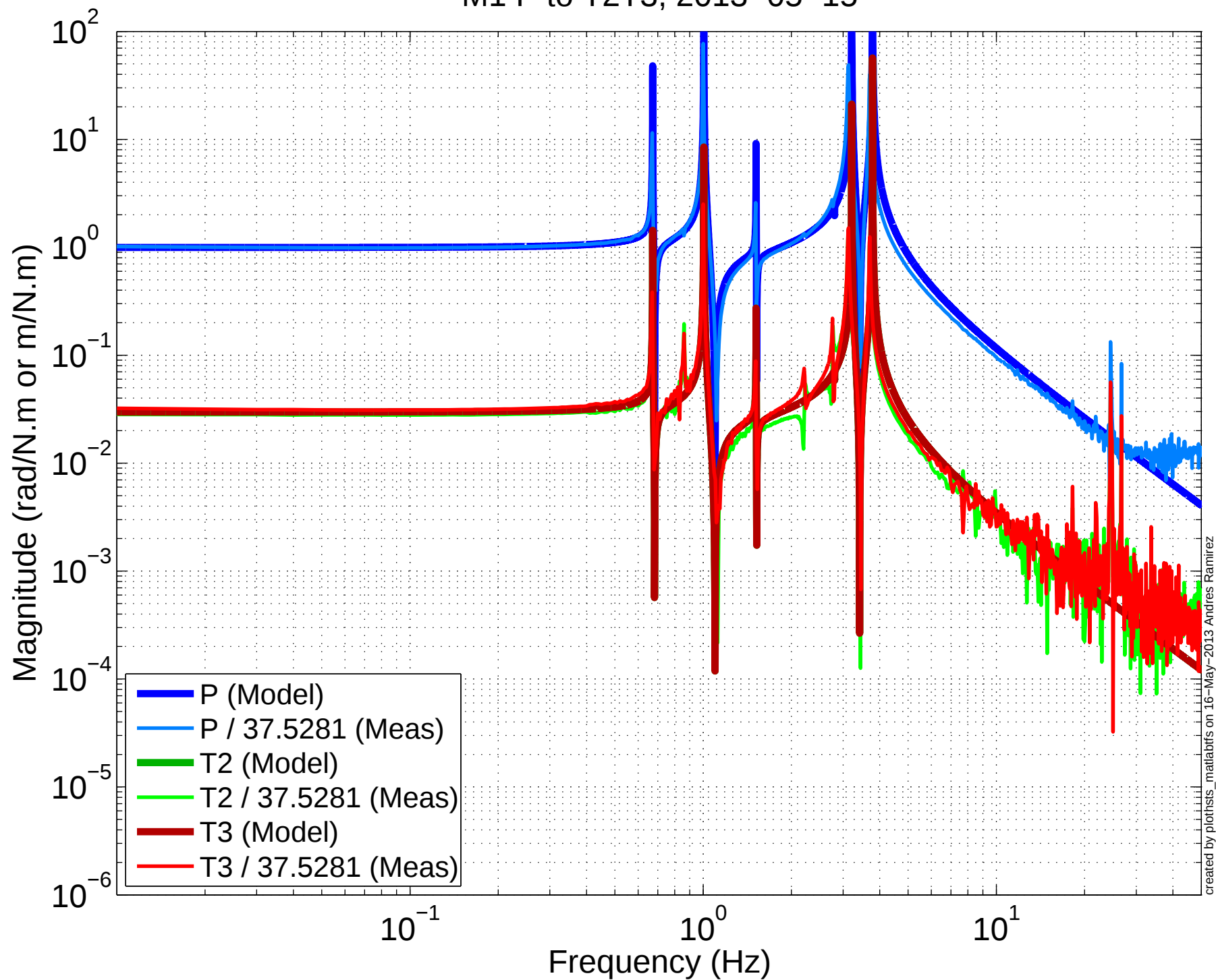
X1SUSSR2
M1 V to T1T2T3, 2013-05-15



X1SUSSR2
M1 R to T1T2T3, 2013-05-15



X1SUSSR2
M1 P to T2T3, 2013-05-15



X1SUSSR2
M1 Y to LFRT, 2013-05-15

