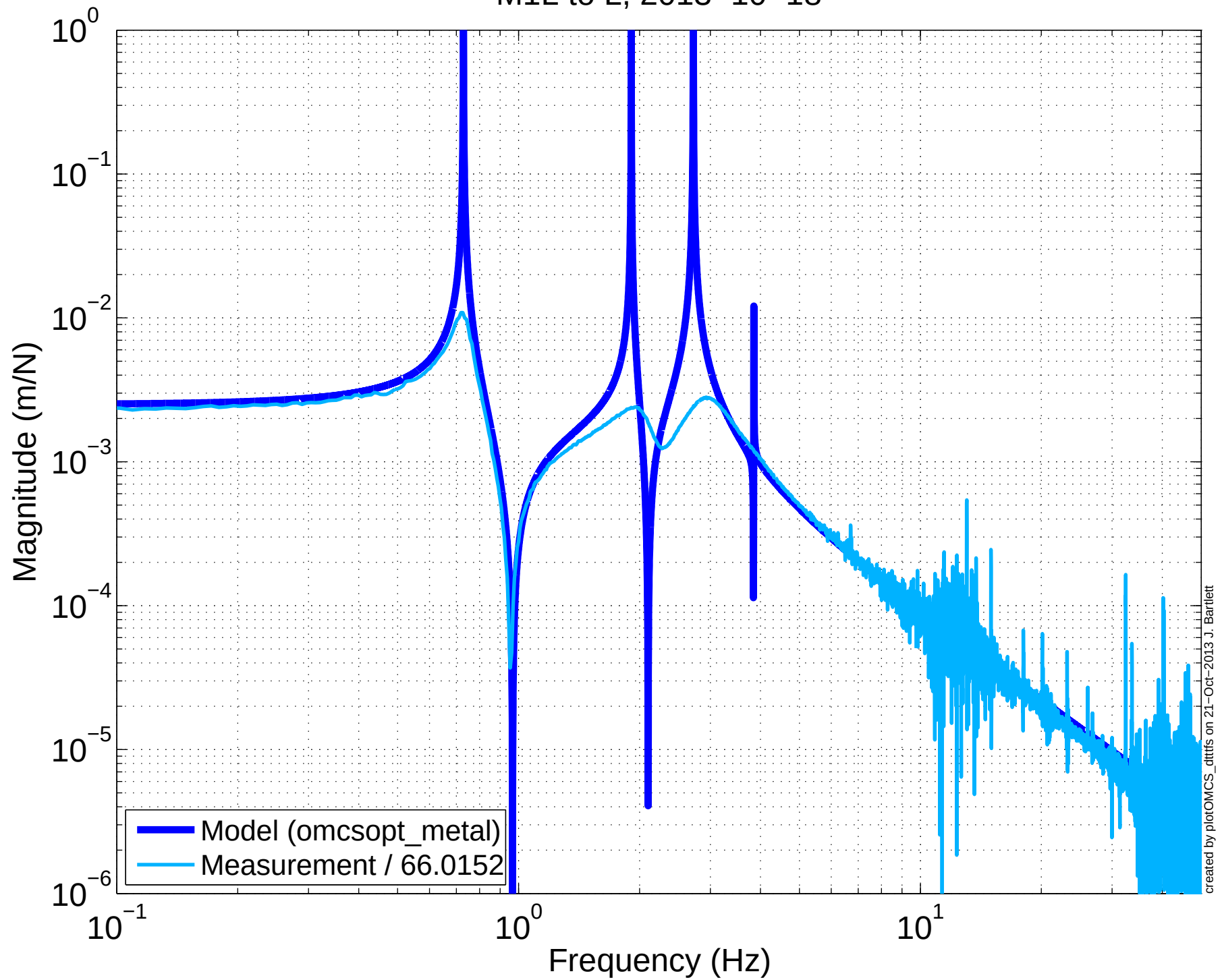
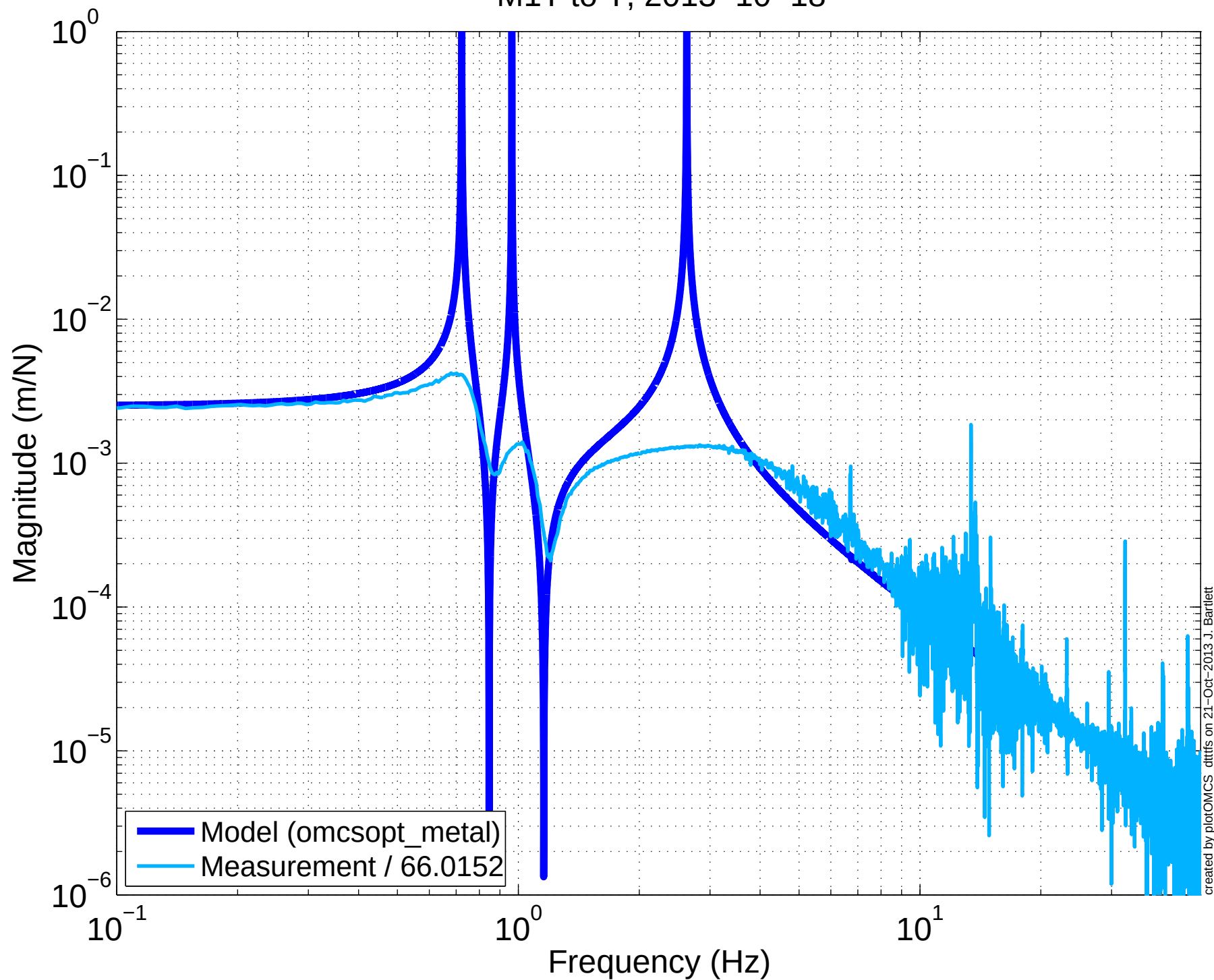


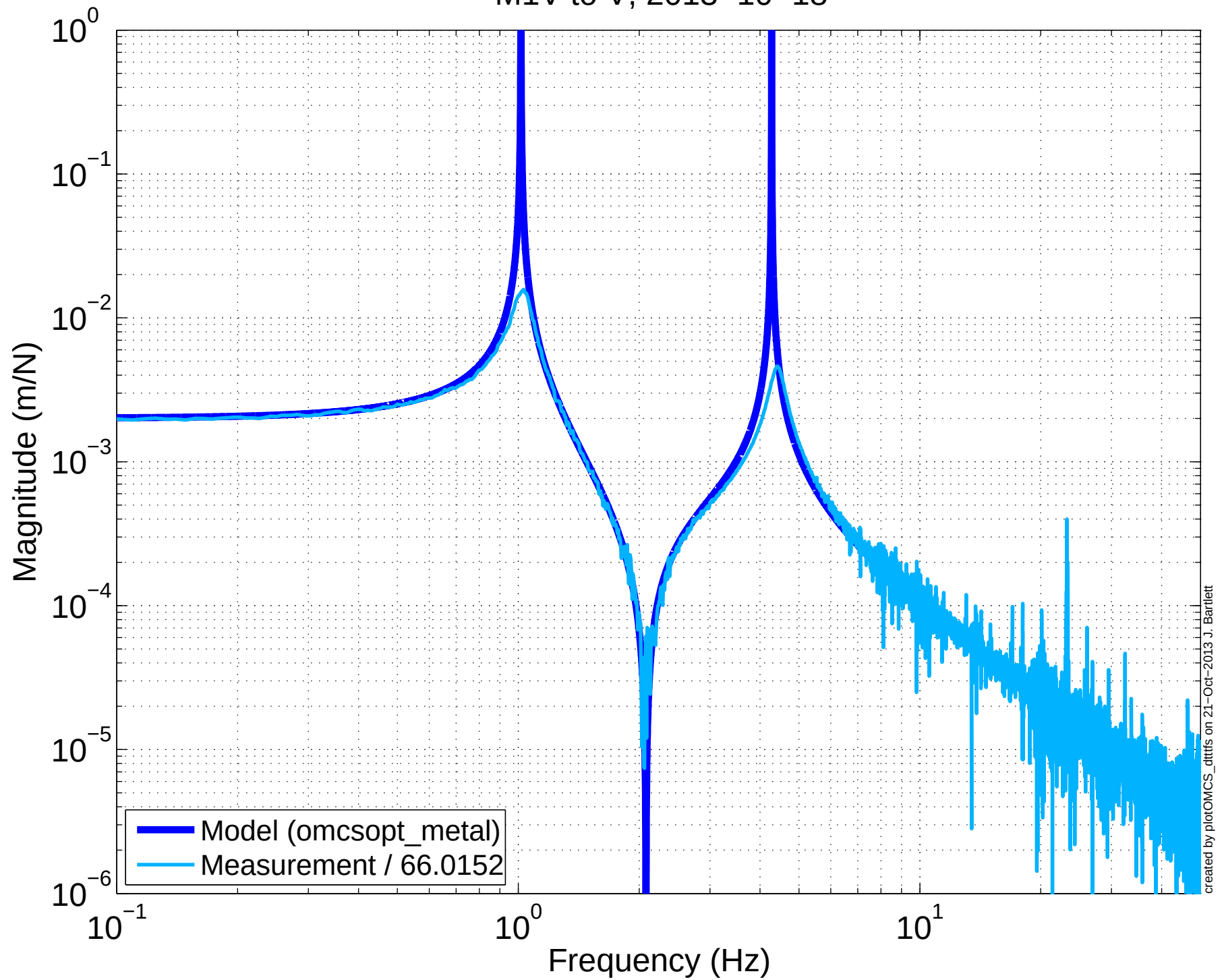
X1SUSOMC BUILD
M1L to L, 2013-10-18



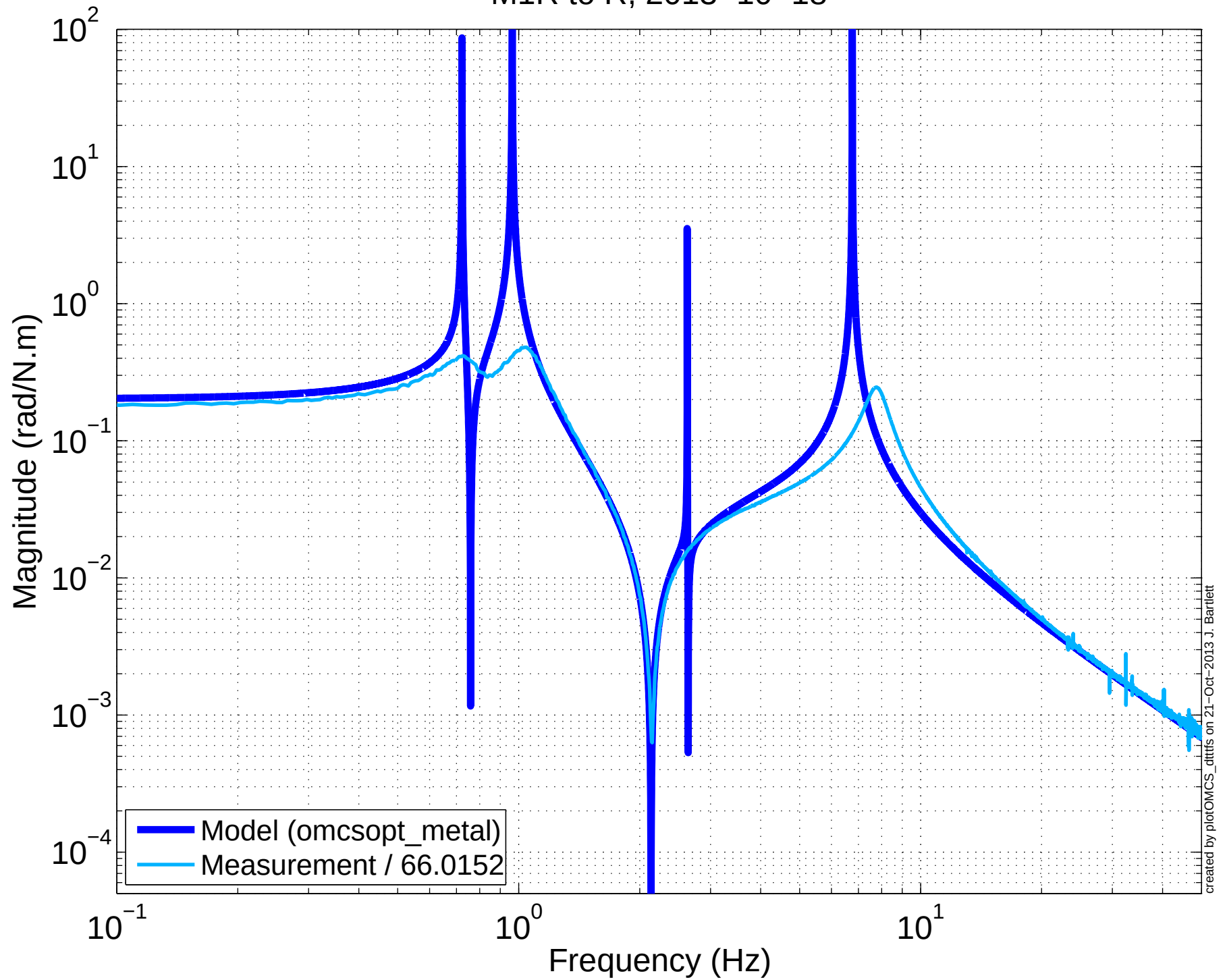
X1SUSOMC BUILD
M1T to T, 2013-10-18



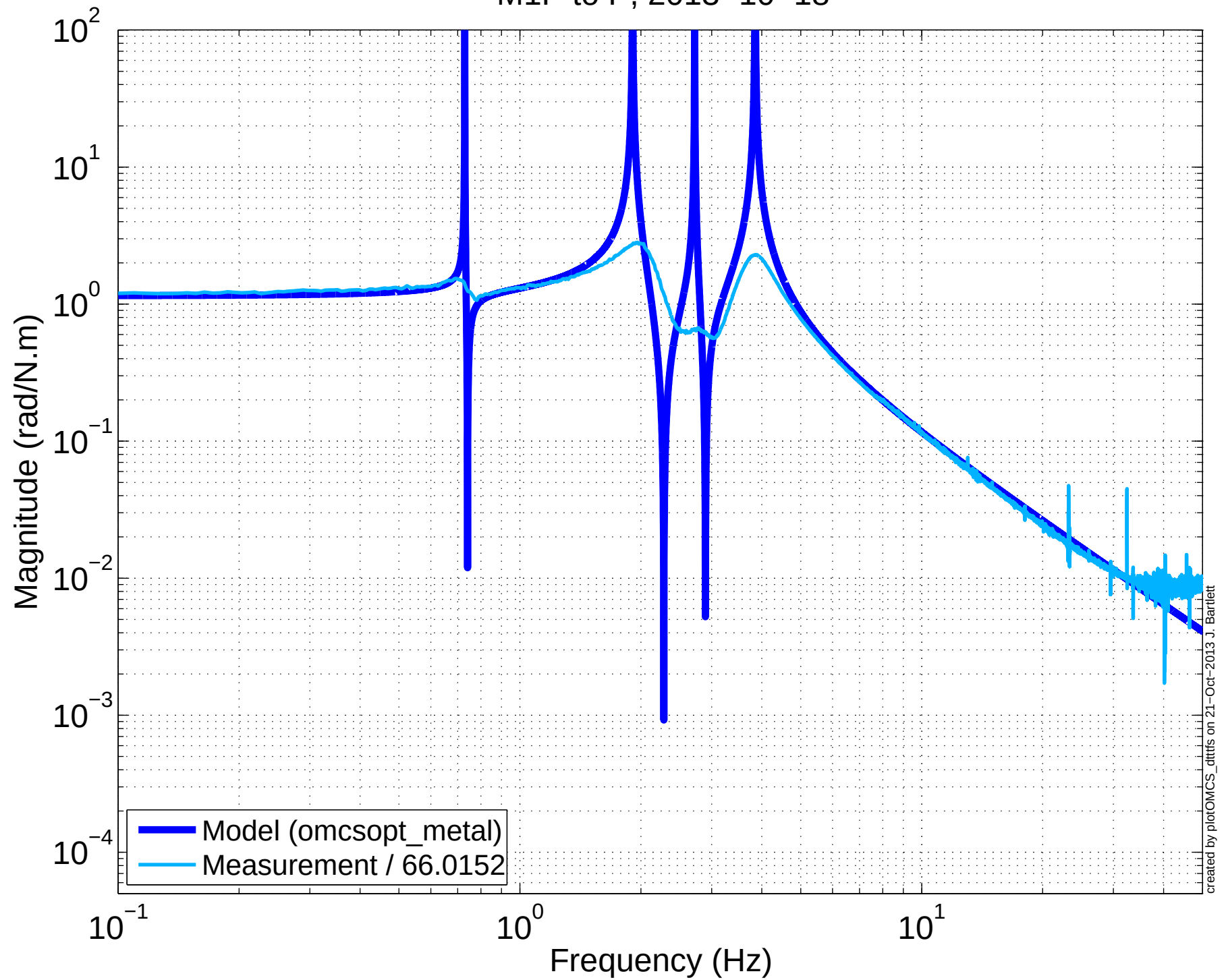
X1SUSOMC BUILD
M1V to V, 2013-10-18



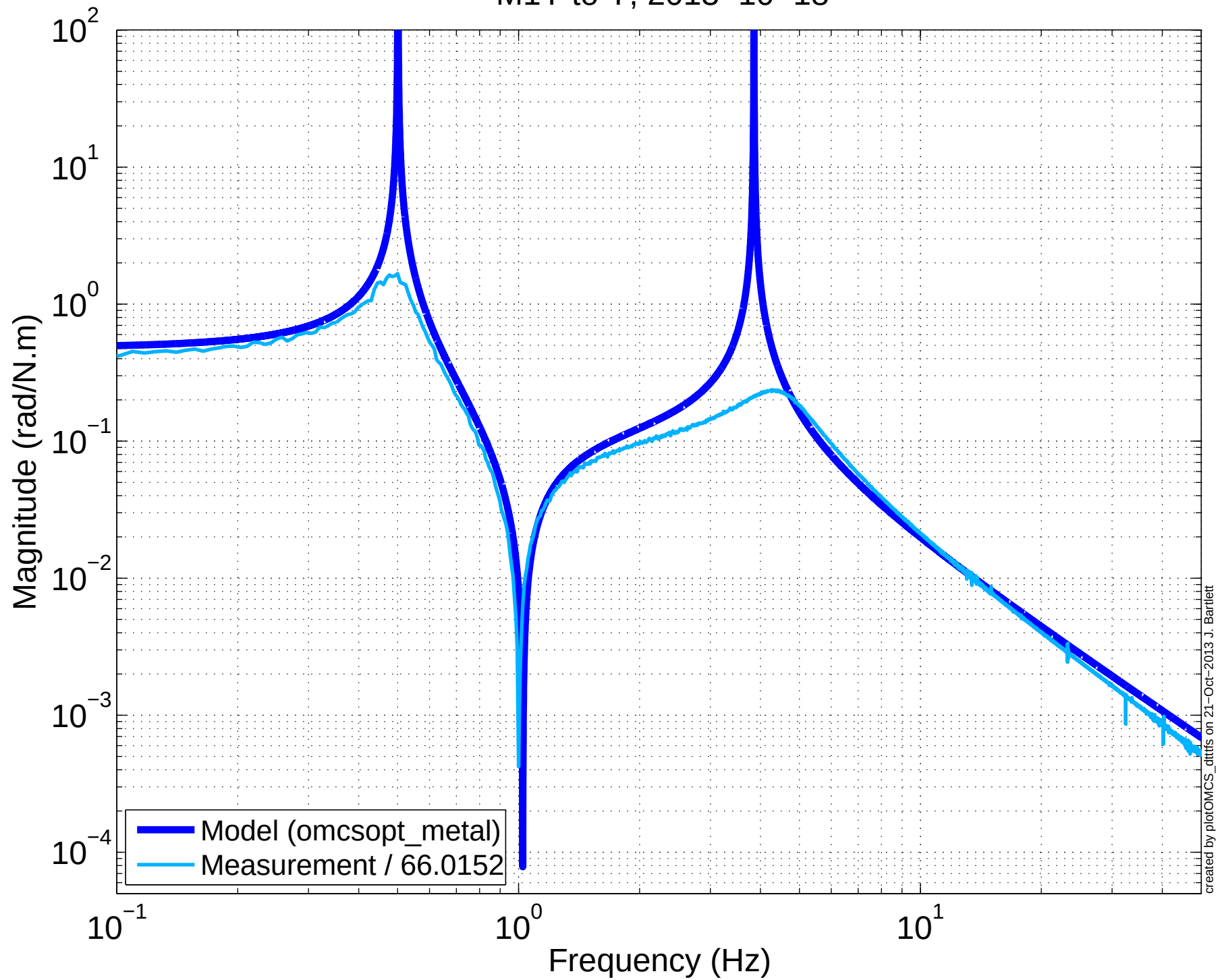
X1SUSOMC BUILD
M1R to R, 2013-10-18



X1SUSOMC BUILD
M1P to P, 2013-10-18

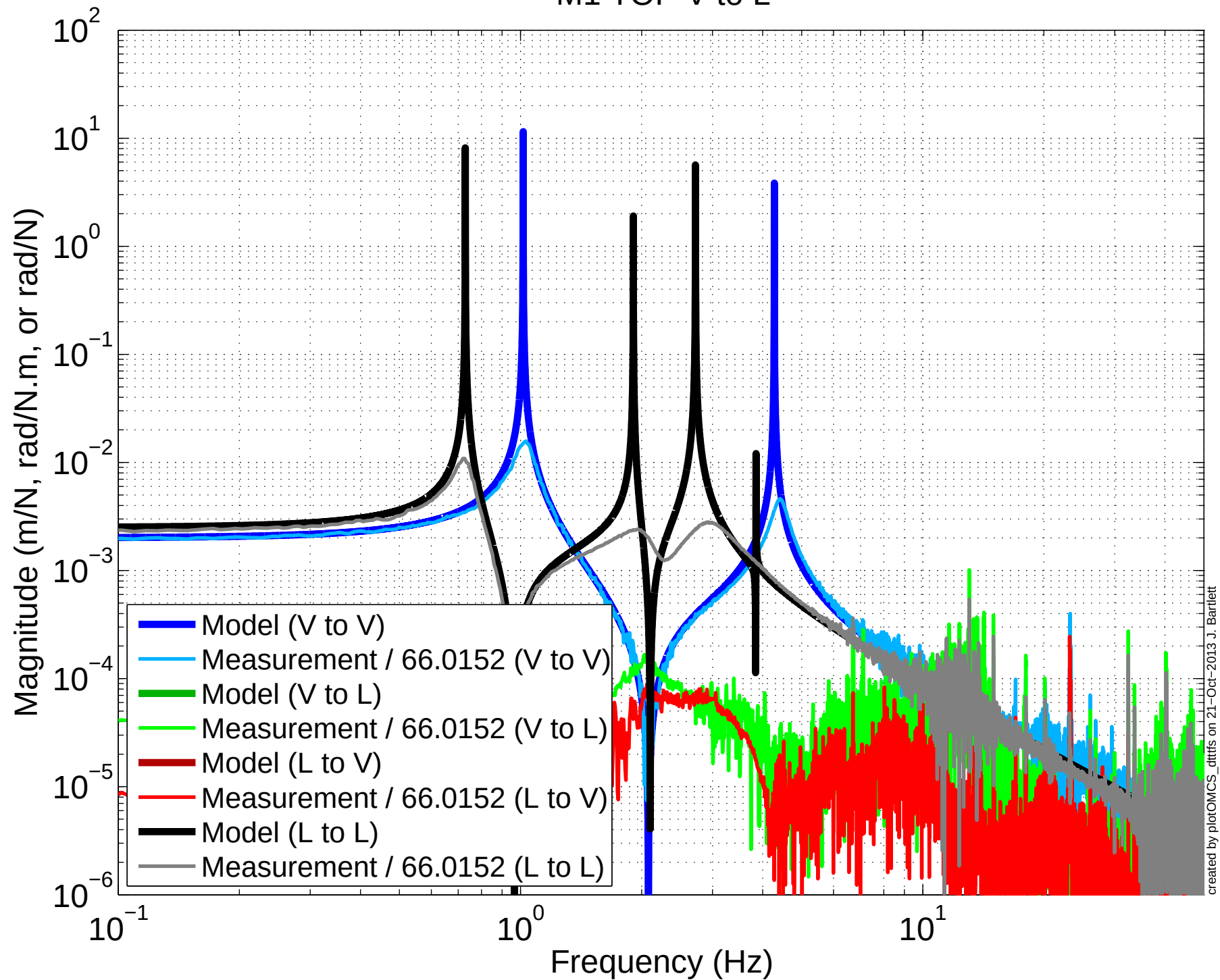


X1SUSOMC BUILD
M1Y to Y, 2013-10-18



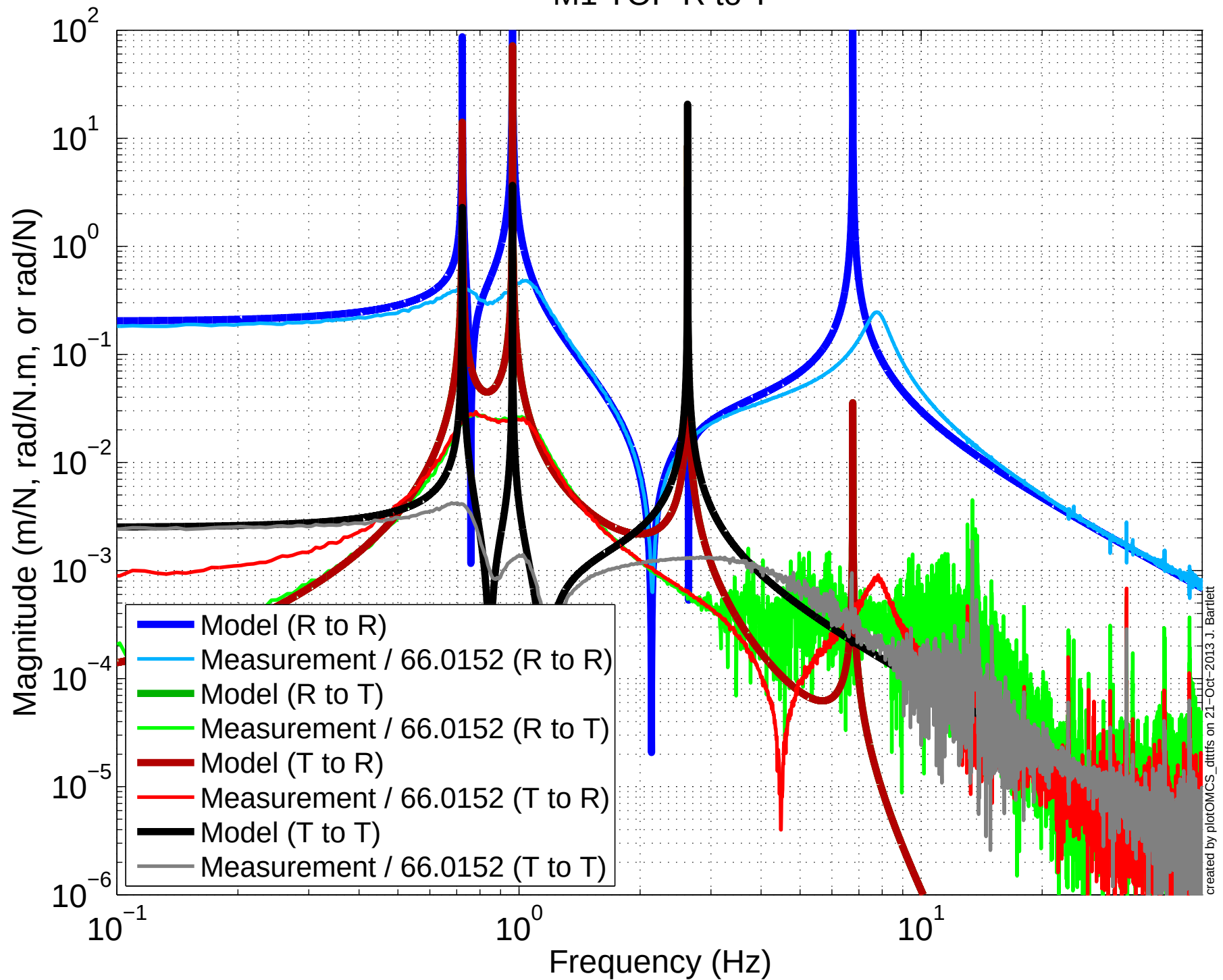
X1SUSOMC BUILD

M1 TOP V to L

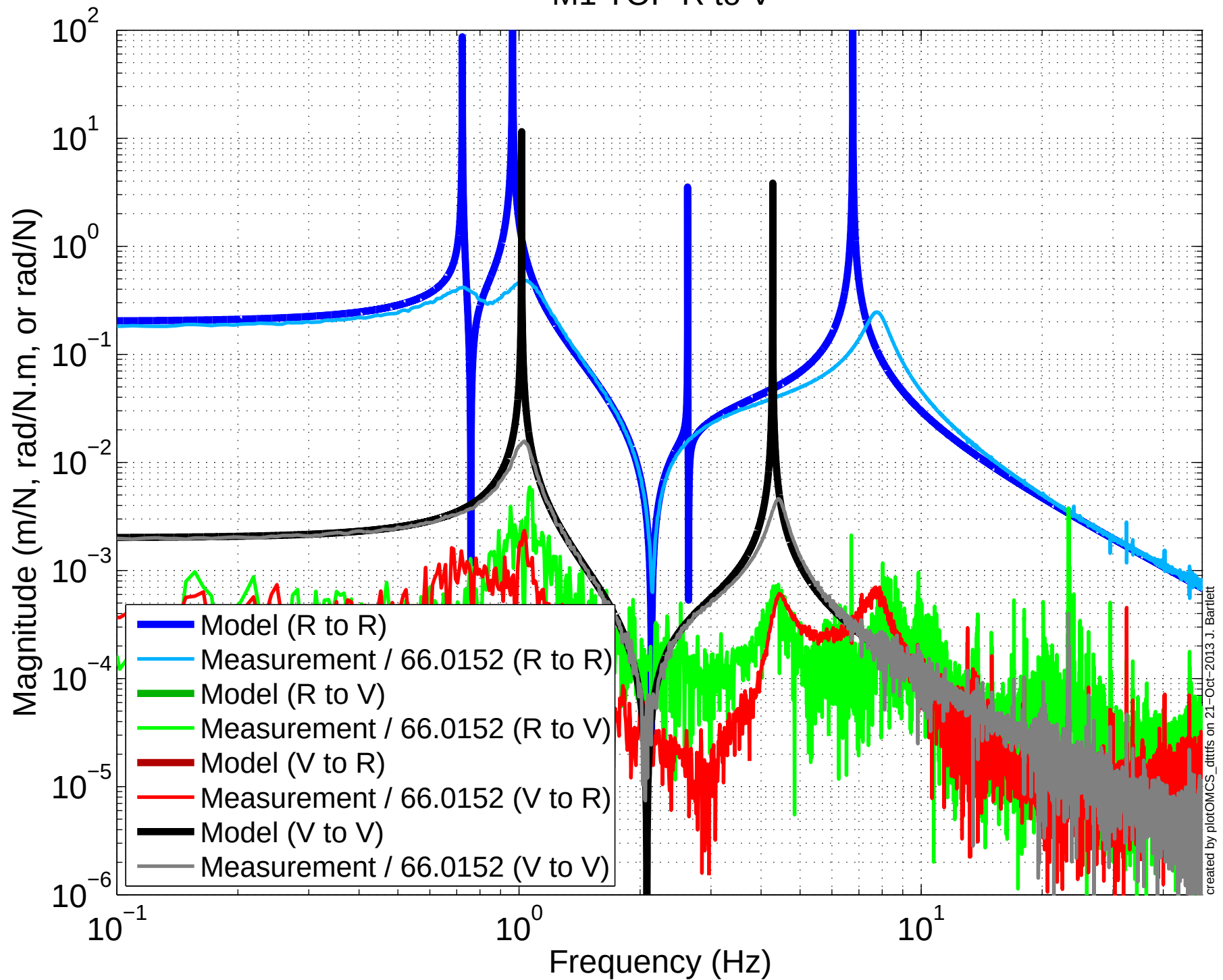


X1SUSOMC BUILD

M1 TOP R to T

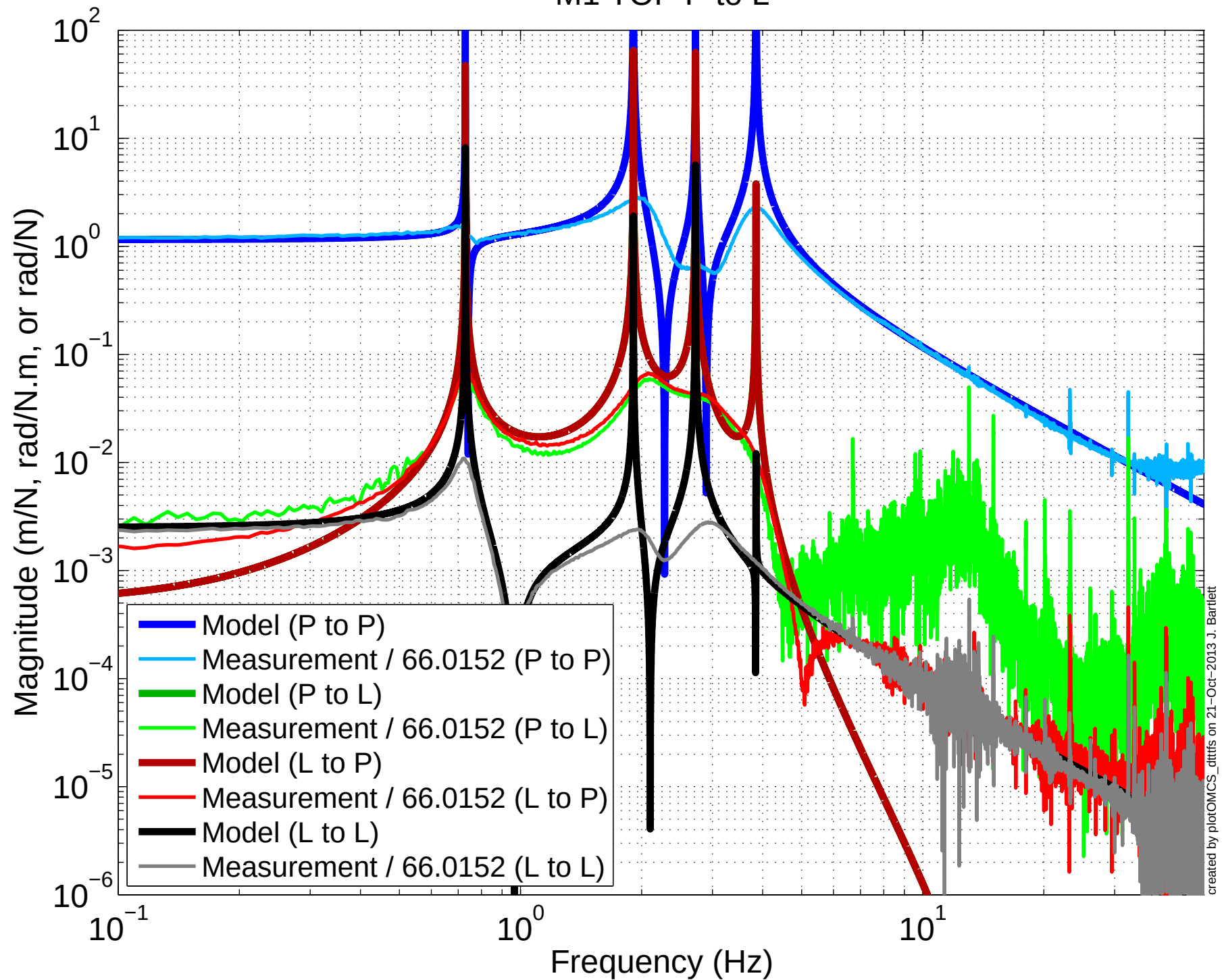


X1SUSOMC BUILD
M1 TOP R to V



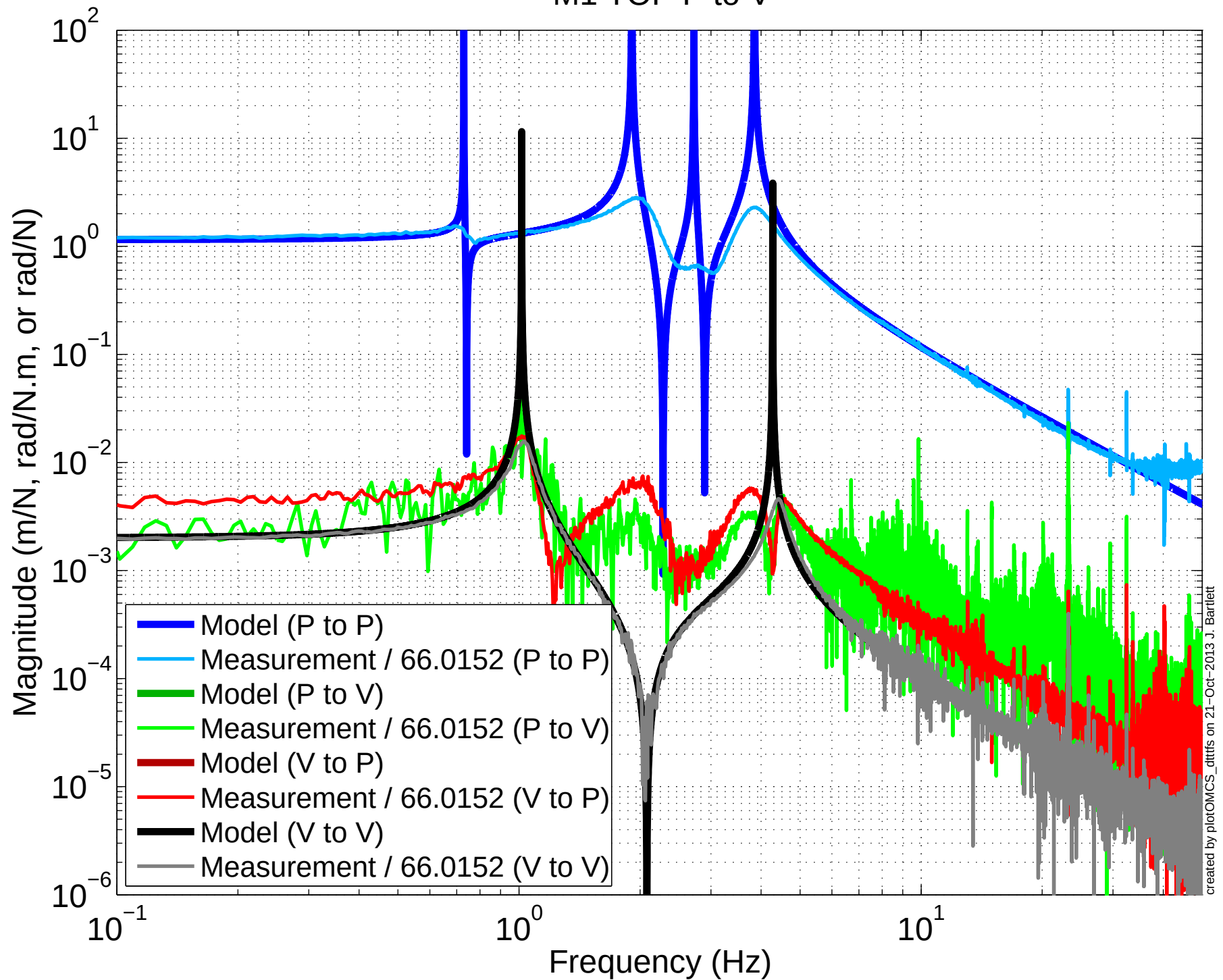
X1SUSOMC BUILD

M1 TOP P to L



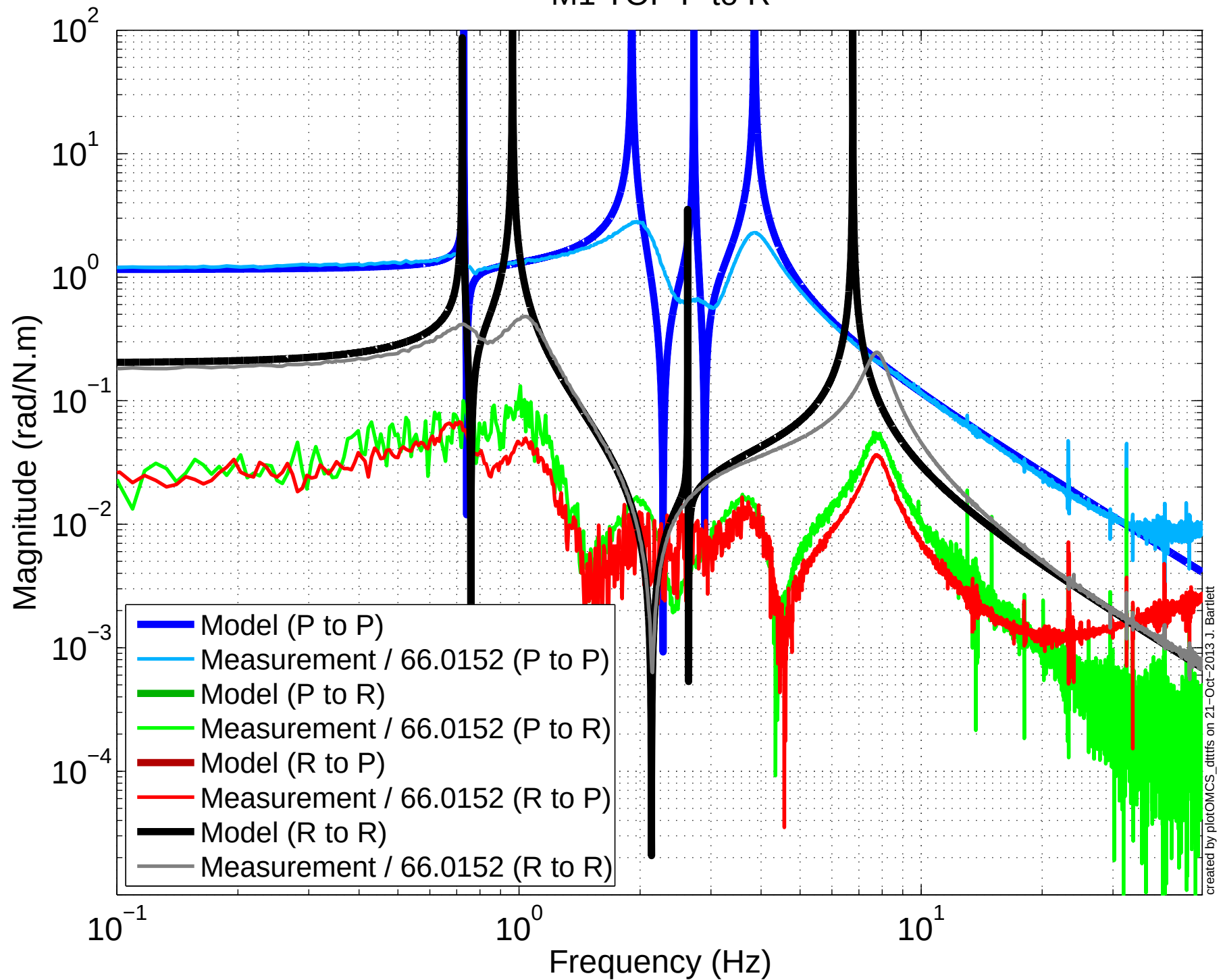
X1SUSOMC BUILD

M1 TOP P to V

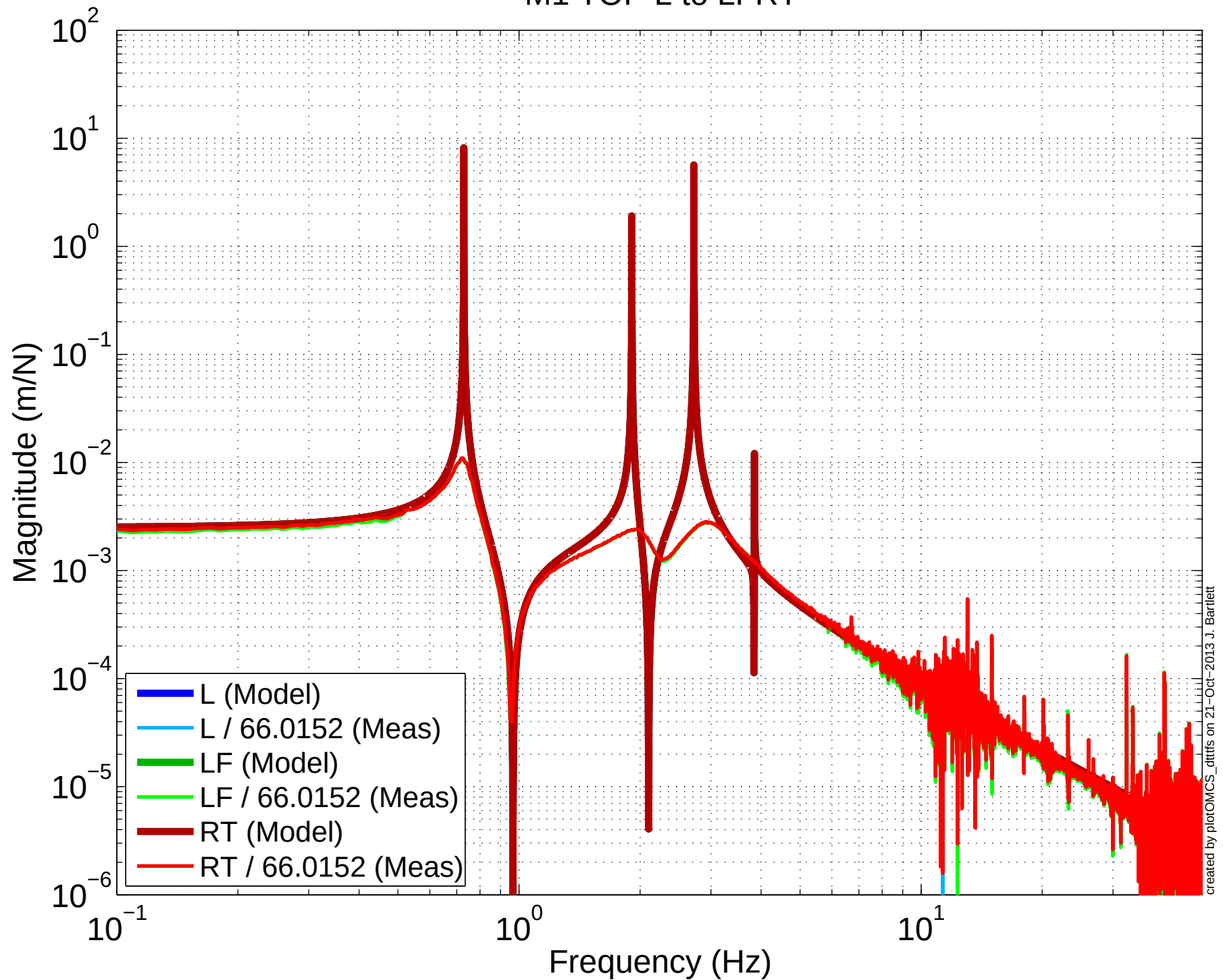


X1SUSOMC BUILD

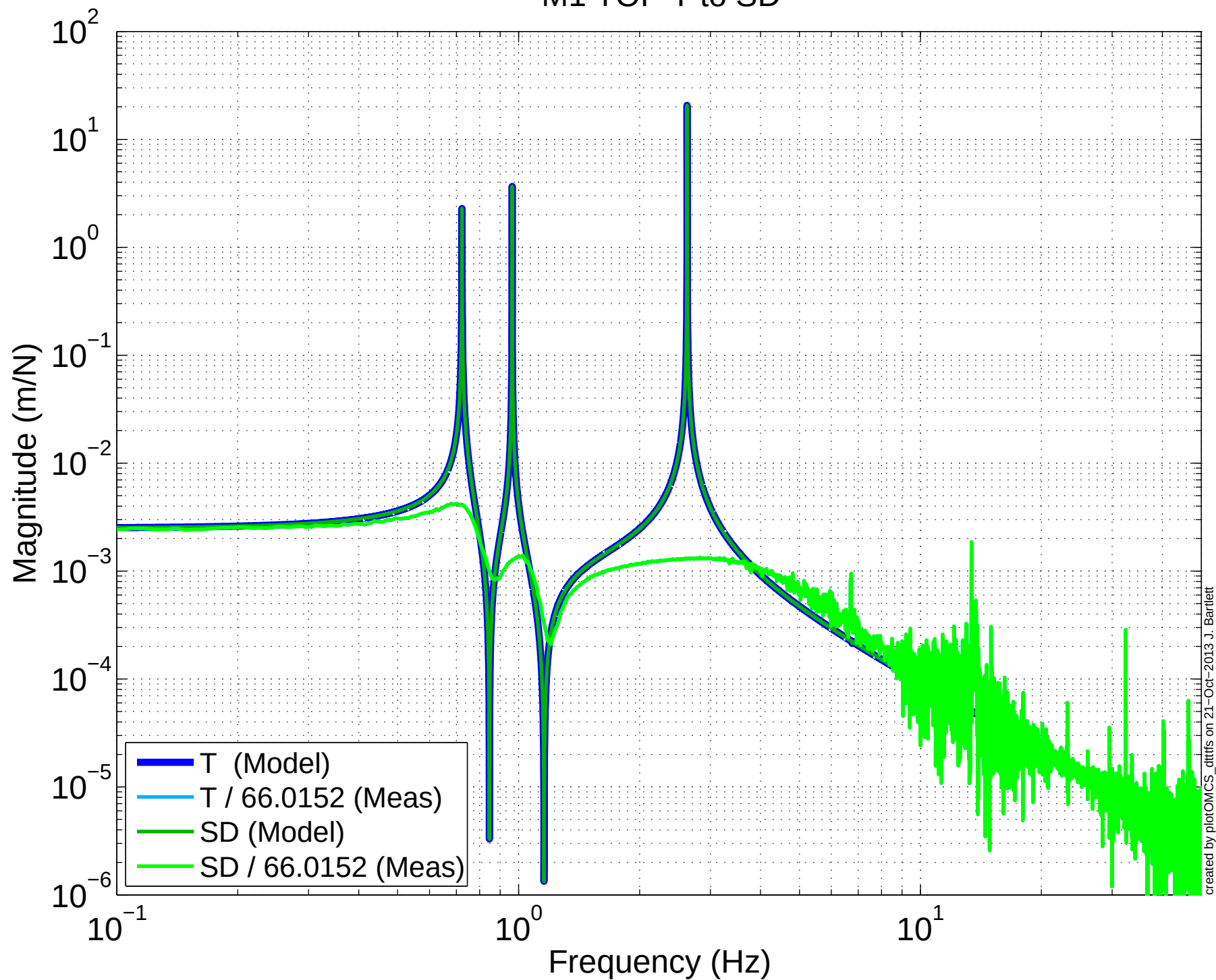
M1 TOP P to R



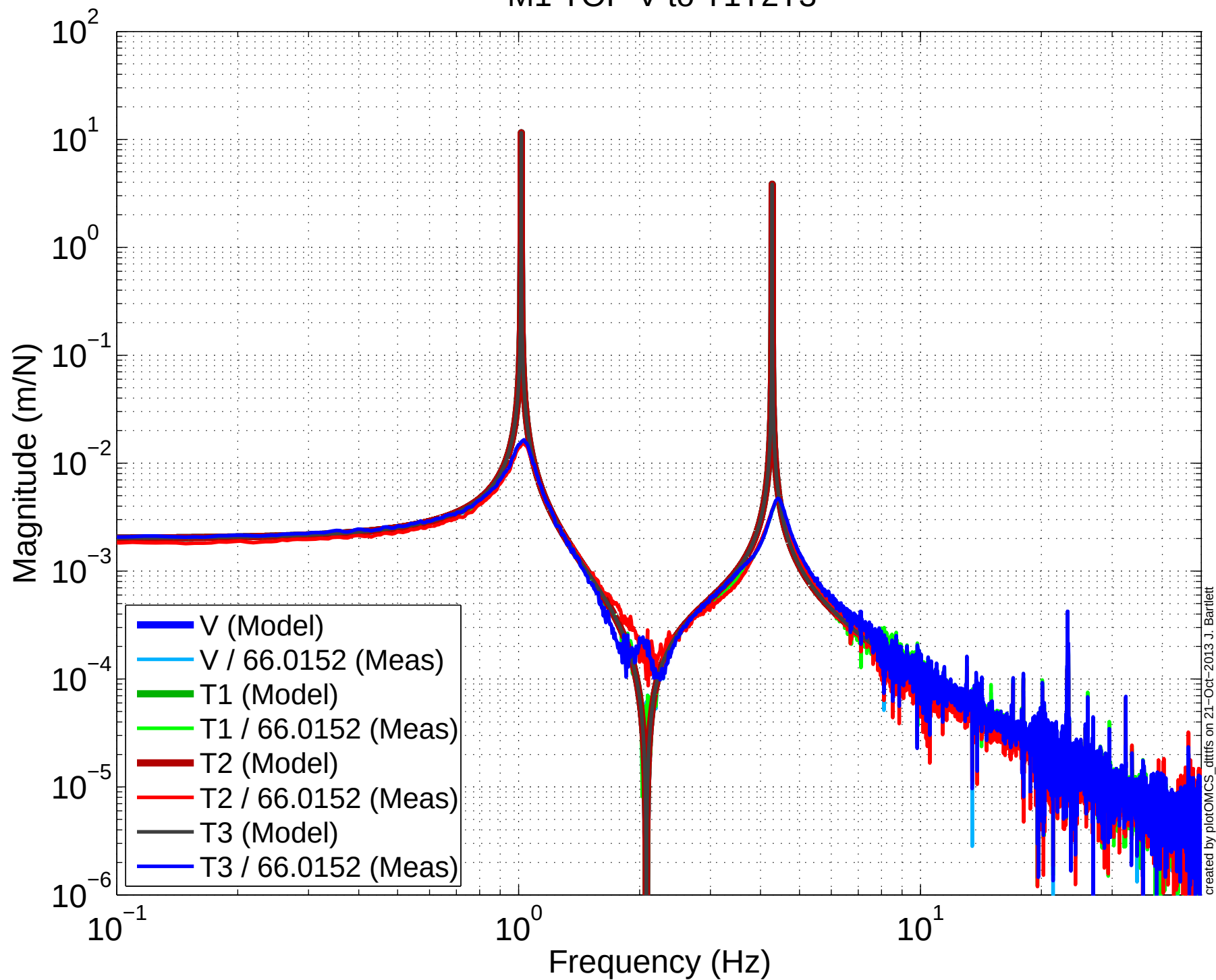
X1SUSOMC BUILD
M1 TOP L to LFRT



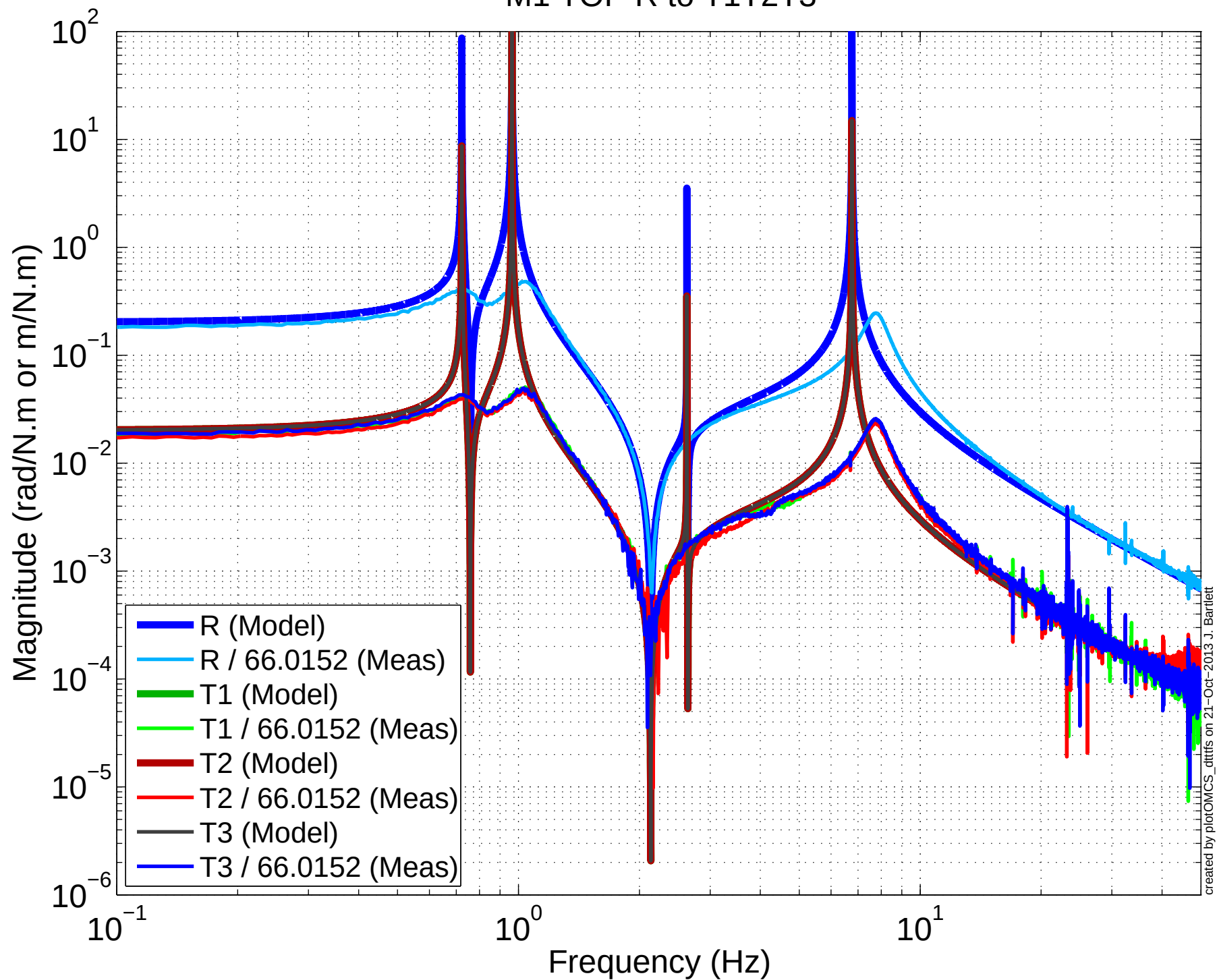
X1SUSOMC BUILD
M1 TOP T to SD



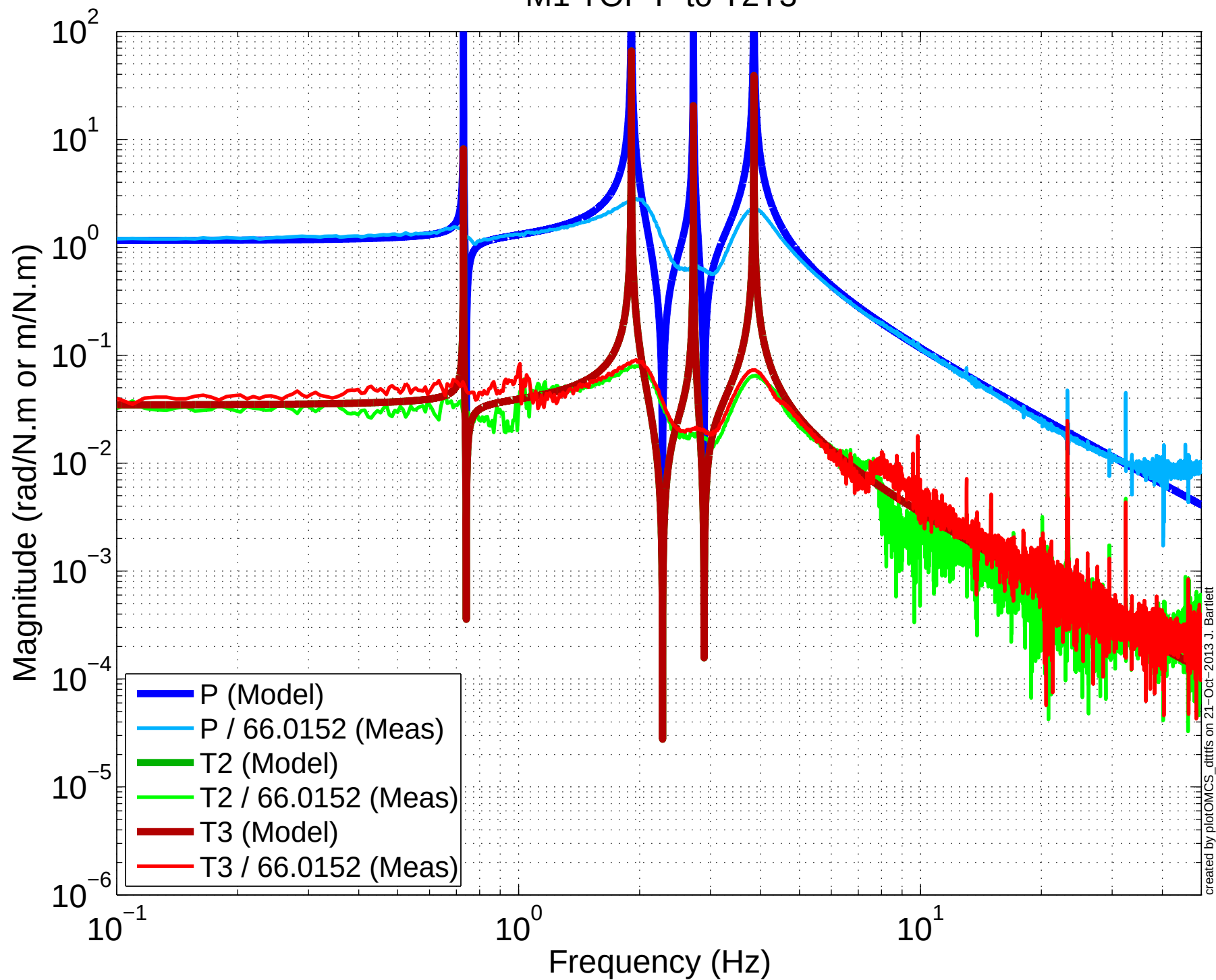
X1SUSOMC BUILD
M1 TOP V to T1T2T3



X1SUSOMC BUILD
M1 TOP R to T1T2T3



X1SUSOMC BUILD
M1 TOP P to T2T3



X1SUSOMC BUILD
M1 TOP Y to LFRT

