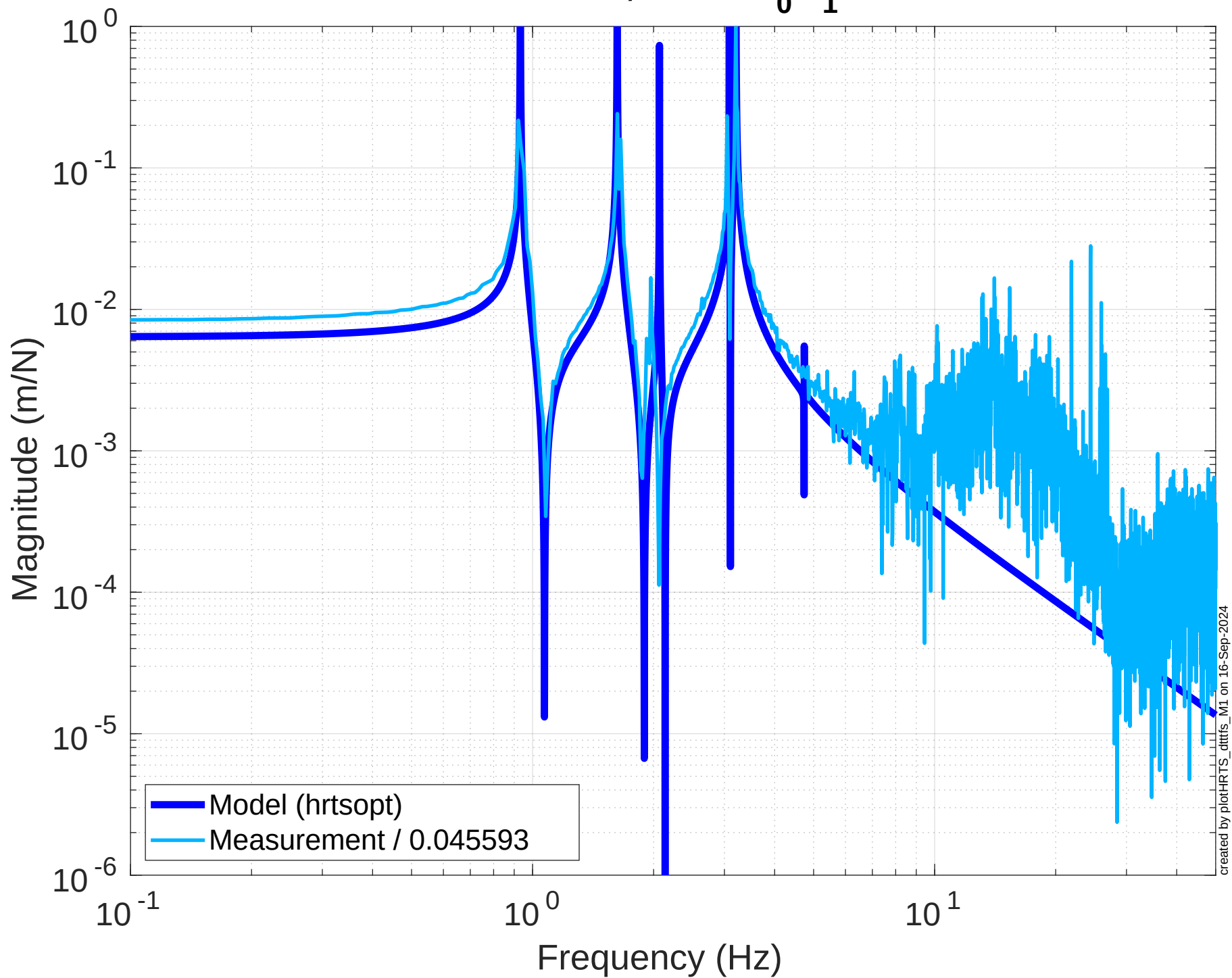
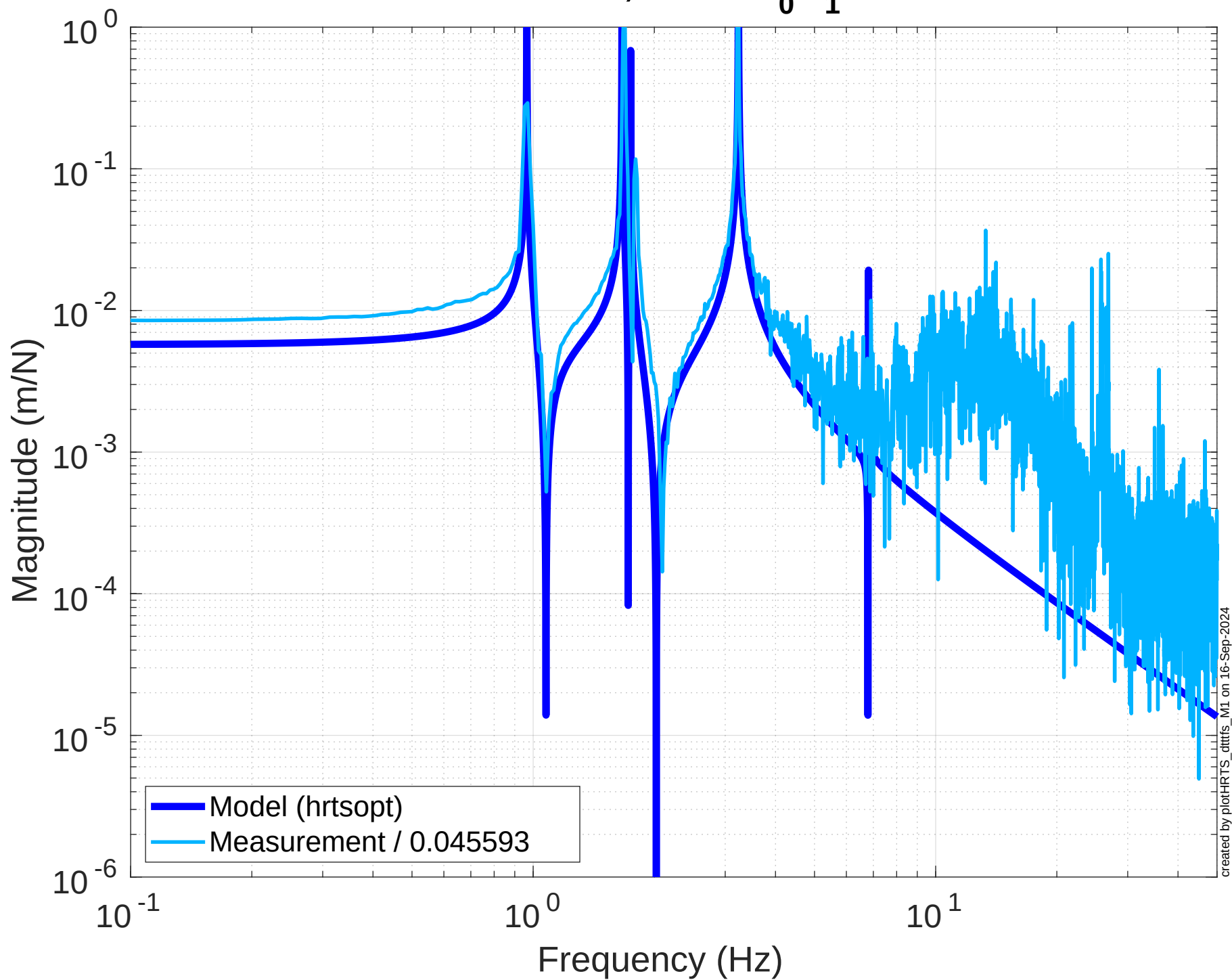


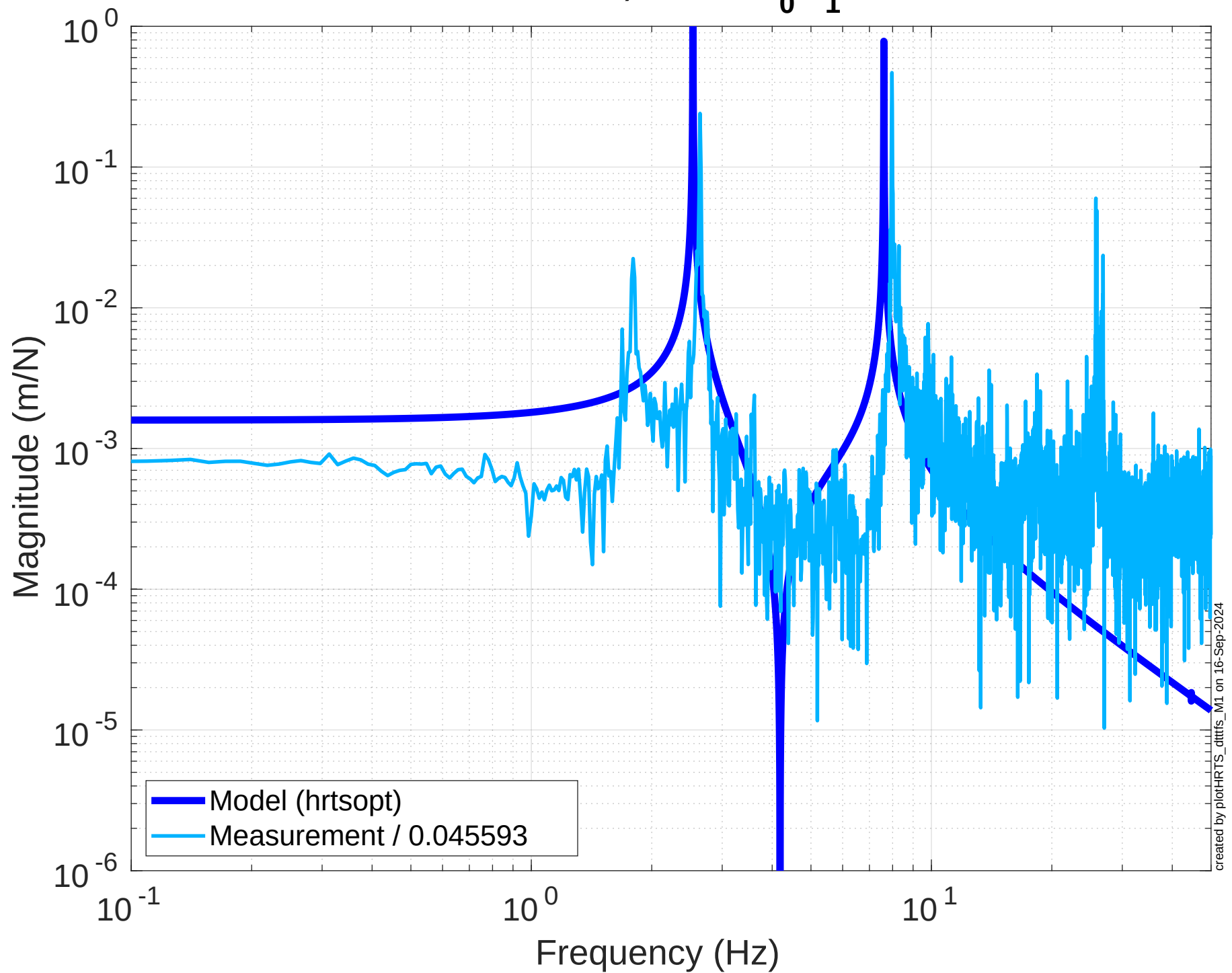
X1SUSLO1 BUILD
M1L to L, 2024 0916



X1SUSLO1 BUILD
M1T to T, 2024 0916



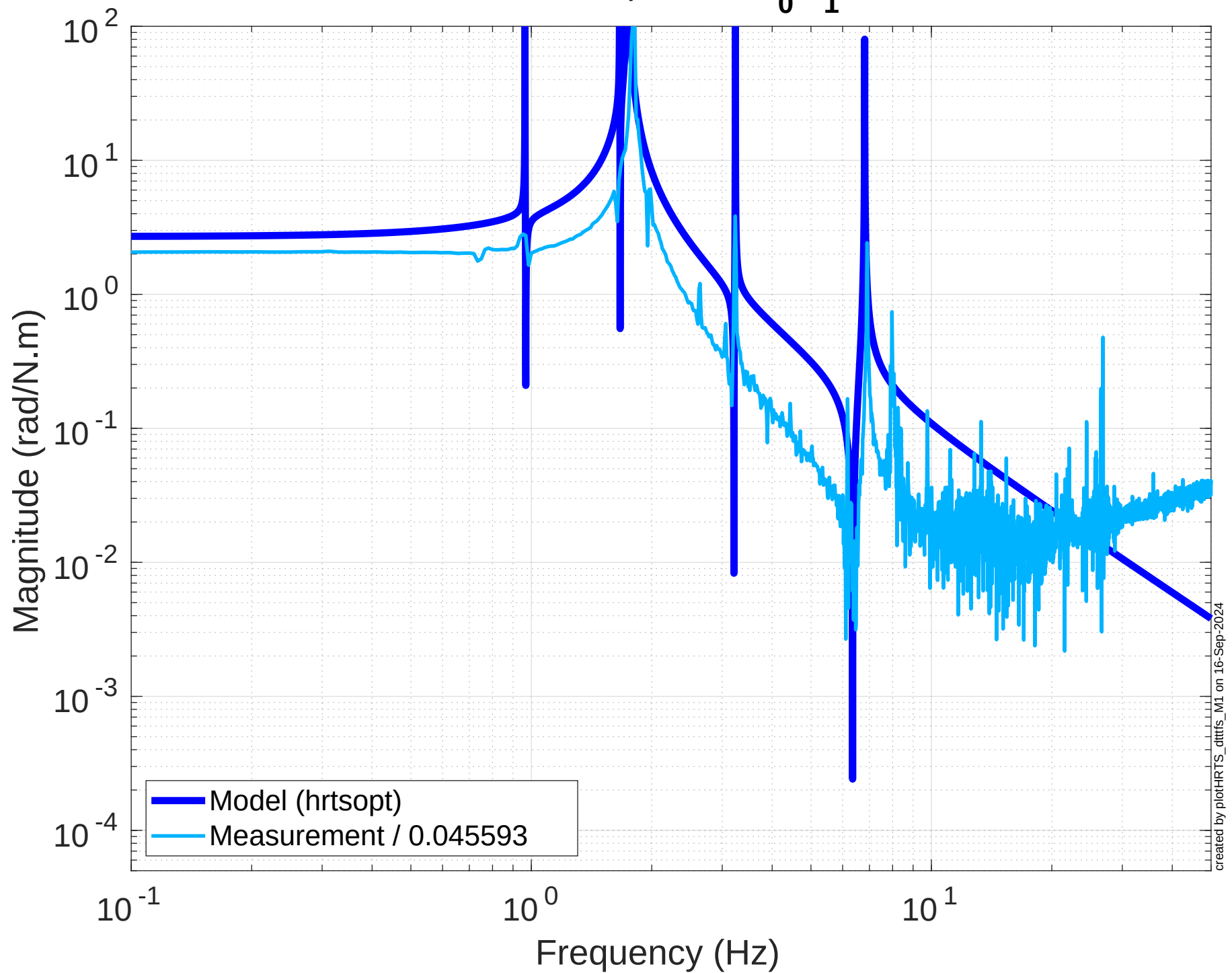
X1SUSLO1 BUILD
M1V to V, 2024 0^9_16



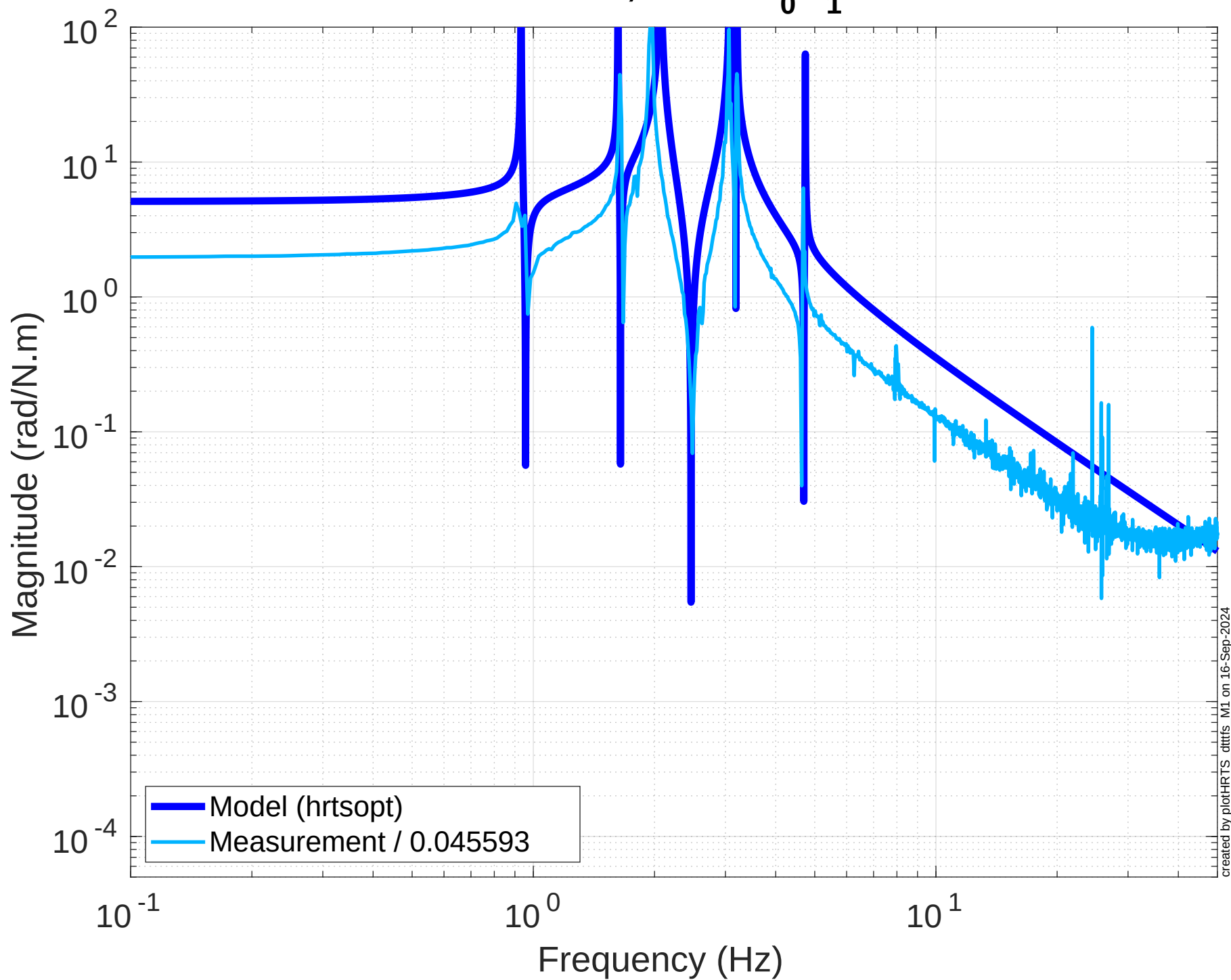
X1SUSLO1 BUILD

M1R to R, 2024

0.916



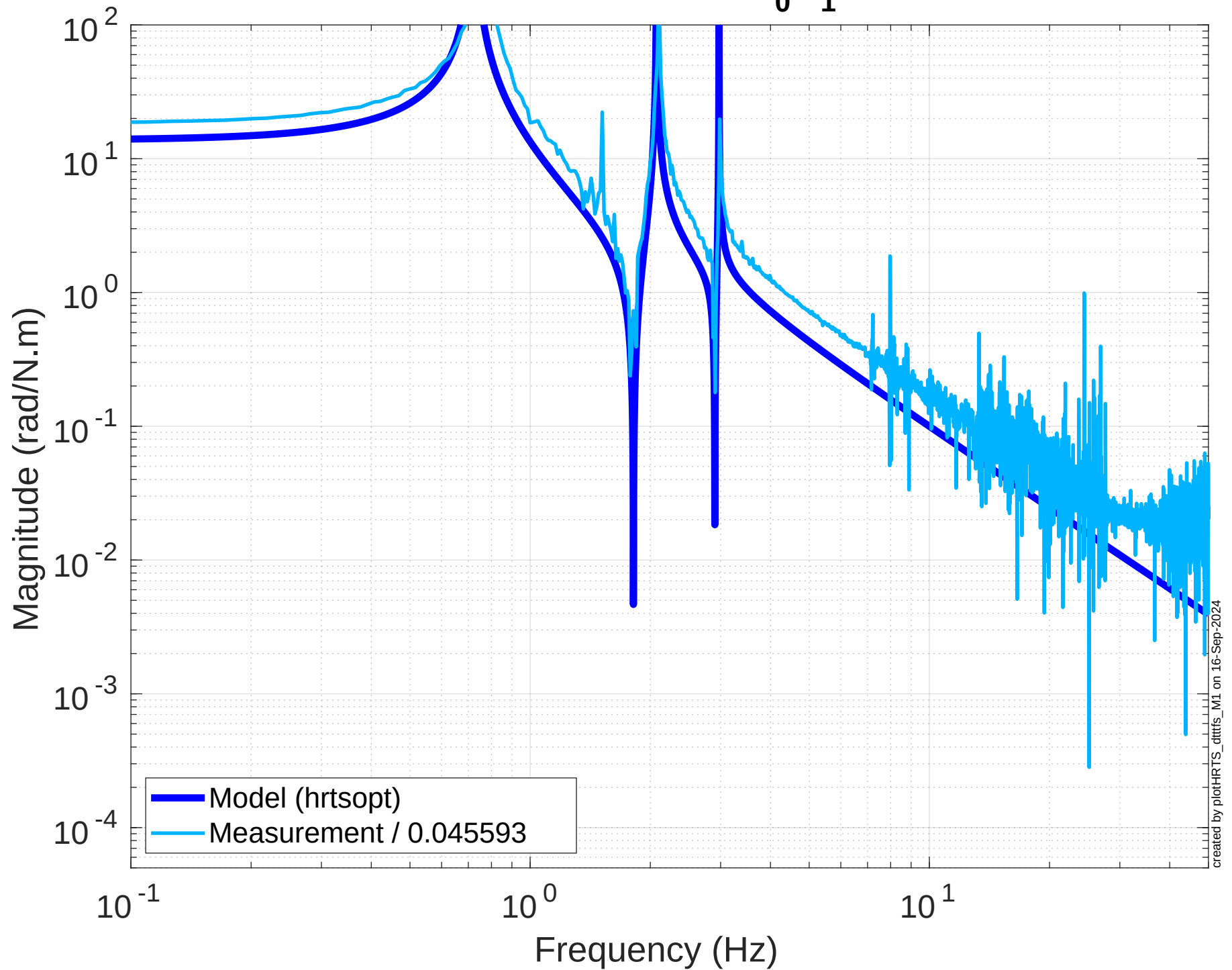
X1SUSLO1 BUILD
M1P to P, 2024 0916



X1SUSLO1 BUILD

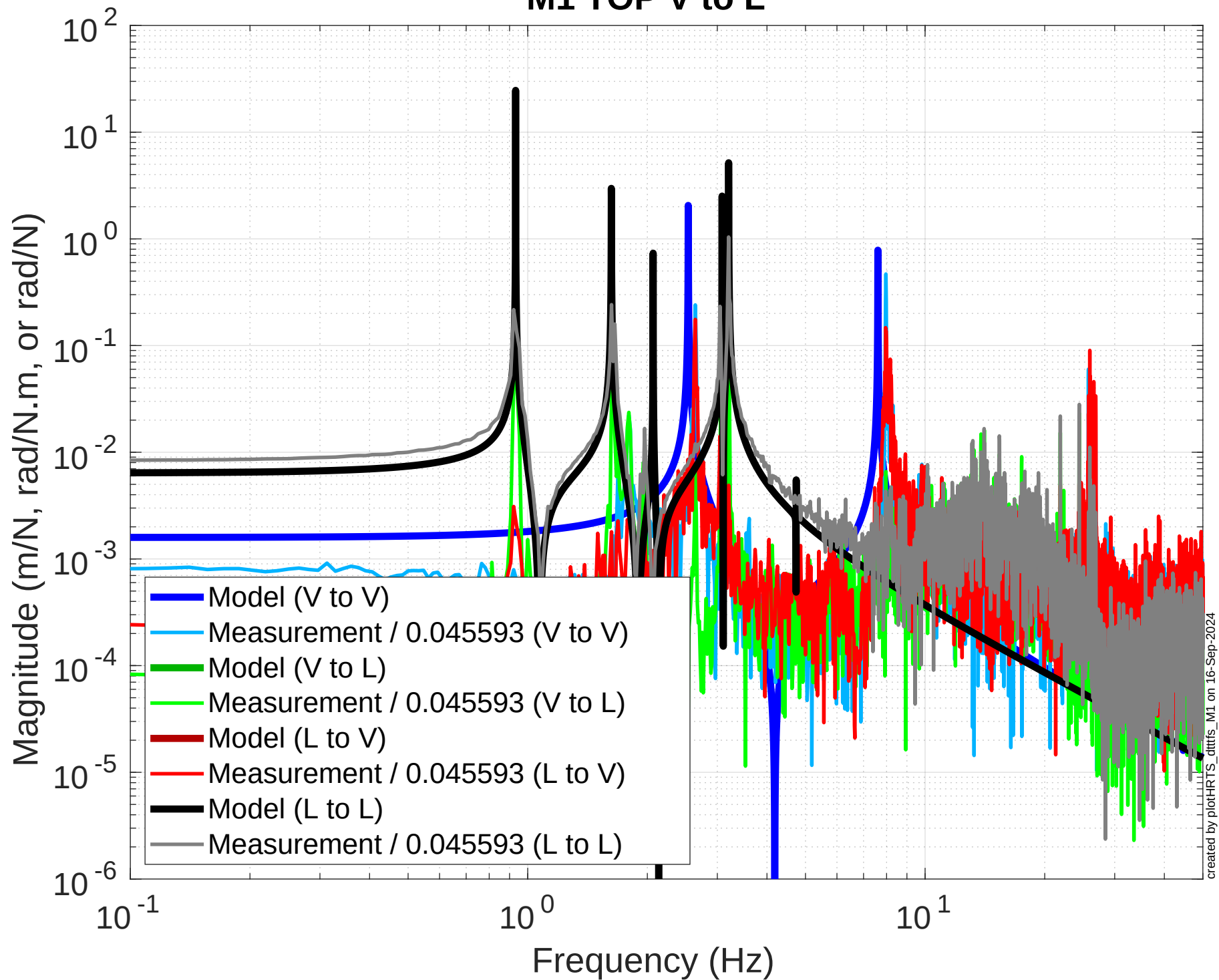
M1Y to Y, 2024

0^9_16



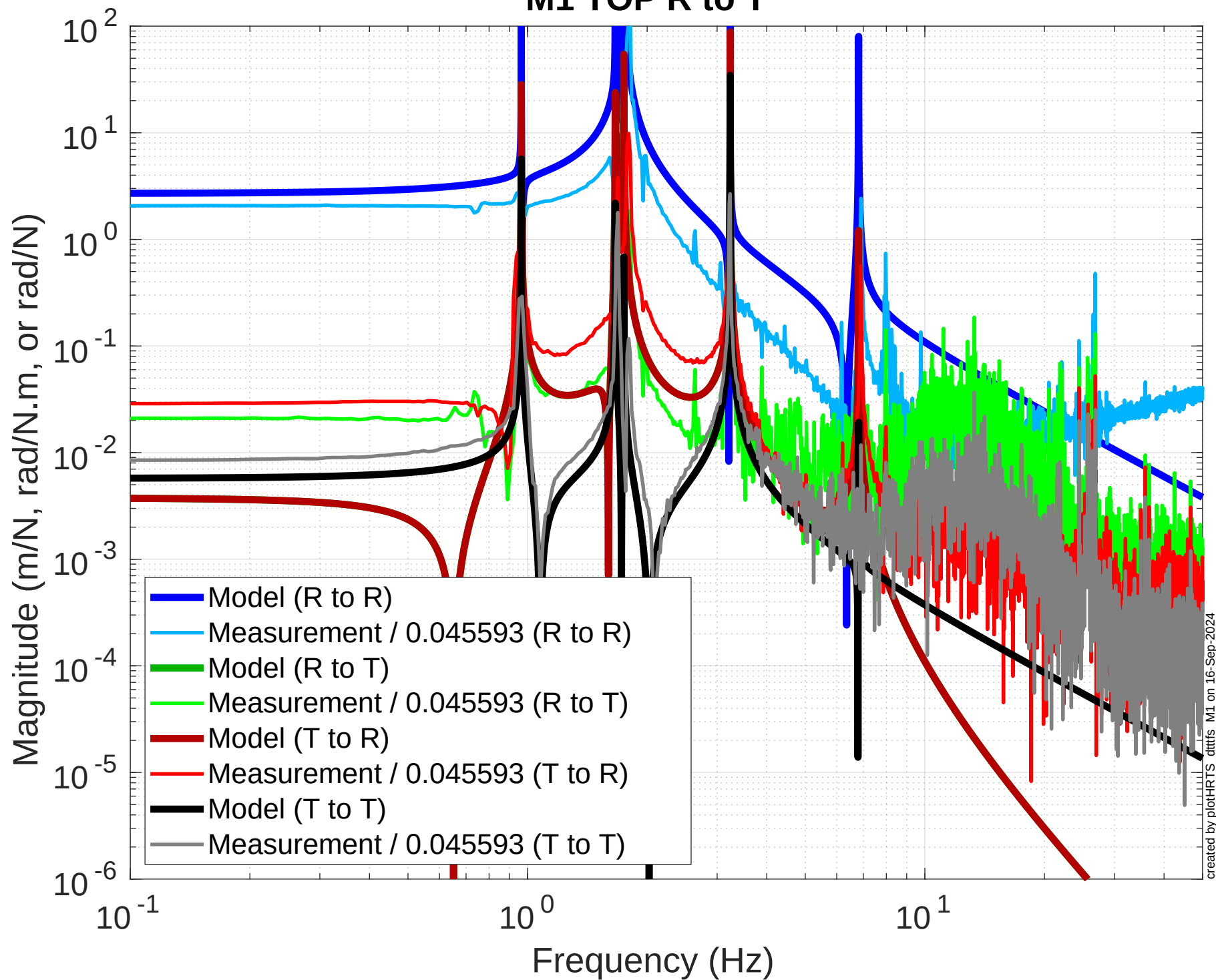
X1SUSLO1 BUILD

M1 TOP V to L



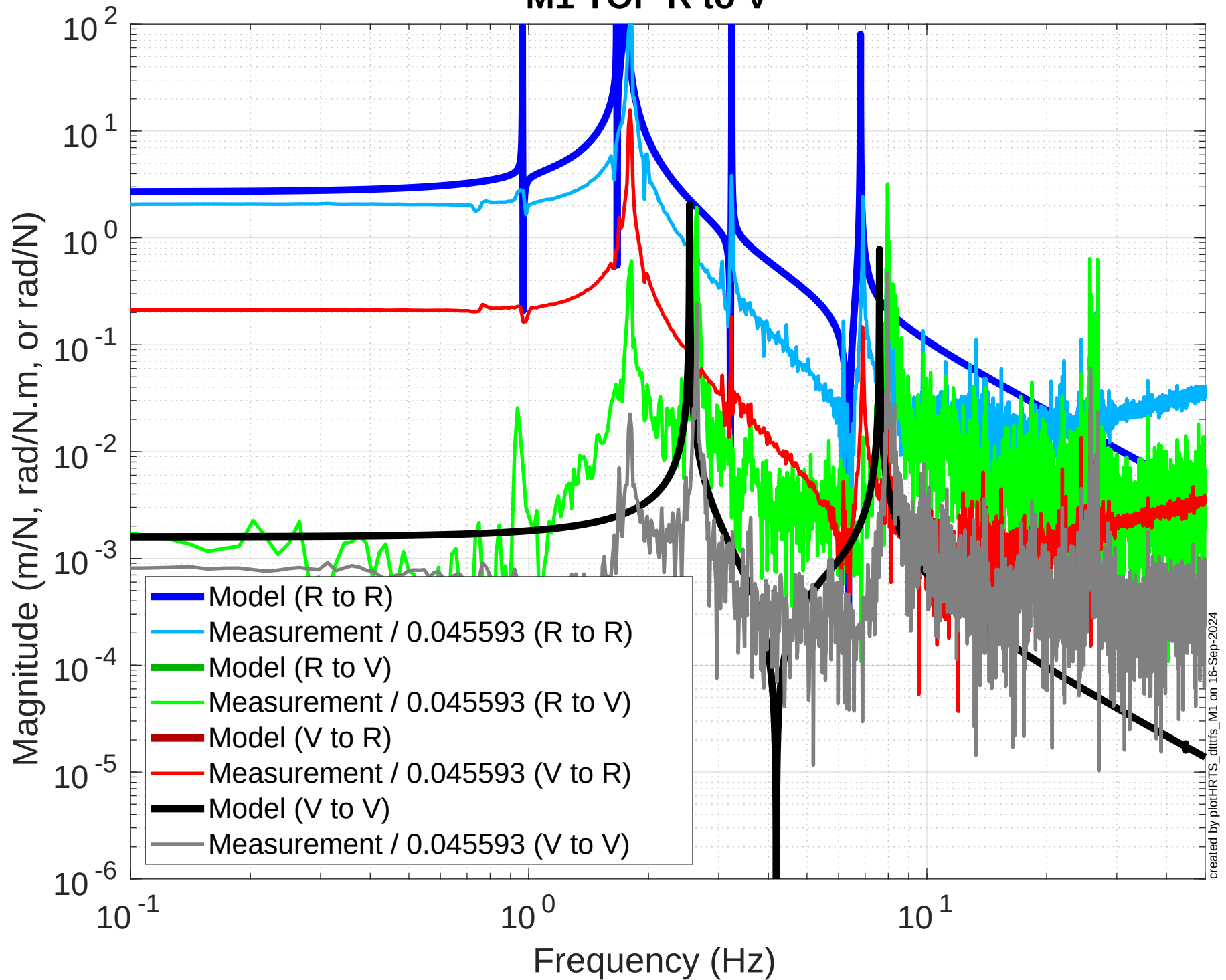
X1SUSLO1 BUILD

M1 TOP R to T



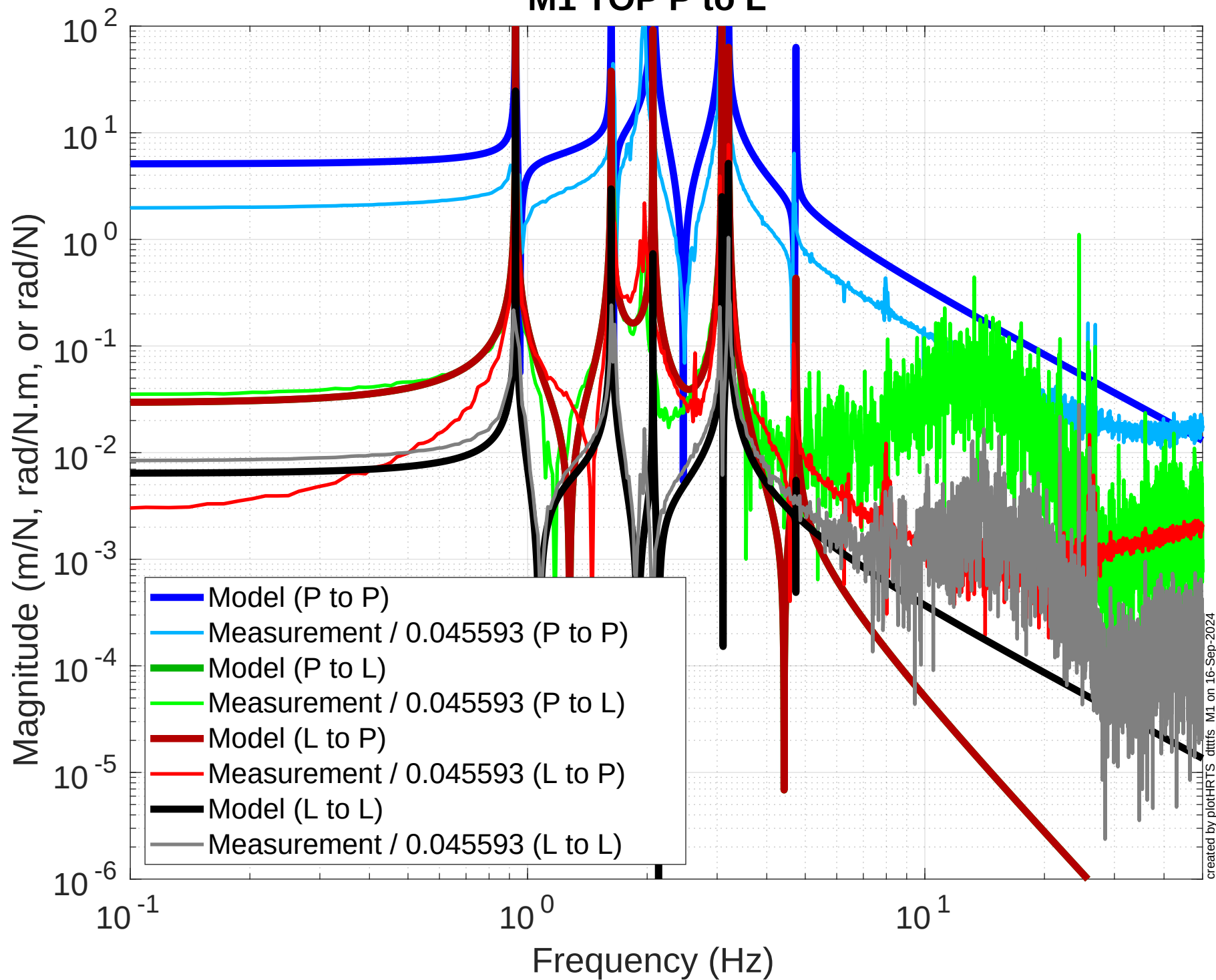
X1SUSLO1 BUILD

M1 TOP R to V



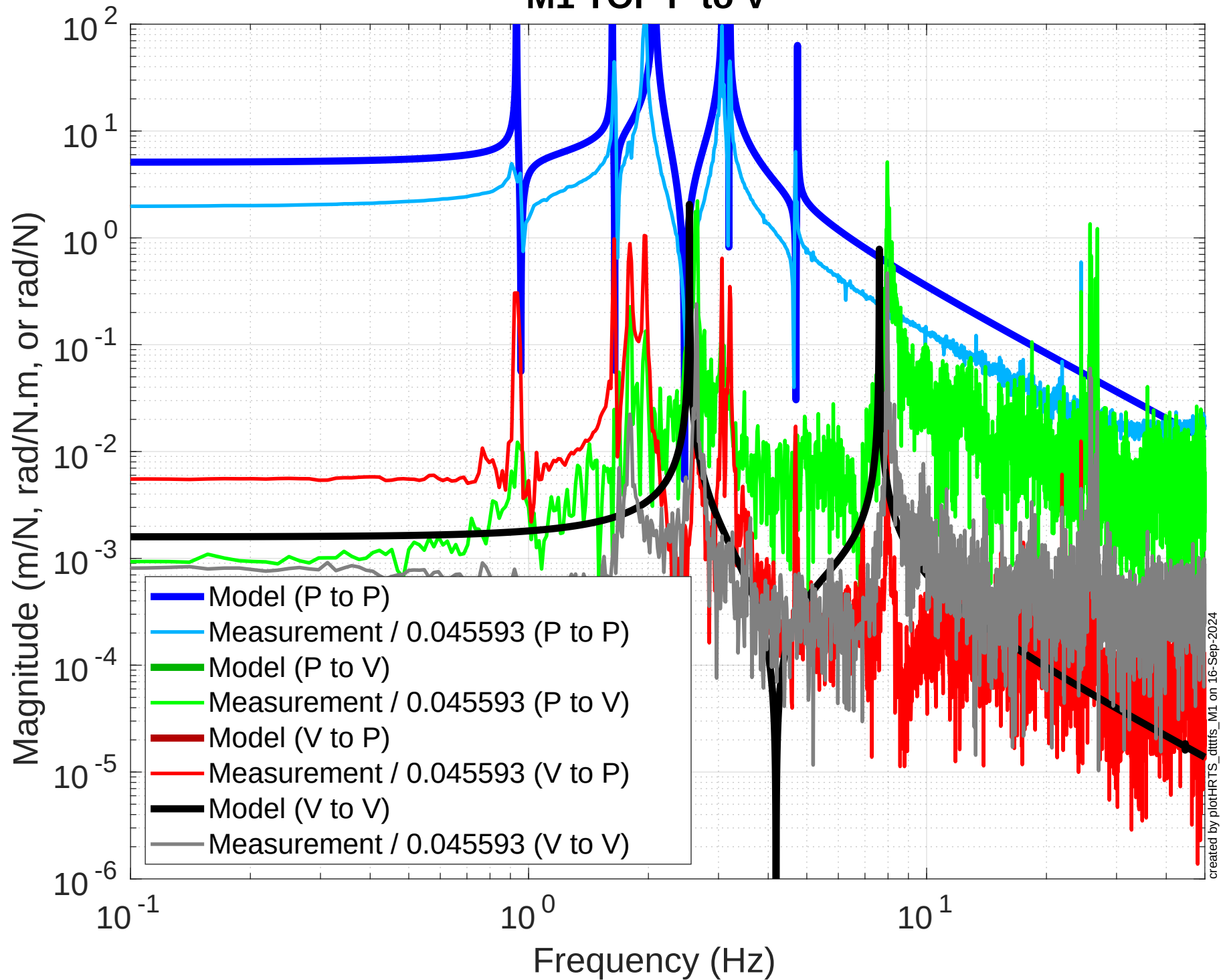
X1SUSLO1 BUILD

M1 TOP P to L



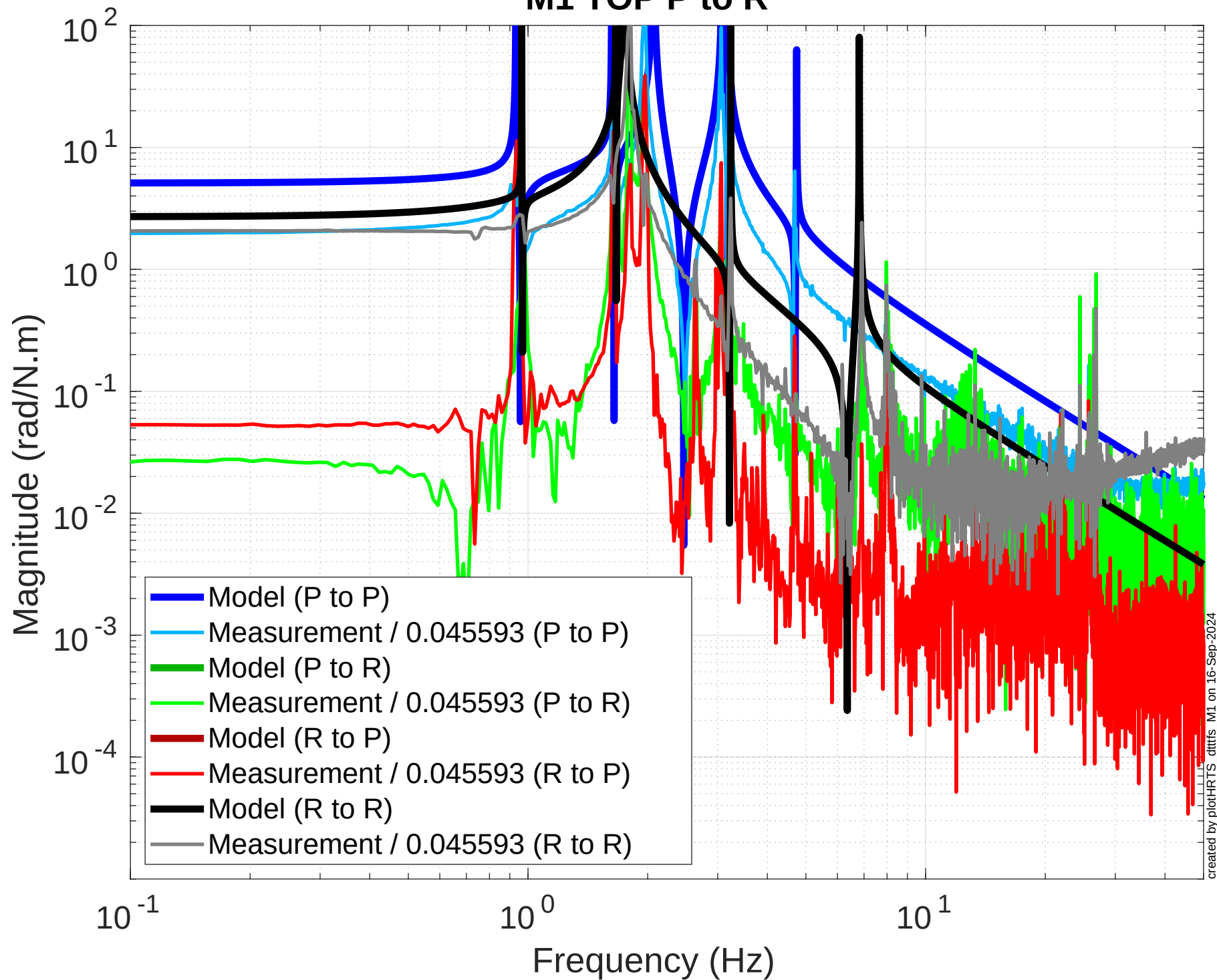
X1SUSLO1 BUILD

M1 TOP P to V



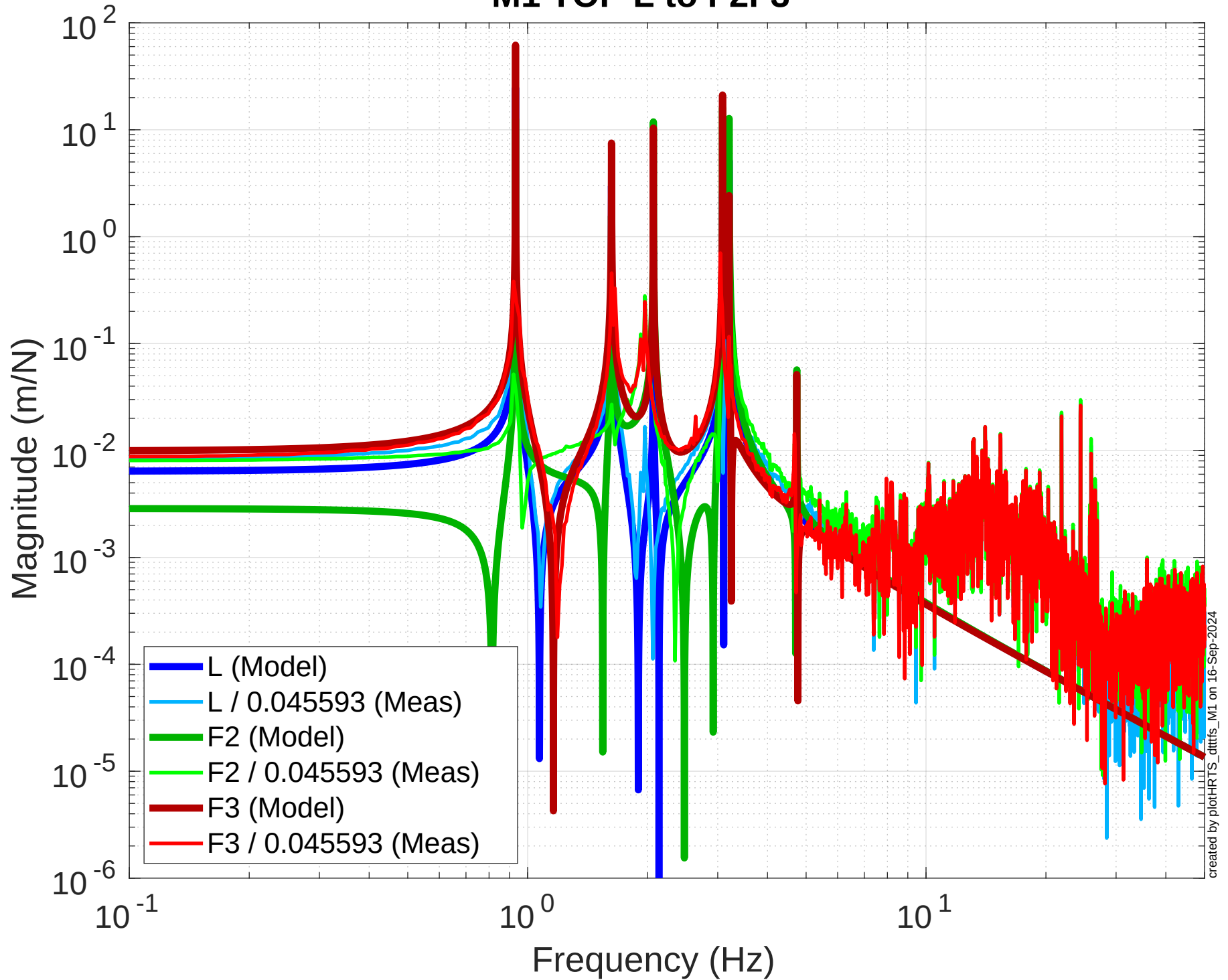
X1SUSLO1 BUILD

M1 TOP P to R



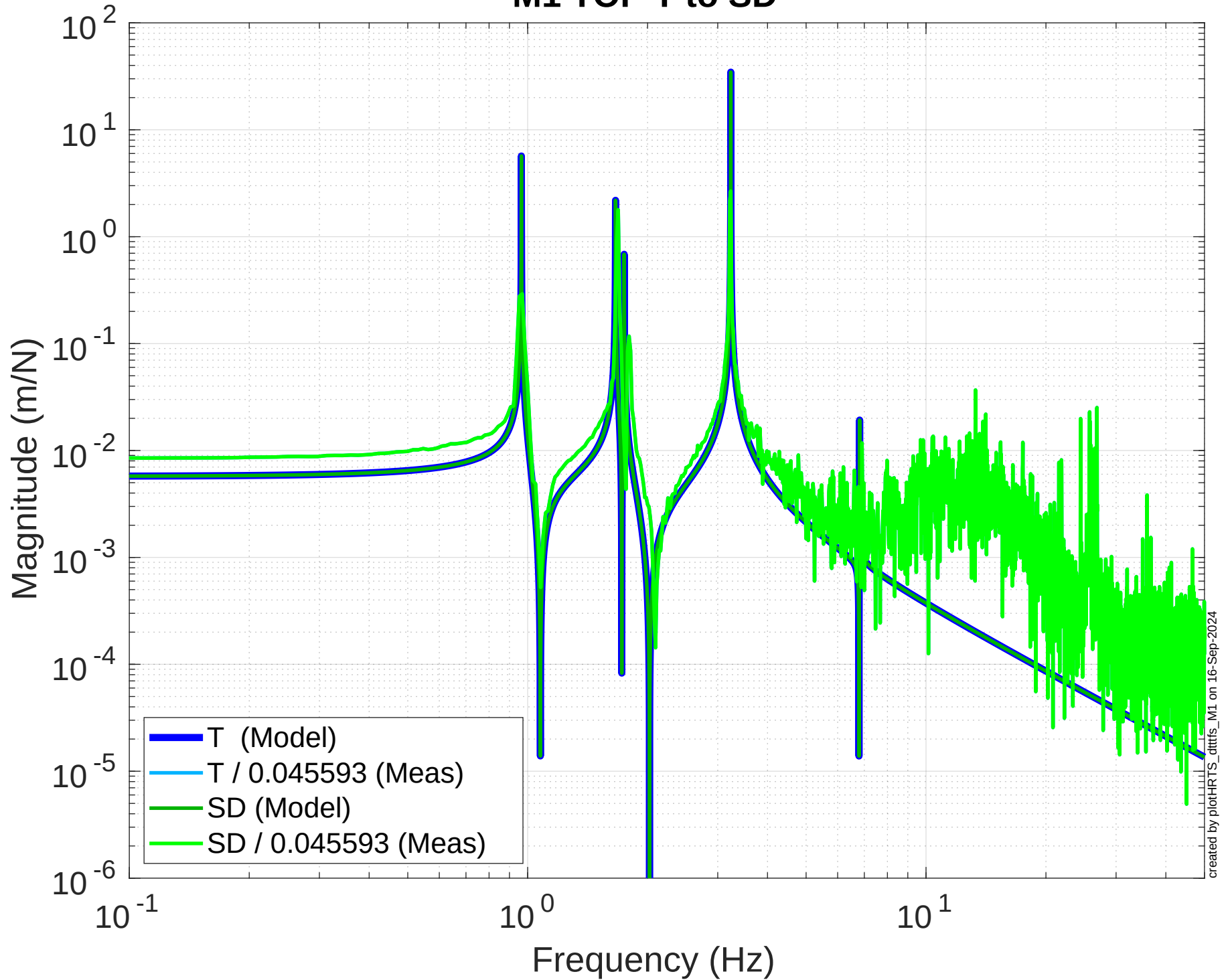
X1SUSLO1 BUILD

M1 TOP L to F2F3



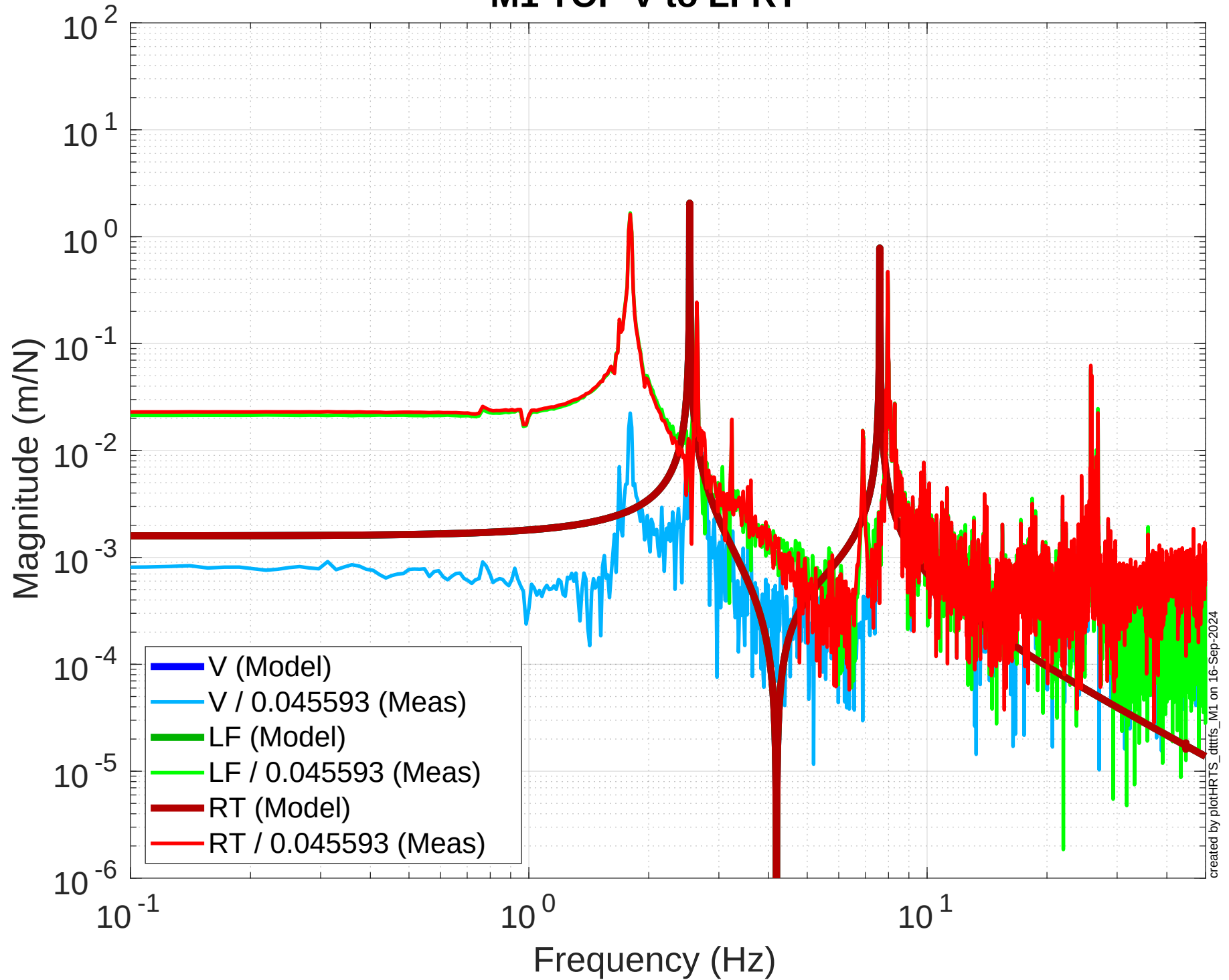
X1SUSLO1 BUILD

M1 TOP T to SD



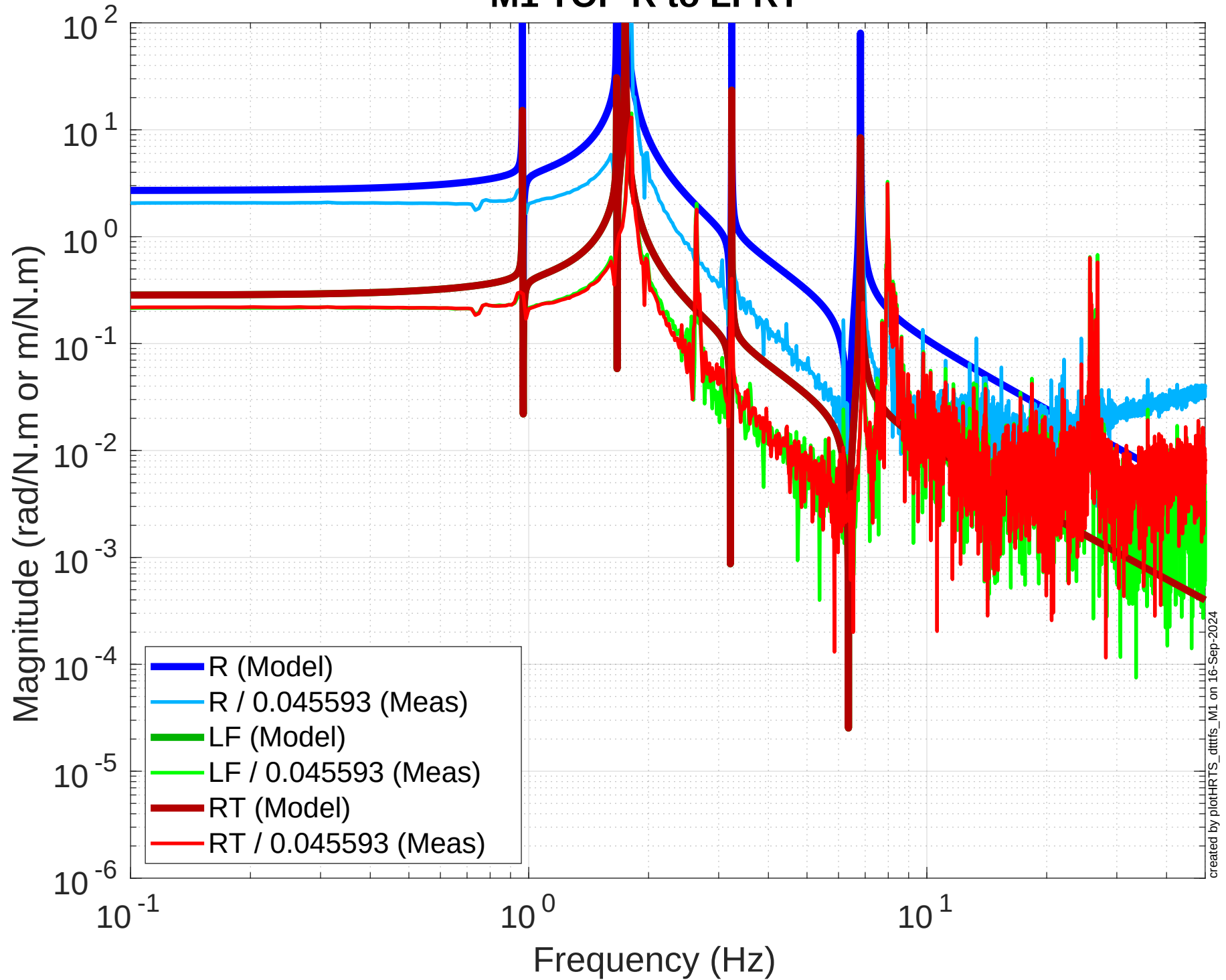
X1SUSLO1 BUILD

M1 TOP V to LFRT



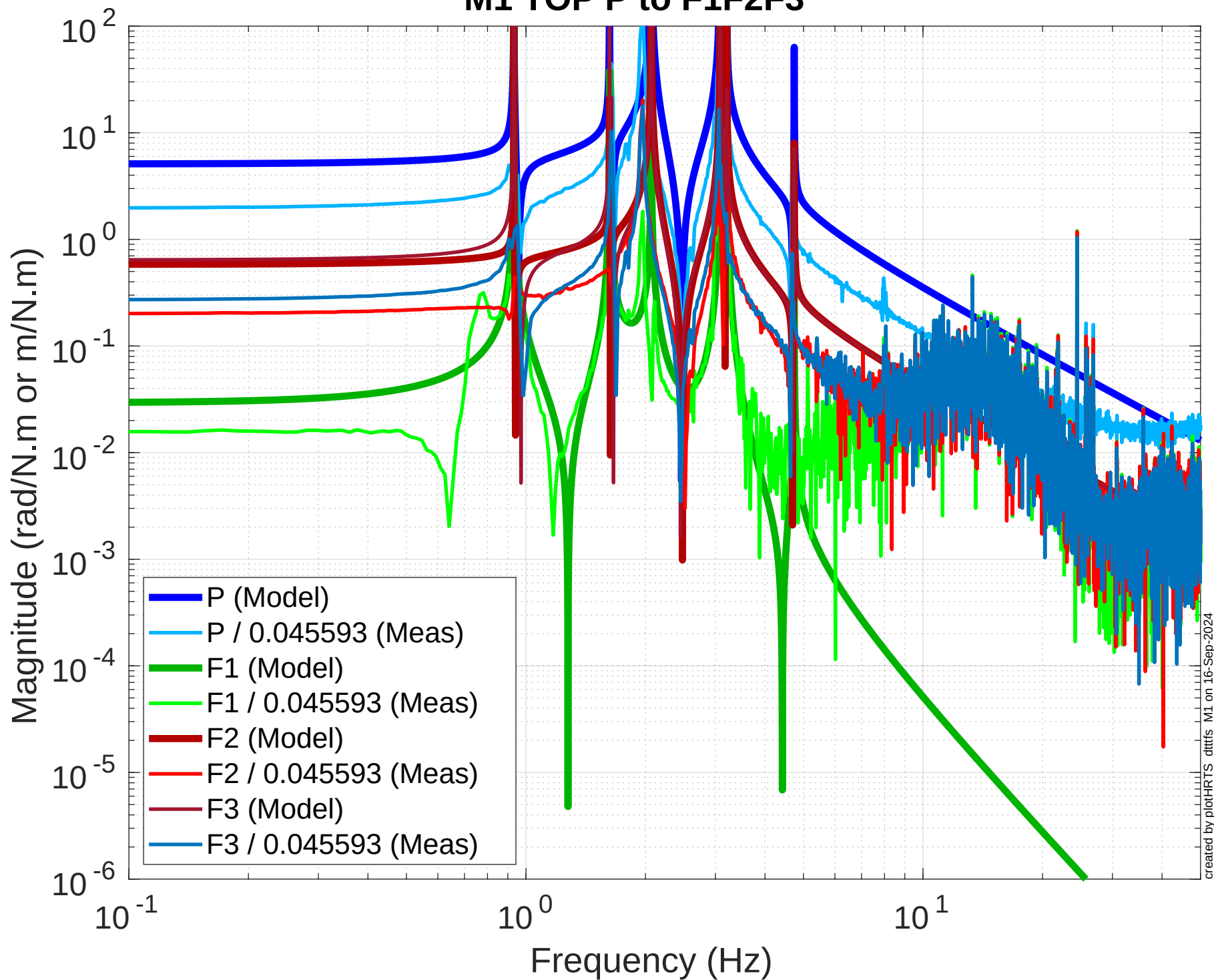
X1SUSLO1 BUILD

M1 TOP R to LFRT



X1SUSLO1 BUILD

M1 TOP P to F1F2F3



X1SUSLO1 BUILD

M1 TOP Y to F2F3

